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Ramload Information

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INTRODUCTION

THE ALTO

The alto is used to prepare and print documents containing text, diagrams, and images, to convey messages electronically, to aid in circuit and IC design, and to write programs. It is a minicomputer consisting of a processor, disk drive, workstation, and Ethernet transceiver. Optionally, it may be ordered with a Diablo Hytype printer and/or a second disk drive. The Alto can also be used to drive other devices ranging from communications interfaces to production machines.

There isn't much special-purpose hardware in the Alto. Most of what you read about in the hardware manual is in fact implemented by microcode. This gives us considerable flexibility in the way we design software interfaces for experimental devices and specialized instructions.

A standard Alto system includes:

An 875-line television monitor, oriented with the long tube dimension vertical. This monitor provides a 606 by 808 point display which is refreshed from main menory at 60 fields (30 frames) per second. One bit in memory shows up as one bit on the screen. Since the screen is 606 by 808 points, a quick calculation shows that a full-screen display requires nearly 32K of the Alto memory. For a machine with only 64K of memory minimum, that seens a big price to pay. The theory is that in exchange for the space we get enormous freedom to experiment with various strange ways of manipulating the screen. It has programmable polarity, and a cursor, whose position is controlled by the mouse, and is a 16 by 16 bit-map whose shape is under program control, independent of display content.

- An encoded keyboard.
- A mouse (pointing device).
- A Diablo Model 31 disk drive. The Model 31 accepts a single disk which can be used to store about 2.5 megabytes. The average seek time is 70 ms, the average transfer rate, 1.22 MHZ. A second drive can be added.
- An interface to the Ethernet, a 3mbps serial communication line. An Ethernet transceiver connects the Alto to the Ethernet. Using the Ethernet, an Alto can communicate with a large number of other Altos, computers special purpose servers.
- The microcoded processor has 64K of 850ns, 16-bit word semiconductor menory (expandable up to 256K in 64K banks). A ⋅ X microinstruction Ram which can be loaded with special purpose microcode to extend the instruction set, perform special functions or drive special I/O devices.
- The processor, disk, and their power supplies are packaged in an under-table-size cabinet for easy placement in the user's office. The other I/O devices may be a few feet away, and are packaged for desk top use.

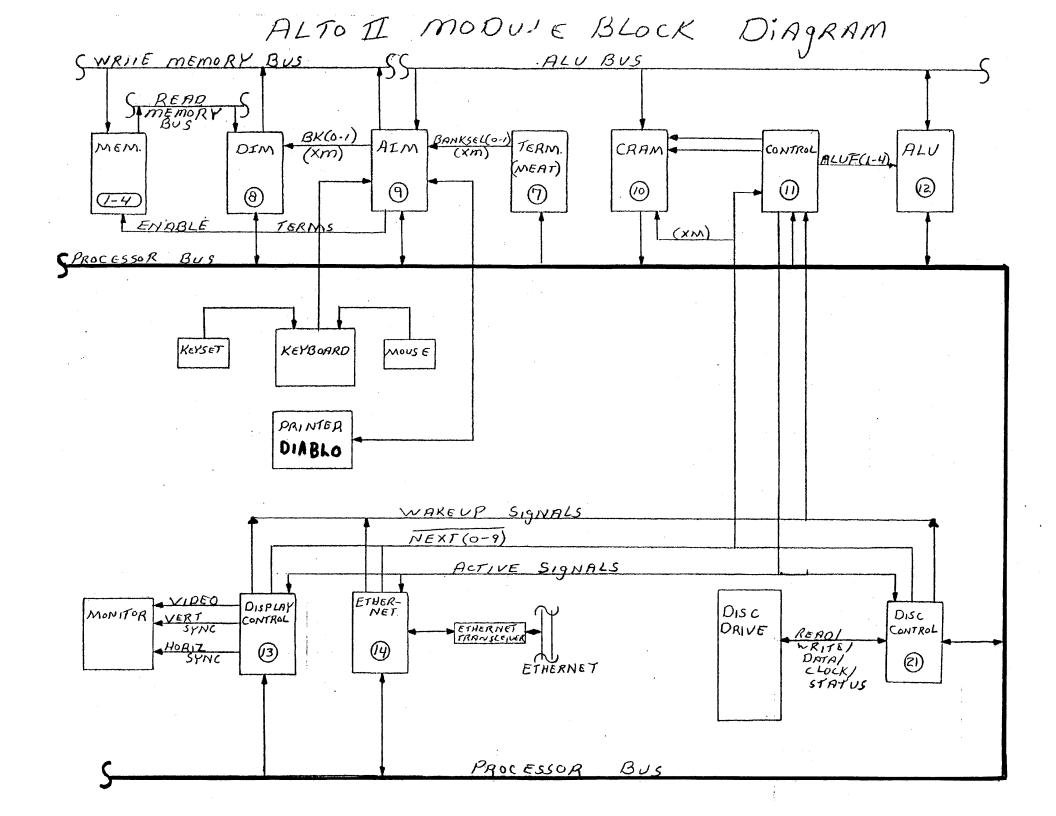
THE NETWORK

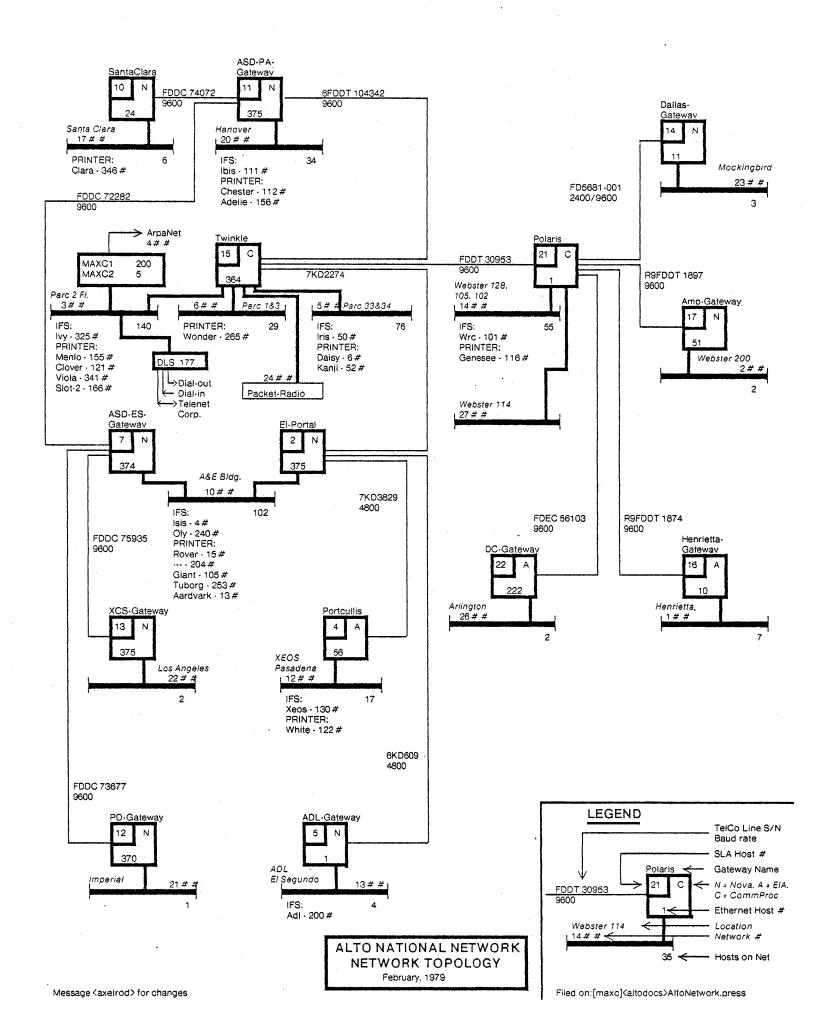
The network is composed of Altos and other computers connected to several geographically disperesed, technologically innovative local computing nets which, in turn, are tied together by minicomputers over standard leased and/or dial-up telephone lines. The local computing nets are called Ethernets; the min-computers linking them are referred to as Gateways, the latter providing several services in addition to linking together Ethernets.

Question: Why have a network? Answer: Because it's nice to be able to pawn off some of the work on other machines, leaving your Alto free. That's why we have a number of machines generically called servers. Normally, server Altos have special purpose, expensive hardware attached to them (e.g. large-capacity disks, printers), and their sole purpose in life is to make that hardware available to more than one person/Alto. We tend to identify servers by function, so we talk about printing servers, file srvers, name lookup servers, mailbox servers, and so on. many of the protocls for use on the Ethernet were developed precisely so that personal Altos could communicate effectively with server Altos.

We all know how uncommunicative computers can be, and Altos are no different. That's why we invent careful protocols for them to use in talking to each other. Most of the protocols now in use on the Ethernet are called Pup-based (Parc Universal Packets). You will probably hear some of the following protocol names being tossed about in conversation:

- (0) EEFTP- a grand-daddy of file transfer protocols (Experimental Ether FTP). No longer in active use.
- (1) EFTP- stands variously for Early FTP, Ears FTP, Experimental FTP. A venerable protocol now mostly used to transfer files to printing servers. The Alto program Empress uses it for this purpose.
- (2) FTP- refers to File Transfer Protocol, as well as the Alto that implements it and provides an interactive user interface. If you come from the Arpanet world, Don't confuse this FTP with the one out there ours is Pup-based and incompatible. On MAXC, where both the Pup and Arpa FTP protocols come in handy, the name FTP refers to the Arpa one and PupFTP (obviously) refers to the Ethernet one.
- (3) BSP- the Byte Steram Protocol. Built on top of Pup, this protocol is used by conversants who want to view the network as a full-duplex stream of 8-bit bytes. BSP is used to implement FTP.
- (4) MTP- the Mail Transfer Protocol. Used by Laurel (the Alto-based message system) to ship messages to and from mailbox servers.





BOOTING INFORMATION

The process of "booting" the Alto is one of setting some or all of the Alto's state either by reading a file from the disk or by accepting packets from the Ethernet.

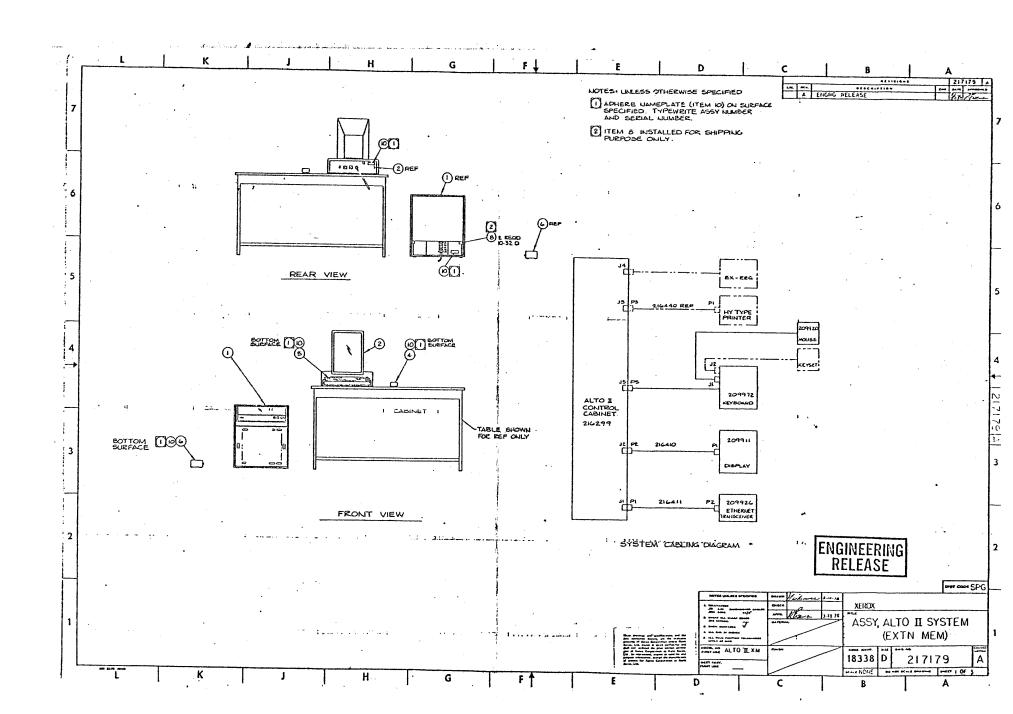
There are four basic steps in "booting" the Alto: (1) the tasks in the microprocessor are reset; (2) a 256-word "boot loader" is loaded into main memory and started; (3) the boot loader loads a portion of Alto main memory from a "boot file" and finishes by transferring to a known place; (4) the user's program loaded by the third step can restore even more of the Alto's state.

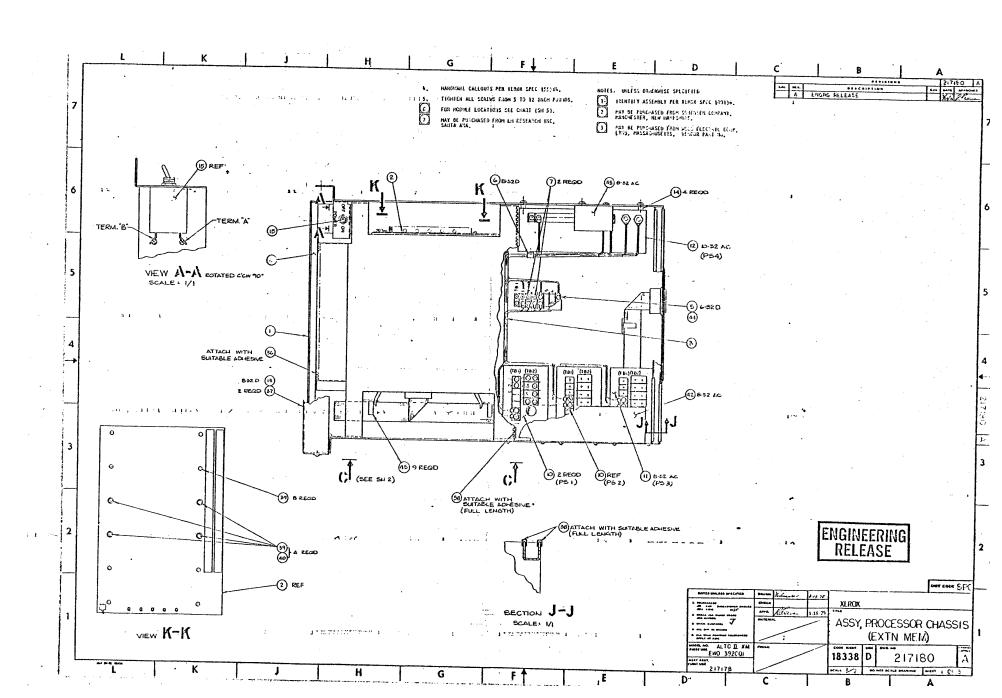
Booting

"Booting" is accomplished either by pushing the "boot button" located on the rear of the keyboard or by the Reset Mode Register, the emulator task is started in a standard boot program. This program reads location #177034, a word whose contents can be altered by pushing various keys on the keyboard. If the \lambda book key is depressed during booting, the machine state will be restored from the Ethernet; otherwise, the state is restored from the disk.

When booting from the disk, the keyboard word is interpreted as a disk address where a "disk boot loader" is located. If no keys are depressed, disk address 0 is generated, which is normal resting place of the "disk boot loader" for the operating system. The emulator reads a single 256-word disk record into memory locations 1, 2, ...#400: the 8-word disk label for this page is placed in #402, #403, ...#411. When the disk transfer is complete, control is transferred to location 1 in the loader. The boot loader uses the saved label to point to the remainder of a "boot file" which is read into main memory and started.

When booting from the Ethernet, the microcode waits until a "breath of life" packet arrives, containing a 256-word "ethernet boot loader" which is read into locations 1-#400 and executed by transferring to location 3. It is up to this loader to establish communications with a party willing to deliver the remainder of the state needed.





ALTO II MODULE LOCATION CHART

		ASSEMBLY/I	DRAWING NUMBER
SLOT	MODULE NAME	NORMAL ALTO II	EXTENDED MEMORY ALTO II
1 2 3 4 5 6	Memory Storage Memory Storage Memory Storage Memory Storage Bus Terminator	216273 216273 216273 216273 216421	217187 (64K), 216644 (128K) or 217188, 217189 (256K) 217187 (64K), 216644 (128K) or 217188, 217189 (256K) 217187 (64K), 216644 (128K) or 217188, 217189 (256K) 217187 (64K), 216644 (128K) or 217188, 217189 (256K)
8 9 10 11 12 13 14 15 16 17 18 19 20	Memory Data Interface Memory Address Interface Control RAM 2K Control Board Arithmetic Logic Unit Display Control Ethernet	216312 216347 216365 216484 216381 216339 216323	217115 or 217174 216645 or 217173 216643 or 217176 216642 or 217175 216381 216339 216323
21	Disc Control	216389	216389

+15, -15, +12 VOLT POWER SUPPLY INFORMATION

GENERAL

SSD units are equipped with two controls; the output voltage, and the overvoltage adjustment (OVP) potentiometers accessible at the terminal-board (TB1) end of the supply. The output VOLT control (R26) varies the output voltage while the OVP control (R24) sets the OVP trip point. Both are facroty-set to nominal values.

OVERVOLTAGE (OVP) TRIP POINT

The OVP trip point is factory-set to 1.2 volts or 10% (whichever is greater) higher than the rated nominal output voltage.

OVP TRIP POINT ADJUSTMENT

To adjust the OVP, proceed as follows:

- 1. Rotate OVP panel control R24 fully CW.
- 2. Rotate output VOLT control R26 on panel until the output voltage is egual to the desired trip point.
- 3. Rotate OVP control R24 slowly CCW until the output voltage suddenly drops to zero. This indicated that the OVP circuit has been triggered.
- 4. Remove the input power. Rotate VOLT control several turns CCW. Allow approximately 10 seconds for unit to discharge.
- 5. Apply input power. Reset output to desired operating voltage.

NOTE

The OVP circuit inckudes a time-delay network such that the overvoltage condition must exist for approximately 100 microseconds before the OVP fires. This delay prevents short-duration overvoltage pulses from triggering the circuit.

RESET AFTER OVP FIRES

If the OVP fires, proceed to reset the circuit as follows:

- 1. Remove input power and disconnect load (in case overvoltage condition is externally induced). Allow approximately 10 seconds for unit to discharge.
- 2. Rotate VOLT adjustment R26 fully CCW (minimum voltage).
- 3. Apply input power and raise output voltage to desired value.

NOTE:

If the OVP trips again, internal failure is indicated, or the output is set too close to operating voltage.

		Revisions	i	217113	3 <u>A</u>	
LAL	Rev.	Description	Chk.	Date	\pproved -	
	A	ENGRG RELEASE		1/13/78 A	Treeman	,

NOTES: UNLESS OTHERWISE SPECIFIED

1. REFERENCE INTERPAK ELECTRONICS, 13536 SATICOY STREET, VAN NUYS, CALIFORNIA, WIRE LIST NO. ALTO II XM - REV A.

THIS DOCUMENT CONTAINS 20 SHEETS

Dist. Code SPG

These drawings and specifications, and	Notes Unless Specified	Drawn Check	Whiming _	1-12-78	Xero El S	x Corporation egundo, California	XER	OX
the data contained therein, are the exclusive property of Xerox Corporation and/or Rank Xerox, Ltd. issued in strict confidence and shall not, without the prior written permission of Xerox Corporation or Rank Xerox, Ltd., be reproduced, copied or used for any purpose whatsoever, except the manufacture of articles for Xerox Corporation or Rank Xerox, Ltd.	1. Tolerances .XX ±.030 Angular .XXX ±.010 ±½° 2. Break All Sharp Edges .010 Approx. 3. Mach. Surfaces 4. All Dim. In Inches	Appr.	Greeman	1-13-78	-	LIST, WIRE - BACKWIRI		
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	10622594	SACT*	1_	10J_029	11J 102	594	_532	0	536_	642_	5.64_	ALIIXM	A
	20622562	6ACT*	2	10J 021	11J 101	562	532	0	536	638	564	ALIIXM	A
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	20652528	A(0)*	•	02J 107	03J 107	528	662		658		662	ALIIXM	
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	20653440	A(0)*	,	06J 055	07J 055	440	698	694	694		698	ALIIXM	
	10653392	A(0)*	ī	07J 055	097 055	392	698	694	694	440	698	ALITAM	
	20652384	A(00)*	- 2	09J 119	04J 110	384	710	678	0	504	674	ALIIXM	A
	10652504	A(00)*	1	04J 110	03J 110	504	674	670	670	528	674	ALIIXM	
	20652528	A(00)*	2	03J 110	02J-110	528	674	670	670	552	674	ALIIXM	* *
W	10652552	A(00)*	ī	02J: 110	01J 110	552	674	670	670	576	674	ALIIXM	A
	30621666	A(00)*	2	017-110	01J 108	666	324	0	320	674	324	ALIIXM	Ä
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	10652504	A(00)*	ì	03J 108	04J 108	504	666	662	662	528	666	ALIIXM	
1.0	20621634	A(00)*	2	04J 108	04J 100	634	396	392	392	666	396	ALIIXM	Ä
	10652504	A(00)*	ī	04J 100	03J 100	504	634	630	630	528	634	ALIIXM	Ä
	20652528	A(00)*	2	03J 100	02J 100	528	634	630	630	552	634	ALIIXM	Ä
	10652552	A(00)*	1	02J 100	017 100	552	634	630	630	576	634	ALIIXM	Ä
	20621618	A(00)*	2_	01J 100	01J 096	618	324	320	320_	634	324	ALIIXM	Α ·
	10652552	A(00)*	1	01J 096	02J 096	552	618	614	614	576	618	ALIIXM	A
	20652528	A(00)#	2	02J 096	03J 096	· 528	618	614	614	552	618	ALIIXM	Ā
	10652504	A(00)*	11	03J 096	04J 096	504	618	614	614	528	618	ALIIXM	A
	20621586	A(00)*	2	04J 096	04J 088	586	396	392	392	618	396	ALIIXM	A :
	10652504	A(00)*	1	04J 088	03J 088	504	586	582	582	528	586	ALIIXM	Α
	20652528	A(00)*	2	03J_088	_02J_088_	528		582	582	552	586_	ALIIXM	Α
	10652552	A(00)*	1	02J 088	01J 088	552	586	582	582	576	586	ALIIXM	A
	30621582	#(00)A	2	01J 088	01J 087	582	324	0	0	586	324	ALIIXM	A
	_10652557	A(00)*	1	01J 087	02J 087	552	582	578	578	576	582	ALIIXM	Α
	20652528	*(00)	2	02J 087	03J 087	528	582	578 .		552	582	VLIIXW	A
	10652504	A(00)*	1	03J 087	04J 087	504	582	578	578	528	582	ALIIXM	A
	20652504.	_A(00)*	2	04J 087	01J 078			550				ALIIXM	
•	10652552	A(00)*	1	01J 078	02J 078			542				ALIIXM	
	20.652528	A(00)*	2	02J 078	03J 078	528	546	542	542	552		ALIIXM	
	10652504	A(00)*	<u></u> l	<u>031 078</u>	04J 078		546		542			ALIIXM	
	20621510	A(00)*	2	04J 078	04J 069			392	392	546		ALIIXM	
	10652504	A(00)*	1	04J 069	03J 069	504				528		ALIIXM	
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	20652552	A(1)*	<u>2</u>	01J 095	02J 095			610	610	576		ALIIXM	
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	20652504	A(1)*	2	03. 095	04J 095			610		528		ALIIXM	
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	20652528	A(2)*		2J 085	03J 085	528	574	570	570		574	ALIIXM		
	10652504	A(2)*		3J 085	-		574			528	574	ALIIXM	Α	
	20621574	A(2)*	-	4J 085			396_			706		ÁLIIXM	Α	
	10653464	A(2)*		5J 057		464	706	702	702		706	. ALIIXM	A	
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	20652504	A(3)*		3J 077		504	542	538	538		542	ALIIXM	Α .	
	10621542	A(3)*		4J 077		542		0	400	710	412	ALIIXM	Α	
	20653464	A(3)*	-	5J 058				.706_			710_	ALIIXM_	Α	
	10653440	A(3)*		6J 058		440	710	706	706	464	710	ALIIXM	A	
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	20621502	A(4)*		14J 067		502	396	0	400		412	ALIIXM		
	10653464	A(4)*		15J 059		464	714	-			714	ALIIXM		
	20653440	A(4)*	-	6J 059				710			714	ALIIXM		
	10653392	A(4)*		7J 059			714	710	710		714	ALIIXM		
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	20653464	A(5)*	-	080 C80		440		714	714		718	ALIIXM		
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	10652320	ALUF (0)	1 1	113 040	123 040	. 520	0.70	034	0,74	247	. 030	716 2 3 7711	^	
	20/52220	ALUF(1)	2 1	11J 041	12J 041	320	642	638	638	344	642	ALIIXM	Δ	
	20652320	ALUFITI	4	110 041	123 041	320	042	050	0.50	744	045	DE 1 1/1/1	^	•
	10(60000	ALUF(2)		L1J_042	12J 042	220	646	642	642	344	646	ALIIXM	A	
	10652320	ALUF (2)		F15_677		224	V.T.V	x=4	¥36		× 7 V			
	20152222	A1116/91	,	11J 043	12J 043	220	650	646	646	244	650	ALIIXM	A	
	20652320	ALUF(3)	2	110 043	120 043	320	070	070	U 7U	, -r -1	0.50	71m & 671-1		•
	10/500/0	41117001	1	9J 100	10J 100	360	634	630	630	384	634	ALIIXM	Α .	
	10652360	ALU(00)	-	107 100				630				ALIIXM		
	20652312	ALU(00)	2	107 100	152 100	212	624	050	, 0 3 0	500	034	VETTVIL	^	
	10/500/5	4111033		101 101	10J 101	360	424	634	631	384	638	ALIIXM	Δ	
	10652360	ALU(01)	_	09J 101			638		634	360		ALIIXM		•
	20652312	ALU(01)	2	10J 101	12J 101	21.5	050	034	034	200	020	VETTVA	^	
	10/633/5	ALU(02)		09J 102	10J 102	360	642	638	AZA	384	642	ALTIXM	A	
	10652360			10J 102				638		-		ALIIXM		
	20652312	ALU(02)	2	103 102	120 102	212	J 7 Z	טעט	ווכט	,,,,	J72			
		4111031	1	09J 103	3 10J 103	360	646	642	6/2	384	646	ALIIXM	Δ	
	10652360	ALU(03)	-					642			646	ALIIXM		
	20652312	ALU(03)	2	10J 103	3 12J 103	312	040	042	042	300	040	VETIVG	^	
				001.55	101101	2/0	100			201	150	ALIIXM	Δ	
	10652360	ALU (04)		09J 104				646			650	ALIIXM		_
	20652312	ALU(04)	2	107 104	+ 12J 104	312	020	646	040	200	650	WEIIVU	^	
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	10652360	ALU(05)		09J 105										
	20652312	ALU(05)	2	10J 109	5 12J 105	312	654	650	ดวบ	00ء	074	ALIIXM	Α	

REFE F	MACH=	LIST=	NAME	Z	FROM	1 to 1	AX	ΑY	VIAA	VIAB	вх	ВҮ	SYSTEM	REV [
	10652360		ALU(06)	1	09J 106	101 .06	360	658	654		384		ALIIXM		
	20652312		ALU(06)	2	10J 106	12J 106			654	654	360	658	ALIIXM	Α ΄	,
	10652360		ALU(07)	1	09J 107	10J 107	360	662	658	658	384	662	ALIIXM	٨	
•	20652312		ALU(07)	2	10J 107	12J 107	312	662	658	658	360	662	ALIIXM	A	
	10652360		ALU(08)		09J 108	10J 10B	360	666	662	662	384	666	ALIIXM	A	
	20652312		ALU(08)	2	101 108	12J 108			662				ALIIXM	A	
	ر جست د				09J 109	10J 109	360	670		666	384	670	ALIIXM		
	10652360 20652312		ALU(09) ALU(09)	2	10J 109	10J 109		670 670	666			670	ALIIXM		
				<u>-</u>							0.04		ALIIXM		
	10652360		ALU(10)	1	09J 110 10J 110	10J 110 12J 110		674 674	670 670	670 670	360	674 674	ALIIXM		
	20652312		ALU(10)	2	103 110	125 110									
	10653360		ALU(11)	1	09J 111	10J 111			674		384		ALIIXM ALIIXM		
	20653312		ALU(11)	2	10J 111	12J 111	312	0.18	674	014	200	678	WETTVU	^	
	10653360		ALU(12)	1	09J 112	10J 112			678	678	-	682	ALIIXM		
	20653312		ALU(12)	. 2	10J 112	12J 112	312	682	678	678	360	682	ALIIXM	Α΄	
	10653360		ALU(13)	1	09J 113	10J 113	360	686	682	682	384	686	ALIIXM	A	
	20653312	•	ALU(13)	ž	10J 113	12J 113		686		682	360	686	ALIIXM	Α	
	1575557				09J 114	10J 114	260	690	686	686	384	690	ALIIXM		
	10653360 20653312		ALU(14) ALU(14)	1 2	10J 114	103 114 12J 114		690	686	686	360		ALIIXM		
		·											41 1 1 041		
	10653360		ALU(15)	1	09J 115 10J 115	10J 115 12J 115	360 312	694 694	690 690	690 690	384 360	694 694	ALIIXM ALIIXM		
•	20653312		ALU(15)	2	105 115	·									
	10652288		AUSYSCLK	1	12J 072	13J 072		522		518	312		ALIIXM		
	20652264		AUSYSCLK	2	13J 072	14J 072	264	522	518	518	288	522	ALIIXM	A	
	10652320		AUSYSCLK*	1	11J 008	12J 008	320		506	506	344	510	ALIIXM		
•	20652296		AUSYSCLK*	2	12J 008	13J 008	296	510	506	506	320	510	ALIIXM		
	10652272		AUSYSCLK*_ AUSYSCLK*		13J 008 14J 012	14J 012 21J 075		<u>526</u>	<u>514</u> 530		296 272		ALIIXM		
	20652096		WOO LOCKY#	۷	140 012	210 015	,,							r	
	10652392_		BANKSELO	_1_	07J 028	09J 021	392	562	0	566	440	590	ALIIXM	A	
	20652392	•	BANKSEL1	2	07J 031	09J 020	392	558	0	562	440	602	ALIIXM	A	
				-											
	10622522		8KO	1	08J 032	09J 011	522	508	488	0	606	484	ALIIXM	A	
	20622526		BK1	2	08J 033	09J 012	526	508	488.	0	610	484	ALIIXM	Α	
***************************************		· <u>······</u>											41 * * 114		
	10652336	•	BLOCK*	1	09J 099	11J 110 13J 110		674	634 670	0 670	384	630 674	ALIIXM ALIIXM		
	20652288 10652096		BLOCK*	1	11J 110 13J 110	21J 110	96	674	670	670	288	674			
	.00,20,0	•		. •			1							•	
	20652320	 	BSZO	_2_	11J 044	12J 044	320	654	650	650	344	654	ALIIXM	Α	
	10653336		BSO	1	09J 120	11J 120	336	714	710	710	384	. 714	ALIIXM	Α .	
	20653336		BS1	2	09J 121	11J 121	336	718	714	714	384	718	ALIIXM	A	
	10653336		BS2	1	09J 122	11J 122	336	. 722	718	718	384	722	ALIIXM	A	

	BEFERENCE	MACH	LIST. NAM	E 2	FROM	TO,	AX	AY	VĮAĄ	VIAB	вх	ВҮ	SYSTEM REV	
1		20652408		11 2	080 لـ20	LRO	408	554_	550	550	432_	554	ALIIXM A	
		10652384	BUS (00	1) 1	08J 080	09J Day	384	554	550	550	408	554	ALIIXM A	
	•	20652360	BUS (00	11 2	09J 080	10J 080	360	554	550	550	384	554	ALIIXM A	ì
' ·L		10652336_	BUS (0 0	111_	10J 080	11J_080	336	554	550	550	360_	554	ALIIXM A	<u> </u>
a 4	\	20652312	. BUS (00	1) 2	11J 080	12J 080	312	554	550	550	336	554	ALIIXM A	İ
FORMNSTSU		10652288	BU\$100	1) 1	12J 080	13J 080	288	554	550	550	312	554	ALIIXM A	
ğ •		20652764	BUS (0 0	11_2	13J_080	14J 080		554_	550				ALIIXM A	
* /	· · · ·	10652240	BUS (00		14J 080	15J 080	240	554		550			ALIIXM A	1
3 1	•	20652216	BUS (00		15J 080	16J 080			55Q				ALIIXM A	į
() K		<u> </u>	BUS (0 0		167 080_	<u>17J_080_</u>			550				ALTIXM A	
7.19		20652168	BUS (00		17J 080	18J 080				550			ALIIXM A	
		10652144	BUS (00		18J 080	19J 080	144	554		550			ALIIXM A	1
å, "-		20652120	BUS (00		191 080	20J 080	120			550			ALIIXM A	
Harrandha Z Z Z Z	•	10652096	BUS (00	1) 1	20J 080	21J 080	96	554	550	550	1,20	554	ALIIXM A	
割		20652408_	BUS(01	12	07J 081_	180_L80	408	.558_,	_554	554	432_	558_	ALIIXM_A	
1.	•	10652384	8US(01	1	08J 081	09J 081		558		554		558	ALIIXM A	
12		20652360	RUS (01		09J 081	10J 081	360	558	554	554	384	558	ALIIXM A	1
14		10652336_	<u>BUS(01</u>	11_	107 081	117 081			554			558	ALIIXM A	
. 12		20652312	BUS (01	-	11J 081	12J 081						558	ALIIXM A	
F-1		10652288	BUS (0 1		12J 081	13J 081		558		554			ALIIXM A	į.
18		20652264_	BUS(01		13J_081_	14J.081						_558	ALIIXM_A	
14		10652240	BUS (01	, -	14J 081	15J 081	240				264		ALIIXM A	ļ
[23]		20652216	BUS (01	-	15J 081	16J 081	216				240		ALIIXM A	•
	555	10652192	BUS (01		16J 081	17J 081	192			554			ALIIXM A	
- [**]		20652168	BUS (01		17J 081	18J 081	168				192		ALIIXM A	ľ
**		10652144	BUS (0 1		18J 081	19J 081				554	- /	558	ALIIXM A	ľ
24		20652120	BUS (01		19J_081_				_554				ALIIXM A	
23		10652096	BUS (0 1		20J 081	21J 081	9,6	558	554	554	120	558	ALIIXM_ A	
1[20652408_	RUS (0 2) 2	07J 082	08J 082				558	432	562	ALIIXM A	
1:	•	10652384	BUS (0 2	1	08J 082	09J 082		562	558	558	408	562	ALIIXM A	l l
1.		20652360	BUS (0 2	1 2	09J 082	10J 082	360	562	558	558	384	562	ALIIXM A	1
H.	· · · · · · · · · · · · · · · · · · ·	10652336_	RUS (0 2	11	10J_082_	11J_082	336			_558		562_	ALIIXM_A	
		20652312	BU\$ (0 2	1 2	11J 082	12J 082	312					562	ALIIXM A	
33		10652288	BUS (0 2	1	12J 082	13J 082					312		ALIIXM A	· .
*		20652264_	BUS (0 2		<u> 13J 082</u>	14J 082		562			260		ALIIXM A	
1.1	4	10652240	BU\$ 102	_	14J 082	15J 082							ALIIXM A	į.
-5	•	20652216	BUS (02		15J 082	16J 082	216				240		ALIIXM A	
" -		10652192_			16J_082_	17J_082_	192			558			ALIIXM_A	
*		20652168	BUS (02		17J 082	18J 082	168	562			192		ALIIXM A	l,
["]		10652144	BUS (02		18J 082	19J 082	144		558			562	ALIIXM A	
"		20652120	BUS (02		19J 082	20J 082	120_			558			ALIIXM A	
		10652096	BUS (0 2) 1	20J 082	21J 082	96	562	558	558	120	562	ALIIXM A	
H.		20652408_	BUS (0 3	12	07J 083_	08J_083	408	.566	562	562	432	566	ALIIXM_A	
ξ.		10652384	BUS (0 3		08J 083	09J 653		566			408	566	ALIIXM A	Ì
47		20652360	BUS (0 3		09J 083	10J 083		566		562		566	ALIIXM A	ŗ
4		10652336_	BU\$ (0 3)1	107 083	11J_083	336	566_	562_	562_	360_	566_	ALIIXM_A	}
"[·:	20652312	BUS (03	1 . 2	11J 083	12J 083		566	562	562	336	566	ALIIXM A	, -
1-1	•	10652288	BUS (0 3		12J 083	13J 083	288	566	562		312	566	ALIIXM A	Į.
l: L		20652264	RUS (0 3		13J 083	141 083	264		_562			_566	ALIIXM A	
3:[10652240	BUS (0 3		14J 083	15J 083	240	566	562		264	566	ALIIXM A	· •
:1	•	20652216	BU\$ (0 3		15J 083	16J 083	216	566	562			566	ALIIXM A	Į.
3.4		10652192	BUS (03		16J 083	17J 083	192	566	562	562		566	ALIIXM A	
::[20652168	BUS (0 3		17J 083	18J 083	168	566	562		192	566	ALIIXM A	·
	•	10652144	BUS (0 3		18J 083	19J 083	144	566	562		168	566	ALIIXM A	
M.		20652120	AUS (03		19J 083	20J 083				562_			ALIIXM A	
, χ		10652096	BUS (0 3	1	20J 083	21J 083	96	566	562	562	120	566	ALIIXM A	

REFERE	m-E 4	IACH#	LIST=	NAME	Z	FRO	MC	Ϊ́		AX	AY	ÁÍVÝ	•		ВŸ	SYSTE	•	'	
1	206	52408		US (04)	Ż	073	384	80	ì			566_				ALIIX		{	
}		52384		US (04)	1	08J (084	09J i	104	384	570	566	566	408		ALIIX		•	
		52360		SUS (04)	2	091	084	103 0	84	360	570	566		384	570	ALIIX			
1		52336		SUS (04)	ĭ	10J (11J 0	286	336	570	566	566_	_360	_570_	ALIIX			
		52312		BUS (04)	2	TilJ (084	12J 0	84	312	570	5 66	566	336	570	ALIIX			
1		552288		3US (04)	ī	12J		13J 0		288	570	566	566	312		ALIIX			
1				3US (04)	ż	13J (14J 0		264	570	566	566	288	570	ALIIX			
`		552264		SUS (04)	<u>f</u>	147		150		240	570	566	566	264	570	ALIIX	A M.		
		552240		3US (04)	2	15J		16J 0		216	570	566	566	240	570	ALIIX	M A	•	
'	_	552216			1			17J_0		192		566	566	216_	570	ALIIX	M. A.		
·		552192 _		3US (04)	<u>+</u>	17J		ieJ		168	570	566	566	192		ALIIX	M A		
1		552168		3US (04)	2	18J		193 0		144	570	566		168		ALIIX	M A		
	_	552144		3US (04)	1	19J		20J 0		120		566		144		ALIIX		•	
2		65212 <u>0</u>		3US (04)				21J (96	570	566	566		570	ALIIX		•	
	100	552096	ł	RUS (04)	1	20J	084	213 (704	•									
V		652408_		BUS (05)_	2		085_			408	574	_570 _ 570	_570_ 570	_432. 408		ALII) ALII)			
0		652384		BUS (05)	1	08J		09J (574	570	570		574	ALII)			
2	20	652360		AUS (05)	2	. 09J		100 0		360			570		574	ALII			
4		652336_		BUS (05)	11	101		117			574	570	570		574	ALII			
:-		652312		RUS (05)	2	11J		12J (312	574	570			574	ALII			
		652288		BUS (05)	1	12J		13J (288	574	570	570			ALII			
ı.ļ	20	652264		BUS (05)	2_	13J		14J_(570					(M A		
	10	652240		RUS (05)	1	14J		15J (240			570		574				
23		652216		BUS (05)	2	15J	085	16J (574	570	570		574		(M A		
24		652192		RUS (05)	1	16J	085	<u> 17J</u>			574		570		574	ALII			
23		652168		BUS (05)	2	17J	085	18J (085	168	574	570	570		574	ALII			
		652144		BUS (05)	1	18J	085	19J	085	144		570	570		574		KM A		
]]		652120_		BUS (05)	2	19J	085	207	085	120	574	570_		144.		ALII			
<u>k</u>		652096		BUS (05)	ì	20J		21J	085	96	574	570	570	120	574	ALII	KM A		
ř.	20	453400	*	RUS (06)	2	07J	086	L80	086	408	578	574	574	432	578	AL 11	KM · A		
^`		652408			<u>-</u> -	087		097		384	578	574	574	408		ALII	A MX		
7		652384		BUS (06)		09J		100		360		. 574	574	384	578	ALII	XM A		
4		652360		BUS (06)	2			113		336		574_	574		578		A MX		
4		652336_		BUS (06)	<u></u> _	joi		12J		312	578	574	574		578		XM A		
F•		652312		BUS (06)	2	11J				288	578	574	574		578		XM A		•
r.		652288		BUS (06)	1.		086	13J		264	578	574	574		578	ALII			
*		652264		RUS (06)		13J		<u> 141</u>		240	578		574	264			XM A		
	10	65224		BUS (06)	1	14J		15J			578		574	240			XM A		•
		652216		BUS (06)	2	15J		16J		216					578		XM A		
n,	10	652192_		BUS (06)	1_		086_	177				574					XM A		
),————————————————————————————————————	20	652168		BUS (06)	2		880	18J		168	578		574	192			XM A		
1-1	10	652144		BUS (06)	1	18J		19J		144	578		574		578 578	ALII			
15	20	652120		BUS (06)	2		086	20J		120	578	574	574				A MX		
	10	652096		BUS (06)	1	207	086	21J	086	96	578	574.	514	120	578	AL!!	A11 A	`	
	2(1652408.		BUS (07)	2	<u>0</u> 7J	087	ัดต์วั	087_				_578	432	582	ALII	XM A		
1		0652384		BUS (07)	ì		087	09J		384		578	578		582		XM A		
J.		0652360		AUS(07)	2	09J	087	10J	087	360	582		578				XM A		
1.1		1652336		BUS (07)	ī		087	11J		336	582	578				ALII			
		0652312		RUS (07)	2		087	12J		312	582		578				XM A		
I.I		0652288		BUS (07)	î		087	13J		288	582	578	578		582		XM A	4	
1.1				BUS (07)	•		087	143		264	582	578	578	288	582	ALII		4	
 		0652264		BUS(07)	1		087	150		240			578		582	ALII	XM A	Α .	
15		0652240					087	16J		215			578				XM A		
"		0652216		BUS(07)	2		087	17J		192		578			-582		XM A		
14		0652192		BUS (07)	 			183		168					582		XM A		
*:		0652168		BUS (07)	2		087				582		578				XM A		
-		0652144		BUS(07)	1		087								582		XH A		
(·)		0652120		BUS (07)	2		087	201		120							XM A		
		0652096		BUS (07)	1	207	087	21J	087	96	202	578	578	120		V#11		-	

REFEREN	MACH=	LIST= NAME	Z	FROM	TO	AX	AY		VIAB		BY	1	6
•	20652408	BUS (08)	2	07J 088		408 _	586	_582	_582_	_432_	586	ALIIXM_A	
		BUS (08)	1	081 098	840 660	384	586	582	582	408	586	ALIIXM A	~
	10652384	BUS (08)	2	09J 088	10J 088	360	586	582	582	384	586	ALIIXM A	
•	20652360			_10J 088	_11J_088	336	586	_582_	582_	_360_	_586	ALIIXM_A	
	10652336		_1		12J 088	312	586	582	582	336	586	ALIIXM A	
	20652312	BUS (08)	2	11J 088	•		586		582		586	ALIIXM A	
·	10652288	BU\$ (08)	1	12J 088	13J 088	288	200	_582_				ALIIXM A	
	20652264	RUS (08)	2	_13J_088_	14J 088				_202_	264		ALIIXM A	
	10652240	BUS (08)	1	14J 088	15J 08B	240	586	582	202	204	500		
	20652216	BU\$(08) -	2	15J 088	16J 088	216	586	582	582.	240	- 584	ALIINO A	
•	10652192	BUS(08)	1	_16J_088	_17J_088	192 _	_586.		_582_	_216_	_586_	ALIIXM_A	
		AUS (OB)	2	17J 088	18J 088	168	586	582		192		ALIIXM A	
	20652168	BUS (08)	ī	18J 088	19J 088	144	586	582	582	168	586	ALIIXM A	
•	10652144		2	193 088	20J 088	120	586	582	582	144	586	ALIIXM A	
•	20652120	BUS (08)		20J 088	21J 088	96	586	582	582	120	586	ALIIXM A	•
	10652096	BUS (08)	1	203 000	215 000	,,	,,,,						
1					201 202	4.00	500	E Q 4	586	432	590_	ALIIXM_A	
•	20652408_	BUS (0.9)	_2	_074 089_	_08J_088				586	408	590	ALIIXM A	
	10652384	BUS (09)	1	08J 089	09J 089	384		586				ALIIXM A	
	20652360	BUS(09)	2	09J 089	107 083	360	590	286			590		
	10652336	BUS (09)	ï	10J 089	11J 089	336		<u> 586</u>				ALIIXM_A	
		. BUS(09)	2	11J 089	12J 089	312			586		590	ALIIXM A	
•	20652312	BUS(09)	î	12J 089	13J 089	288	590	586	586		590	ALIIXM A	
	10652288		, †	13J_089	14J 089	264	590	586			590_	ALIIXM_A	
	20652264_	BUS (09)			15J 089	240		586	586	264		ALIIXM A	
	10652240	BUS (09)	1	14J 089		216	590				590	ALIIXM A	•
	20652216	BUS(09)	2	15J 089	16J 089			500			590	ALIIXM A	
	10652192	BUS (09)	1	16J 089	17J 089	192						ALIIXM A	
	20652168	BUS (09)	2	17J 089	18J 089	168	590		586	192			
	10652144	BUS (09)	1	18J 089	19J 089	144			586	168		ALIIXM A	
		BUS (09)	2	19J 089	20J 089	120_	_590	586_	586_		590_	ALIIXM_A	
	20652120			20J 089	21J 089	96	590	586	586	120	590	ALIIXM A	
,	10652096	BUS (09)	7	200 00)									
				671.000	001 000	408	594	590	590	432	594	ALIIXM A	
	20652408_	BUS (10)		<u>07J 090</u>	08J 090				590		594	ALIIXM A	
	10652384	BUS(10)	1	08J 090	09J 090	384					594	ALIIXM A	
	20652360	BUS (10)	2	09J 090	10J 090	. 360							•
	10652336	BUS(10)	_1_	101 090	<u> 11J 090</u>		594				594_		
	20652312	BUS (10)	2	11J 090	12J 090	312						ALIIXM A	
		BUS (10)	1	12J 090	13J 090	288	594	590		-	594	ALIIXM A	
	10652288		2	13J 090	14J 090	264	594	590	590	288	594	ALIIXM A	
	20652264_	BUS(10)		14J 090	15J 090	240				264	594	ALIIXM A	
	10652240	RUS (10)	1			216					594	ALIIXM A	
j	20652216	BUS(10)	2	15J 090	16J 090						594		· · · · · · · · · · · · · · · · · · ·
•	10652192	BUS(10)	1	16J_090_	17J_090_	192		590				ALIIXM A	
· · · · · · · · · · · · · · · · · · ·	20652168	BUS (10)	2	17J 090	18J 090	168							
	10652144	BUS (10)	1	18J 090	19J 090	144						ALIIXM A	
	20652120	BUS (10)	2	19J 090	20J 090	120	594	590				AL LIXM A	
				20J 090	21J 090	96		590	. 590	120	594	ALIIXM A	
•	10652096	BUS(10)		200 070	E 0.0								
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	10652384	BUS(11)	1	08J 091	09J U91	384						ALIIXM A	
	20652360	BUS(11)	2	09J 091	10J 091	360						ALIIXM A	
	10652336	BUS(11)	1	107 051	117 051	336	598						
	20652312	BUS(11)	2	11J 091	12J 091		598					ALIIXM A	
4		BUS(11)	ī	12J 091	13J 091	288	598	594				ALIIXM A	
•	10652288		2	13J 091	14J 091_	264				286	598	ALIIXM_A	
	20652264	BUS (11)	{-		15J 091	240						ALIIXM A	
	10652240	BUS (11)	1	14J 091									
	20652216	RUS(11)	2	15J 091	16J 091	216							
	10652192		1	16J 091	17J 091	192							
	20652168		2	17J 091	18J 091	168							
Í	10652144		ī.	18J 091	19J 091	144							•
			2	19J 091	20J 091	120	598	594			4 598		
•	20652120	BUS(11)	$-\frac{2}{1}$	20J 091	213 091	96	500	594	594	120	0 598	ALIIXM A	

10052740		REFERENCE	MACH*	LIST= NAME	Z	FROM	10	XA	ΑY	VIAA	AIVB	вх	BY	SYSIEM KEV	nd 5
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REFERENCE	MACH=	LIST= NAME	2	FROM	ĮU #	KA 1	` '	v 4 ****			- "			₽ #	
		BUS=2*	2	11J 020	15111		556			6 <u>86</u> 240	660 686	ALIIXM ALIIXM	A		
	_2062255B	BUS=2*	1	15J 113	16J 11.							ALIIXM		₹.	
	10653216		2	16J 113	17J 113			-		216			A		
	20653192	BU\$=2#	4	17J 113	18J_113	168	686	682		192		_ALIIXM_	<u> </u>		
	1065316 <u>8</u>	BUS=2*			195 113	144	686	682	682	168	68 6	ALIIXM			
	20653144	BUS=2*	2	18J 113				682	682	144	686	ALIIXM	A		
	10653120	BUS≖2*	1	19J 113	20J 113	120	000								
	20075104					552	590	586	586	576	590	ALIIXM	A		
· · · · · · · · · · · · · · · · · · ·	20652552	CAS	2	01J 089	02J 089		590	586		552		ALIIXM	Α		
¥		CAS	1	02J 089	03J 089				586			ALIIXM	Α		
	10652528	***	2	03J 089	_04J_089	504			500	250		ALIIXM	Δ		
	20652504	CAS	1	04J 089	09J 041	392	642	594	0	504	240	VETTVII	^		
•	10652392	CAS	•							- • •		ALIIXM	Α .		
		CONCTA	2	09J 097	10J 097	360	622	618	618	384			^		
	20652360	CONST*		10J 097	11J 097	336	622	618	618	360	622	ALIIXM	A		
· · · · · · · · · · · · · · · · · · ·	10652336	CONST*	÷	102 031	113 07	•	•								
					** * * * * *	606_	532	0	536_	690	264	ALIIXM_			
	20622606	CURTAC*	2	<u> 101 032 </u>	_117_114		690.			336	690	ALIIXM	Α		
	10653288	CURTAC#	1	11J 114	13J 114	288	370	550							
	10033200				•			410	610	564	414	ALIIXM	Α		
	00/035/0	DBARC	2	101 063	_11J_063				410	612	414	ALIIXM	A		
	20631540	DBARC	1	11J 063	13J 063	564	414	410	410			ALIIXM			
:	10631564			13J 063	14J 063	612	414	410	410		414				
•	20631612	DRARC	2	144.063_	15J 063	636_	414_		410_	_660_		ALIIXM_			
	10631636	DBARC	<u></u>	14J.003 15J 063	16J 063	660	414	410	410	684	414	ALIIXM			
	20631660	DRARC	. 2			684	414	410	410	708	414	ALIIXM	A		
	10631684	DBARC	1	161 063	17J 063	708	414	410	410	732	414	ALIIXM	_A		
•	20631708	DRARC	_ 2	17J 063	<u> 187 063 </u>			410	410	756	414	ALIIXM	Α	•	
	10631732	DBARC	1	18J 063	19J 063	732	414		410	780	414	ALIIXM	` A		•
		DBARC	2	19J 063	20J 063	756	414	410	410	100	747				
•	20631756	Dimite									410	ALIIXM	Δ		
		DREVECTY	1	10J 062	11J 062	540	418	414	414	564	418			•	
	10631540	DBSYSCLK	•	11J 062	13J 062	564	418	414	414	612	418	ALIIXM			
	20631564	DRSYSCLK	2		14J 067	264	502	486	0_	288	482	ALIIXM			
	10652264	DRSYSCLK	<u>-</u> _	131 062		240	502	498	498	264	502	ALIIXM			
······································	20652240	DRSYSCLK	2	14J 067	15J 067		502	498	498	240	502	ALIIXM	Α	•	
1	10652216	DRSYSCLK	1	15J 067	16J 067	216			498			ALIIXM	_A		
•		DRSYSCLK_	2	16J_067_	17J_067	192_	_502			192	502	ALIIXM	Α		
	20652192_	DRSYSCLK	1	173 067	18J 067	168	502	498	498	-		ALIIXM			
	10652168	DASYSCLK	2	18J 067	19J 067	144	502		498	168	502				
	20652144			19J 067	20J 067	120	502	498	498	144	502	ALIIXM			
	10652120	DRSYSCLK	<u>+</u>	173 001											
			_		07J 105	336	654	650	650	432	654	ALIIXM	A		
i	20652336	DCT1B	2	11J 105	012 105	ن ر ر									
						22/	658	654	654	432	658	ALI1XM	Α		
	10652336	DCT28	1	11J 106	07J 106	336	סכם	024	024						
	10037330		•					,	450	432	662	ALIIXM	Α		
	00/5000/	DCT4B	2	11J 107	07J 107	<u> 336</u>	662	658	658	426	- 502				
	20652336	00140										AI TTYM	Δ		
			•	11J 108	07J 108	336	666	662	662	432	666	ALIIXM	^		
	10652336	DCTBB	1	112 100	V •••										
				11J 006	13J 906	296	502	498	498	344	502	ALIIXM	A		
	20652296	DDR-	2	112 000	170 100										
					111022	51A	532	0	536	570	556	ALIIXM	A_		
	10622510	DHTAC*	1	107 008	111 023		570	566			570	ALIIXM	Α		
	20652296		2	11J 023	13J 023	270	, 0	. , , , ,							
	400000						634	E 2 0	620	236	534	ALIIXM	Α		
•	10/52212	DNS	1	11J 075	12J 075	312	534	530	2 20	بار د					
	10652312	V114									F C -	ALIIXM		•	
		A A.	•	10J 007	11J 022	506	532	2 0			556				
	20622506	DVTAC*	2			296		562	562	344	566	ALIIXM	<u> A</u>		
	10652296	DVTAC*	1	11J 022	170 022	=		·							
					111001	610	532	, (536	574	556	ALIIX			
		DWTAC*	2	10J 010	11J 024	210	236				574				
	20622518	DNIAC	- 4	111 024					ነ ፍግሳ	1 244	. "1"	MLIANI			

1	REFEREN	MACH=	LIST= N	AME	Z	FROM	TO	AX	AY	VIAA	VIAB	BX	BY	SYSTEM REV	
	(.	00/0054/	EMAC	т	2	091 078	10J	546	516	0_	520	_630_		ALIIXM A	— \ —-{-
1		20622546			i	10J 099	11J 05.		630	626	626	360	630	ALIIXM A	
1		10652336	EMAC		-	11J 099	12J 099	312	630		626		630	ALIIXM A	Į.
:	,	20652312	EMAC		2		141 099	264	630	626_	626	_312	630 _	ALIIXM_A	
		10652264_	EMAC			137 0 <u>68</u>				534			538	ALIIXM A	
		20652264	ESTO	P	2	10J 076	14J 076	264						ALIIXM A	
, -		10622550	ETAC	Ħ	1	10J 018	11J 100	550		0		634		ALIIXM A	
-	•	20652264	ETAC	* -	2	11J 100	14J 100	264	634	630	630	3.20	634		
-		10652272	F110))	1	11J 030	14J 030	272 248	598 598	594 594	594 594	344 272	598 598	ALIIXM A ALIIXM A	
11		20652248	F1(0		2	14J 030	15J 030		598	594	594	248		ALIIXM A	
2		10652224	F1(0		1	15J 030	167 030	224_ 200	598	594	594	224		ALIIXM A	
: -		20652200	F110))	2	16J 030	17J 030 .	176	598	594	594	200		ALIIXM A	•
	•	10652176	F1(0		1	17J 030	18J 030		598	594	594		598	ALIIXM A	
n_{j} .	•	20652152 _	F1(0		2	187 030_	19J_030	128		594	594	152		ALIIXM A	
ij,		10652128	F1(0		-1	19J 030	20J 030	104	598	594	594	128		ALIIXM A	
2		20652104	F1(0) }	2	20J 030	21J 030								
" _		10652272	F1()	11	1	11J 031	14J 031	272		598	598	344		ALIIXM A	
1		20652248	F1()		2	14J 031	15J 031	248	602	598	598			ALIIXM A	
"		10652224	F1()		7	15J 031	16J 031	224	602	598	598	248	602	ALIIXM_A	
-		20652200	F1(16J 031	17J 031	200	602	598	598			ALIIXM A	
-			F1(ī	17J 031	18J 031	176	602	598	598			ALIIXM A	• ,
	. ·	10652176	F1(,	18J 031	19J 031	152	602	598	598			ALIIXM A	
" _		20652152	Fii			19J 031	20J 031	128	602	598	598			ALIIXM A	
-	•	10652128			2	20J 031	21J 031	104		598	598	128	602	ALIIXM A	
		20652104	F1(11	۲							344	606	ALIIXM A	
1	······································	10652272	F1(2)	1	11J 032	14J 032	272			602 602			ALIIXM A	•
		20652248	F1(2)	2	14J 032	15J 032	248				-		ALIIXM A	
1	•	10652224	F1(21	11	15J 032	16J 032	224			602			ALIIXM A	
ŀ		20652200	F11	2)	2	16J 032	17J 032	200			602			ALIIXM A	
		10652176	F11	2)	· 1	17J 032	18J 032	176						ALIIXM A	
.,		20652152	F10	21	2_	187_035	<u> 19J 032 </u>	152					606	ALIIXM A	
÷		10652128	F11		1	19J 032	20J 032	128						ALIIXM A	
33	÷	20652104	F1(2	20J 032	21J 032	104	606	602	602				
1-			F1(21	1	11J 033	14J 033	272	610	606	606	344		ALIIXM A	•
3.	,	10652272			1	14J 033	15J 033	248			606	272	610.		
4	•	20652248	F1(2	15J 033	16J 033			_606	606	248	610	ALIIXM A	
٤.		10652224	F1(16J 033	17J 033	200				224	610	ALIIXM A	
M		20652200	F1(2	- -	18J 033	176				200	610	ALIIXM A	
41		10652176	F1(1	17J 033	19J 033	152	_				610	ALIIXM A	
45		20652152				18J 033	20J 033	128						ALIIXM A	
		10652128	F11		1	19J 033		104						ALIIXM A	
"		20652104	F10	(3)	2	20J 033	21J 033								
1		10652320	F21	(0)	1	11J 036	12J 36	320	622	618	610		4 622	ALIIXM A	
		20652272		101	2	12J 036	14J 036			618	61	320	0 622	ALIIXM A	
	,	10652248		(0)	1	14J 036		248		2_618		<u> </u>	2_622	ALIIXM A	
1		20652224		(0)	2	15J 036	16J 036	224	622	618		B 24	8 622	ALIIXM A	
["]				(0)	ī	16J 036	17J 036	200	622	2 618	61	B 22	4 622	ALIIXM A	
[]		10652200		(0)	,	17J 036	18J 036	176	62	2618	61	820	0_622	ALIIXM_A_	
1 12		20652176		(0)	1	18J 036		152	2 62	2 618	61	8 17	6 622	ALIIXM A	
[1]		10652152		(0)	2	19J 036		128	3 62	2 618	61		2 622		
111		20652128		(0)	í	20J 036		104	4 62	2 618	61	8 12	8 622	ALIIXM A	
34		10652104										9 24	4 626	ALIIXM A	
	1	20652320	F2	(1)	2			320	0 62	0 022	. 02	2 22	7 040 7 494		
T,	1	10652272	and the second second	(1)	1	12J 037		21	4 62	0 044	- 02	2 22	0 626		
Ľ.	J	20652248		(1)	2	14J 037	15J 037	241	n 62	0 624	. 02	4 61	2 626	DESERTE A	

1 1	REFERENCE	MACH	LIST# NAME	Z	FROM	TO	AX	AY	AAIV	VIAB	вх	BY	SYSTEM REV	-
10	A "	10652224	F2(1)	1	15J 037	16 7	224		(22	422	248	6.26	A1 1 T V M A	
ं कं		20652200	F2(1)	— } —	16J 037	175 37			622 622				ALIIXM A ALIIXM A	- ┖
~! :!		10652176	F2(1)	ī	17J 037	18J 037			622				ALIIXM A	3
1: i	• •	20652152	F2(1)	•		19J_037			_622				ALIIXM_A_	1.
- 1.		10652128	F2(1)	1	19J 037	20J 037	128		622				ALIIXM A	3
11	•	20652104	F2(1)	ž	20J 037	21J 037			622				ALIIXM A	ļ:
¥ 1.	•													j.
2 1		10652320	F2(1)*	1	11J 029	12J (29	320	594	590	590	344	594	ALIIXM A	
- (j. 1)-	•	20652320	F2(2)	2	11J 038	121 038_	320	630	626	626	344	630	ALIIXM A	 -(
- V U-		10652272	F2(2)	1	12J 038	14J 038			626			630	ALIIXM A	()
7 10		20652248	F2(2)	2	14J 038	15J 038			626				ALIIXM A	l'
ANTERNIERA Parente como		10652224	F2(2)	1_	15J 038	16J_038	224_	_630_	626	626	248	630_	ALIIXM A	
		20652200	F2(2)	· 2	16J 03B	17J 038	200	630	626	626	224		ALIIXM A	
- 51		10652176	F2(2)	1	17J 038	18J 038		630	626		200		ALIIXM A	
43		20652152	F2(2)		18J_038_	<u> 191 038 </u>			_626_				ALIIXM_A_	
		10652128	F2(2)	1	19J 038	20J 038			626		152		ALIIXM A	21
		20652104	F2(2)	2	20J 038	21J 038	104	630	626	626	128	630	ALIIXM A	22
-		10652272	F2(3)	1	11J 039	14J 039	272	634	630	630	344	634	ALIIXM A	
	•	20652248	F2(3)	2	14J 039	15J 039			630			634	ALIIXM A	
'n	•	10652224	F2(3)	-1	15J 039	16J_039			_630_			634_	ALIIXM_A	i i
В		20652200	F2(3)	2	16J 039	17J 039			630			634	ALIIXM A	:
23		10652176	F2(3)	1	17J 039	18J 039		634		630	200	634	ALIIXM A	
- _		20652152	F2(3)	2	18J 039	19J 039	152	634		630	176	634	ALIIXM A	37
1.3	÷	10652128	F2(3)	1	19J 039	507 038	128	634	630	630		634	ALIIXM A	133
10	•	20652104	F2(3)	2	501 039	21J 039	. 104	634	630	630	128	634	ALIIXM A).
27		10653296	F2=10#	1	11J 051	13J 051	296	682	678	678	344	682	ALIIXM A) i
x	•	20653312	F2=11*	2	11J 116	12J 116	312	698	694	694	336	APA	ALIIXM A	39
- -		10653288	F2=11*	$\frac{1}{1}$	12J 116	13J 116	288	698	694	694	312	698	ALIIXM A	41
, bi							_							142
ß -		20631492	H-LATCH	2	08J 062	<u>09J 062</u>	492	418	414	414_	516	418_	ALIIXM A	
35 32		10652392	INTIO	1	08J 031	09J 026	392	582	0	586	416	602	ALIIXM A	4
		20652312	IR	2	11J 071	12J 071	312	518	514	514	336	518	ALIIXM A	.50
"_	•	10652320	IR(00)*	_1_	117 010	12J 010	320_	518	514	514	344	518	ALITXM A	
11 11		20652320	IR(01)#	2	113 011	12J 011	320	522	518	518	344	522	ALIIXM A	24 34 37 31
		10652320	IR(02)*	1	11J 012	12J 012	320	526	522	522	344	526	ALIIXM A	27
11		20652320	JR(03)*	2	11J_013_	12J_013	320_	_530_	_526_	_526_	_344_	_530	ALIIXMA	
		10652320	IR(04)*	1	11J 014	12J 014	320	534	530	5 3,0	344	534	ALIIXM A	63
	•	20652320	IR(05)*	2	11J 015	12J 015	320 /	538	534	534	344	538	ALIIXM A	-
' ·		10652320	IR(06)#	_1_	11J 016	12J 016	320	542	538	538_	344	542	ALIIXM A	
. 13		20652312	IR(07)*	2	11J 078	12J 078			542				ALIIXM A	. 20 20 21
* -		10652320	IR(08)*	1	11J 018	12J 018	320	550	546	546	344	550	ALIIXM A	73
(j s -		20652320	IR(09)*	2	. 11J 019	127 018						554		

	10150011				a a water a a				·					ant	
	10653264	KDATA*	<u> ţ</u>	<u> </u>	-14 P 1			674				ALIIXM		-1	
	20653096	KDATA*	2	14J 111	21 1	96	678	674	674	264	678	ALIIXM	A·	J	
•	10,005.00	10 10 10 10 10						_					_		
	10622538	KSTAC*	<u>-</u>		11J_028	,538 <u>.</u>	532	0	_ 536 .	590 _	556	ALIIXM			
•	20652104	KSTAC*	2	11J 028	21J 028	104	590	586	286	344	590	ALIIXM	A		
	10/5000/	*	_					470					٠.		
	10653096	KSTAT*	1	117 115	21J 112	96	682	678	6/8	336	682	ALIIXM	_A		
	20152104	VCVCCIV	-	121.007	23 1 007	104	E 0.4	F 0.7	E 0.3	201	• • •	44 7 7 7 7 14			
	20652104	KSYSCLK	2	13J 007	21J 007	104	206	502_	202	290	500	ALIIXM	А		
	10622526	KWDTAC*		10J 012	11J 025	526	532	ō	536		664	ALIIXM	<u>-</u> -		
	20652104	KWDTAC*	2	11J 025	213 025	104			574			ALIIXM			
	20032104	KIID I AC	L	113 025	213 023	104	7.0	217	217	277	۱.۵ ز	VETIVA	^		
	10652320	LALUCO	1	11J 049	12J 049	320	574	670	670	344	674	ALIIXM	Δ		
			•			. 525	• • •	4.0		•	V. 1 1	7.2.			
	20652320	LCY8*	2	11J 048	12J 048	320	670	666	666	344	670	ALIIXM	A		
											_=				-
	10652320	LOADL	1	11J 007	12J 007	320	506	502	502	344	506	ALIIXM	Α		
	20652312	LOADRX	2	11J 068	12J 068	312	506	502	502	336	506	ALIIXM	A		
													•		
	10652312	LOADT*	_1_	11J 069	12J 069	312	510	506	506	336	510	ÁLIIXM	<u> </u>		
**			•												
	20652320	LSH1*	2	11J 046	12J 046	320	662	658	658	344	662	ALIIXM	A		
	30/22/25	14.5		001000										· · · · · · · · · · · · · · · · · · ·	
	10622522	MAR-	1	09J 038	11J 072	522	564	512	0	630	508	ALIIXM			
• :	20652336	MAR-	2	11J 072	07J 072	336	522	518	>18	432	525	ALIIXM	A		
	10652336	MD-*	1	07J 073	11J 073	221	- E 2 /	522	E 2 2	133	537	AL TYVII			
•	10074330	riu-#	4	013 013	113 073	220	220	222	244	432	240	ALIIXM	A		
	20622482	ME-	2	07J 062	09J 024	482	044	. ^	472	574	500	ALIIXM	A		
	20022702	114		010 002	070 024	702	770		714			VETTVO			
1	10652456	MIARC	1	05J 075	06J 075	456	534	530	530	. 480	534	ALIIXM.	Δ		
	20652432	MIARC	2	06J 075	073 075		534			456		ALIIXM		•	
	10652384	MIARC	1	07J 075	09J 075	384	534	530	530		534	ALIIXM			
	20652288	MIARC	2	09J 075	13J 071	288		0	522	384		ALIIXM			
			, -		77.			-		:		•••••	••		
:	10652416	MISYSCLK	1	07J 034	08J 034	416	614	610	610	440	614	ALIIXM	A		
•	20652392	MISYSCLK	2	08J 034	09J 037	392		618	0	416	614	ALIIXM			
	10622510	MISYSCLK	1	09J 037	13J 069	510	612		0	626		ALIIXM			
		,	:												
•	20622486	MRTAC*	2	09J 063	11J 109	486	516	0	520	670	564	ALIIXM	Α		
						-									
1	10622502	NERROR	1	08J 067	09J 116	502	492	. 0	496	698	516	ALIIXM	A		
!	20/62000	WEBS 454	_	001000	001.000				,						
	20652392	NERRSEL	2	08J 028	09J 028	392	590	586	586	416	590	ALIIXM	<u>A</u>		
	10462244	NEUTIOCL		101.114	1/ 1 105	~		^							
	10652264	NEXT (05)*		10J 116	14J 105		654		658	360	698	ALIIXM			
	20652240 10652216	NEXT (05)* NEXT (05)*		14J 105 15J 105	15J 105 16J 105	240		650	650		654	ALIIXM			
i	20652192	NEXT (05)*		16J 105			654	650	650	240	654	ALIIXM		•	
•	10652168	NEXT (05)*			17J 105		654	650	650		654	ALIIXM			
	20652144	NEXT (05)*		17J 105 18J 105	18J 105 19J 105		_654 _654	_650_ 650	650_ _650	168	654 654	ALIIXM			
	10652120	NEXT (05)*		19J 105	20J 105		654	650	650	144		ALIIXM ALIIXM			
	20652096	NEXT (05)*		20J 105	211 105	96	654	650	650	120	654 654	ALIIXM			
	20072070	HEAT TOJI"		<u> </u>	210 100	70	4-رن	020	010	120	024	WEIIVW	_^_		
		NEW#1015		101117	16 1 106	24.1	(5 0	0	662	360	702	ALIIXM	٨		
ē	10652264	NEXT (OP)*		103 117	147 1117	764	กาห								
•	10652264 20652240	NEXT (06)* NEXT (06)*		10J 117 14J 106	14J 106 15J 106	264 240	658		654	-		ALIIXM			

REFERENCE	MACH= L1	ST= NAME Z	FROM	TO	AX	AY	VIAA	VIAB	ВА	BY	STSIEM REV .	
1		HEVTIALLS &	16J 106	173	192	658	_654_	654	216_	658	_ALIIXM_A_	_(_
	20652192	NEXT (061 * 2		183	168		654	654	192	658	ALIIXM A	
	10652168	NEXT(06)* 1	17J 106				654				ALIIXM A	
. et •	20652144	NEXT (06) * 2	18J 106	19J 106	144	(50	654_	464	144	65R	ALIIXM A	
	10652120	NEXT (06) # 1	<u>19J106</u>	_501_106	120	.Q?Q_	_ 92 1	_S.J.B	7 2 T T T T T	_U / U	ALIIXM A	
	20652096	NEXT(06)* 2	20J 106	21J 106	96	658	654	624	120	020	VETTVII V	
	10652264	NEXT(07)* 1	10J 118	14J '07	264			666			ALIIXM_A	
	20652240	NEXT(07)* 2	14J 107	15J 107	240	662	658	658	264		ALIIXM A	
	_	NEXT (07) * 1	15J 107	16J 107	216	662		658	240		ALIIXM A	
•	10652216	NEXT (07)* 2	16J 107	17J 107	192	662	658_	_658	216_	_662	ALIIXM A	
	20652192	NEXT (07) * 1	17J 107	18J 107	168		658		192	662	ALIIXM A	
	10652168		18J 107	19J 107	144		658	658	168	662	ALIIXM A	
	20652144	NEXT(07)* 2		20J 107					144		ALIIXM A	
	10652120	NEXT (07)* 1	19J 107		96	662		658	120	662	ALIIXM A	
	20652096	NEXT (07) * 2	20J 107	21J 107	70	1002	070	0,70				
							_	(70	260	710	ALIIXM_A_	
•	10652264	NEXT (08) * 1	107_118_	14J 108			0_		_3 2 Y_	_1.5 \(\cdots	ALIIXM A	
	20652240	NEXT (08) # 2	14J 108	15J 108	. 240	666	662	662		666		
	1065221	NEXT(08)* 1	15J 108	16J 108	216	666	662	662	240	666	ALIIXM A	
•	20652192	NEXT(08)* 2	16J 108	17J 108	192	666	662				ALTIXM A	
		NEXT (08) * 1	17J 108	18J 108	168	666	662			666	ALIIXM A	
	10652168		18J 108	19J 108	144	666	662	662	168	666	ALIIXII A	
	20652144	NEXT (08) # 2	19J 108_	20J 108	120	666	662	662	144	666	ALIIXM_A_	
	10652120	NEXT (08) * 1			96	666		662	120	666	ALIIXM A	
	20652096	NEXT (08) * 2	203 109	719 100	70	500						
				101 200	200	470	0	67/	340	714	ALIIXM A	
	10652288	NEXT (09) * 1	10J 120	131 109	288	670		666	288	670	ALIIXM A	
	20652264	NEXT (09) * 2	13J 109	14J 109	264	670					ALIIXM A	
	10652240	NEXT(09)* 1	14J 109	15J 109	240	670		666		670		
	20652716	NEXT (09) * 2	15J 109	16J 109	216					_670_	ALIIXM A	
	10652192	NEXT (09) * 1	16J 109	17J 109	192	670	666	666		670	ALIIXM A	
		NEXT(09)* 2	17J 109	18J 109	168	670	666	666		670	ALIIXM A	
	20652168		18J 109	19J 109	144	670		666	168	670	ALIIXM A	
	10652144	NEXT (09)* 1	19J 109	20J 109	120	670		666	144		ALIIXM A	
	20652120	NEXT (09) * 2	-		96	670		666	120		ALIIXM A	
ĺ	10652096	NEXT(09) * 1	20J 109	5j7 109	70	010	000	500	120	- 10	• · · · · · · · · · · · · · · · · · · ·	•
1						F 0 0			408	486	ALIIXM A	
	20652392	NFETCH 2	08J 063	09J 006	392	502	490	0	400	400	Vettivii V	
Ì								_	,	,	ALTTVM A	
1	10622538	NRSTERR 1	08J 114	09J 076	538	516	496	0	690	492	ALIIXM A	
	100:1330											
	20(21(12	NSTART 2	05J 001	06J 001	412	418	414			418	ALIIXM A	•
1	20631412		06J 001	07J 001	436	418	414				ALIIXM A	
	10631436	NSTART 1		08J 001	460				484		ALIIXM A	
:	20631460	NSTART 2	07J 001		482				646		ALIIXM A	
:	10622482	NSTART 1	087 001	09J 042	402	404	9	-,00	J . J			_
		at 100 at			202	7 7 7	710	710	416	722	ALIIXM A	
	20653392	NTESTMODE 2	08J 061	09J 061	392	122	118	110	- 10	122	WE 1 1 W. 1	
									,	= 0.0	AL STVM A	•
نی	10652392	ODD1_	08J_03Q_	09J_030	392	598	594	594_	410	ひりは	ALIIXMA_	
ł ————												
	20652272	OKOTORUN 2	10J 001	14J 011		522	486		368		ALIIXM A	
1		OKOTORUN 1	14J 011	15J 011	248					522	ALIIXM A	
	10652248	OKOTORUN 2	- i šj-čii-	16J 011	224				248	522	ALIIXM A	
· •	20652224		16J 011	17J 011	200				224	522	ALIIXM A	
· j	10652200	OKOTORUN 1		18J 011	176						ALIIXM_A	
s	20652176	OKOTORUN 2	17J_011_		152				176		ALIIXM A	
::	10652152	OKOTORUN 1	18J 011	19J 011							ALIIXM A	•
::	20652128	OKOTORUN 2	19J 011	20J 011	128						ALIIXM A	
	10652104	OKOTORUN 1	20J 011	21J 011	104	522	518	518	128	222	VETTVII V	
		The state of the s									41 1 1 1 1 1 1	
1	20622542	PARTAC* 2	09J 098	10J 016	542			0		516	ALIIXM A	
	FOO#F74#	PARTAC* 1	10J 016	11J 061	542	532	2 0	691	722	556	ALIIXM A	

REFER" TE	MACH=	LIST# NAM	1E Z	FROM	To	AX AY		VIAB		ВҮ	STSIEM RE	. v	·)
	20653392	POK	2	08J 115	09.	392 694	690_	690	408	694	ALIIXM	· · ·	1
-						552 518	514	514	576	518	ALIIXM A	4	
	10652552	RASIO) 1	01J 071	02J 071	502 510	614	514	552	518	ALIIXM		i
	20652528	RASIO	12_	02J_071_	037 071			514	528	518	ALIIXM		
ساير علأاءا الرابط ووالرواب معصبي	10652504	RASIO	1	03J 071	04J 071	504 518		. ,		508	ALIIXM		
	20621518	RASIO) 2	04J 071	09J 044	518 396	U	400	024	,,,,			
							546	546	576	550	ALIIXM	A	
	10652552	RAS(1	1	01J 079	02J 079	552 550			552		ALIIXM		
	20652528	RAS(1		02J 079	031 079	528 550	546	546_	50a	550	ALIIXM		
	10652504	RAS(1) 1	03J 079				246	504	550	ALIIXM		
	20652392	RAS(1		04J 079	09J 046	392 662	554	0	504	,,,			
	20072372							590	676	594	ALIIXM	A	
	10652552	RAS12	1	01J 090	057 060	552 594			552	504	ALIIXM		
	20652528	RAS (2		02J 090	03J 090	528 594 504 594	590				ALIIXM		
	10652504	RAS(2		03J 090	04J 090	504 594	590	230	504	504	ALIIXM_		
	20652392	. RAS (2	_	041 090	09J_047	392 666	- 3 A B		2.93	_2 2 ·L			
								574	576	578	ALIIXM	Α	•
į ·	1065255.	RAS (3	1	01J 086	02J 086	552 578			552		ALIIXM		
	20652528	RAS (3		02J 086	037 086	528 578			528	578	ALTIXM		
	10652504	RASIS		03J 086	04J 086	504 578			504		ALIIXM		
	20652392	RAS (3		04J 086	09J 048	392 670	582	0	904	,,,	,		
	20032334		•					638	576	642	ALIIXM	A	
	10652552	RAS (4	1	01J 102	02J 102	552 64	2 020	638		642	ALIIXM		
	20652528	RAS (4		02J 102	03J 102		2 638		528		ALIIXM		
	10652504	RASIA				504 64	2 630	638	504	642	ALIIXM		
<u> </u>	20652392	RASIA		04J 102	09J 049	392 67	4 646	U	504	042		•	
	10071771			•		552 65	n es.	454	676	658	ALIIXM	A	
	10652552	RAS (5) 1	01J 106		552 65	0 24		552	- 658	ALIIXM		
ļ	20652528	RASI	5) 2			528 65				658	ALIIXM		
j	10652504	RAS (03J 106		504 65	8 654		504	658	ALIIXM		
.]	20652392	' RASI	5) 2	04J 106	09J 050	392 67	8 662	0					
						552 67	0 (76	674	576	678	ALIIXM	Α .	
	10653552	RASI	6) 1			552 67	0 (7)	47/			ALIIXM	A	
	20653528	RASI	61 2	027 111		<u>528 67</u>			528	678	ALIIXM		
	10653504	RASI				504 67				678	ALIIXM		
	20653392	RASI		04J 111	09J 051	392 68	2 614	674	204	0.0			
	20073772						436	626	676	630	ALIIXM	A	
*	10652552	RASI	71 1	01J 099		552 63				630	ALIIXM		
*1	20652528	RASI				528 63		626	528		ALIIXM		
^ .	10652504	RASI	_	031 099	04J 099	504 63			504	630 630	ALIIXM		
1	20652392	5.6.		04J 099	09J 052	392 68	16 634	, 0	204	0.30	NET IN.	••	
" }	20072372								204	702	ALIIXM	Α .	
41	10653336	. READ	MOUSE 1	09J 117	1 11J 117	336 70	12 698	8 698	304	102			
"	10033330								E 0.9	434	ALIIXM	Α	
*1	20631460	RESE	T 2	2 07J 002	2 09J 002	460 41	14 410	0 410	500	616			
11	10631508	0505		09J 002	10J 002			0 410		-414 -414	ALIIXM		
"	20631532	·		101 00:	2 11J 002	532 4					ALIIXM		
"	10631556			111 00			14 41		628		ALIIXM		
["]			- •	2 14J 00	2 15J 002	628 4			652		ALIIXM		
*	20631628 10631652			1 15J 00			14 41		676		ALIIXM		•
n		2525	-	2 16J 00	2 17J 002		14 41		700				
1-1	20631676			1 17J 00			14_41			414	ALIIXM		
IF	10631700			18J 00	2 19J 002		14 41		748		ALIIXM		
1 1	20631724			1 19J 00			14 41						
**	10631748			2 20J 00		772 4	14 41	0 410	796	414	ALIIXM		
3+	20631772	IL 31								621	ALIIXM	Δ	
I. I	10/69/00	RMO(0.#	1 01J 07	5 05J 026		82 53						
["]							0.7 6.7	0 670	488	582	ALLIAN		
5c	10652488 20652464		=	2 05J 02	6 06J 026	464 5 440 5	82 57 82 57	8 578 8 578				Δ	

REFER	MACH-	LIST	NAME	Z	FROM		AX	AY	VIAA	VIAB	BX	BY	SYSTEM	REV .	
<u> </u>	2065241	5	RM00*		07J 026	083 26	416	582	578	578	440	582	_ALIIXM	_A	
	1065248	a	RM01*	1	02J 075	05J 025	488	578	538	0	552	534	ALIIXM	A	
•	20652464		RM01*	,	05J 025	06J 025			574		488		ALIIXM		
	10652440		RM01*	1	06J 025	07J 025	440	578	574	574	464	578	ALIIXM	A	
	2065241	-	RM01*	2	07J 025	08J 025	416	578	574	574	440	578	ALIIXM		
	1065248	3	RM02*	1	03J 075	05J 024	488	574	538	0	528	534	ALIIXM	A	····
•	2065246		RMO2*	2	05J 024	06J 024		574			488		ALIIXM		
	1065244		RM02#	1	06J 024	07J 024			570				<u>ALIIXM</u>		
	2065241	6	RM02*	2	07J 024	08J 024	416	574	570	570	440	574	ALIIXM	A	
	1062153		RM03*	1 '	04J 075	05J 023	534		0	400		412	ALIIXM		
	2065246		RM03*	2	05J 023	06J 023			566	566		570	ALIIXM		
	1065244		RM03*	1	06J 023	07J 023	440	570	566	566		570	ALIIXM		
	2065241	6 .	RM03*		07J 023	08J 023	415_	_5 (O'	566	555	440	_5 £Q	_ALIIXM	Α	
	1065248		RM04*	1	· 01J 073	05J 022		566	530	0		526	ALIIXM		
	2065246		RM04 *	2	05J 022	06J 022		_566_		562		566	ALIIXM	,	
	1065244		RM04#	1	06J 022	07J 022		566	562	562	464		ALIIXM		
	2065241	6	RM04#	2	07J 022	08J 022	416	566	562	562	440	566	ALIIXM	A	
	1065248		RM05*	1	02J 073	05J 021	488		530	0	552		ALIIXM		
	2065246		RM05 *	. 2	05J 021	06J 021	464	562	558	558	488	562	ALIIXM		
	1065244		RM05*	<u>l_</u>	06J 021	07J 021		562		558		562	ALIIXM		
	2065241	6	RM05*	2	07J 021	08J 021	416	562	558	5 58	440	562	ALIIXM	A	
	1065248		RM06*	1	03J 073	05J 020		558				526	ALIIXM		
	2065246		RM06#	2	05J 020	06J 020	464	558	554	554	488		ALIIXM		
•	1065244		RM06*	j	06J 020	07J 020		558	554	554	464		ALIIXM		
	2065241	6	RM06*	2	07J 020	087 050	416	558	554	554	440	558	ALIIXM		
	1062152		RM07*	1	04J 073	05J 019	526	396	0	400		412	ALIIXM		٠.
	2065246		RMO7*	2		06J 019			_550_			554_	_ALIIXM		
	1065244		RM07*	. 1	06J 019	07J 019	440	554	550	550	464	554	ALIIXM		
	2055241	6	RM07*	2	07J 019	019 U80	416	554	550	550	440	554	ALIIXM	Α	
	1065248	J	RMO8 *	1	01J 084	05J 018	488	550	0	554	576	570	ALIIXM		
	2065246		RM08#	2	05J 018	06J 018	464	550	546	546	488	550	ALIIXM		
	1065244		RM08*	1	06J 018	07J 018	440		546	546		_550	ALIIXM		
	2065241	6	RMO8*	2	07J 018	08J 018	416	550	546	546	440	550	ALIIXM	A	
	1065248	8	RM09*	1	02J 084	05J 016	.488	542	0	546	552	570	ALIIXM	Α	
	2065246	4	RM09*	2	05J 016	06J 016	464	542		538	488	542	ALIIXM	A	
	1065244		RM09*	1	06J 016	07J 016	440		538		464		ALIIXM		
· · · · · · · · · · · · · · · · · · ·	2065241	6	RM09*	2	07J 016	067_016	416_	542	_538_	538	440	5.42	_ALIIXM	A	
	1065248	8	RM10*	1	03J 084	05J 015	488	538	0	542	528	570	ALIIXM	A	
-	2065246		RM10*	2	05J 015	06J 015			534			538	ALIIXM		
	1065244		RM10#	ī	06J 015	07J 015			534		464	538	ALIIXM		
İ	2065241	6	RM10*	2	07J 015	08J 015	416	538	534	534	440	538	ALIIXM	Α	
	1062153	4	RM11*	<u>1</u>	04J 084	05J 014	534	412	400	0	570	396	ALIIXM	A	
	2065246		RM11#	2	05J 014	06J 014			530			534	ALIIXM		
	1065244		RM11*	ī	06J 014	07J 014			530			534	ALIIXM		
	2065241		RM11*	2	07J 014	08J 014			530			534	ALIIXM		
	1065248	A	RM12*	1	01J 081	05J 013.	ሬ ጸል	530	0	534	576	5:5 A	ALIIXM	A	
	2065246		RM12*	1	057 013	067 013	700	- 530	526	524	7.88		ALIIXM		

REFEREN S	MACH#	LIST=	NAME	Z	FROM	TO	AX	AY	VIAA	VIAB	BX	BY	SYSTEM RE	∠ *
•			0411.04		06J 013	07J	440	530	526	526	464	530	ALIIXM A	1
	10652440 20652416		RM12# RM12#	2	-073 013-	08J C.3	416	530		526	440	530	ALIIXM A	
					02J 081	05J 012	488	526	0	530	552_	558	ALIIXM_A	:
	10652488		RM13*	<u>‡</u>		06J 012	464	526	522	522	488	526	ALIIXM A	
	20652464		RM13*	2	05J 012				522	522	464	526	ALIIXM A	
	10652440		RM13*	1	06J 012	07J 012	440	526				526	ALIIXM A	
	20652416		RM13*	2	073 012	08J 012	416	526	522	522	440	220_	ALIIM:	
	10652488		RM14*	1	03J 081	05J 011	488	522	0	526	528	558	ALIIXM A	
	20652464	1	RM14*	2	05J 011_	_067 011	464	. 522	_518_	_518_	_488_			
	10652440		RM14*	ì	06J 011	07J 011	440	522	518	518	464	522	ALIIXM /	
	20652416		RM14#	2	07J 011	08J 011	416	522	518	518	440	522	ALIIXM A	•
	10/21519		RM15*		04J 081	05J 010	518	412	400	0	558	396	ALIIXM /	
	10621518		RM15*	2	05J 010	06J 010	464	518	514	514	488	518	ALIIXM /	\
	20652464			ž	06J 010	07J G10	440	518	514	514	464	518	ALIIXM /	\
	10652440		RM15*				416	518	514	514	440	518	ALIIXM	\
	20652416		RM15*	2	07J 010	087 010	410	314	J	714				
	10652416	•	RM16#	1	01J 094	800 L80	416	510	0	514	576	610	ALIIXM	<u>'</u>
	20652416		RM17*	2	02J 094	08J 007	416	506	0	510	552	610	ALIIXM	۸ ۰
	10652416		RM18*	1	03J 094	08J 006	416	502	0	506	528	610	ALIIXM	1
700			RM19#	2	04J 094	08J 002	486	484	400	0	610	396	ALIIXM	4
	20621486		Kul A.								67/		AL TIVM	:
4	10652408		RM20*	, . 1 .	01J 092	08J ^79	408	550	0	554	576		ALIIXM	
	20652408		RM21*	2	02J 092	08J 078	408	546	0	550	552	602	ALIIXM	Α
•	10652408		RM22#	11_	03J 092	08J 077	408	542	0	546	528	602	ALIIXM	<u> </u>
	20652408		RM23*	. 2	04J 092	08J 076	408	538	. 0	542	504	602	ALIIXM	A
	10652408		RM24*	1	01J 097	08J 075	408	534	0	538	576	622	ALIIXM	A
			RM25#	2	02J 097	08J 074	408	530	0	534	552	622	ALIIXM	A
	20652408		RPIZ 3 =						,				ALIIXM	Α .
•	10652408	,	RM26*	1	03J 097	08J 073	408	526	0	530				
	20621522		RM27*	2	04J 097	08J 072	522	492	400	0	622	396	ALIIXM	A
	10652408		RM28*	1	01J 103	08J 071	408	518	0	522	576	646	ALIIXM	Α
	20652408		RM29*	2	02J 103	08J 070	408	514	. 0	518	552	646	ALIIXM	A
						08J 069	510	492	376	• 0	646	372	ALIIXM	A
	10621510		RM30*	1	03J 103			•						
	20621506		RM31*	2	04J 103	08J 068	506	492	400	0	646	396		
	10652408		RM32*	1	01J 109	08J 116	408	698	674			670		
	20652408		RM33*	2	02J 109	08J 117	408	702	674	0	552	670	ALIIXM	A
	10652408	i	RM34*	1	03J 109	08J 118	408	706	674	0	528	670	ALIIXM	A .
	20652408		RM35*	2	04J 109	08J 119	408	710	674	0	504	670	ALIIXM	A

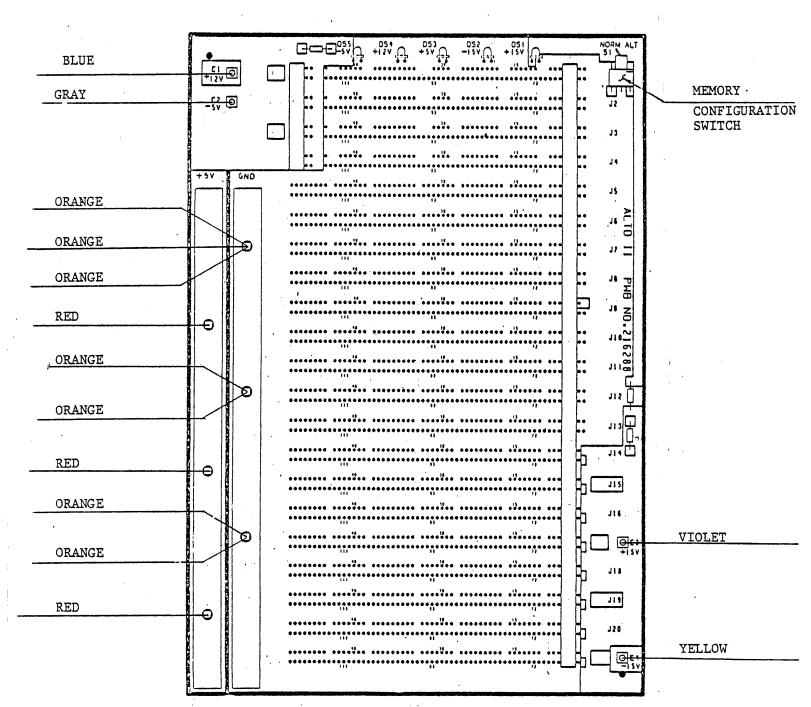
REFERENCE	MACH=	LIST=	NAME	Z	FROM	T 0	AX	AY	VIAA	VIAB	вх	BY	SYSTEM REV	· ·
	2065340 <u>8</u>		RM37*	2	02J 113	<u>OB</u> 1	408	718_	690	0_	552	_686	ALIIXM A	_(_
	10653408		RM38*	j	03J 113	08J 122	408	722	690	0	528	686	ALIIXM A	3
	20622602		RSELO	2	09J 031	11J 054	602	508	0	512	694	556	ALIIXM A	1
3	10653320		RSELO	1	11J 054	12J 054	320	694	690	690	344	694	ALIIXM A	7
,	20622606		RSEL1	2	09J 032	11J 055	808	508	0	512	698	556	ALIIXM A	12
	10653320		RSEL1	1	11J 055	12J 055	320	698	694	694	344	698	ALIIXM A	
8	20622610		RSEL 2	2	09J 033	11J 056	610	508	0	512	702	556	ALIIXM A	
11	10653320		RSEL2	1	11J 056	12J 056	320	702	698	698	344	702	ALIIXM A	11
	20622614		RSEL3	2	09J 034	11J 057	614	508	0	512	706	556	ALIIXM A	11
•]	10653320		RSEL3	1	11J 057	12J 057	320	706	702	702	344	706	ALIIXM A	a) 7:
	20622622		RSEL4	2	09J 036	11J 059	622	508	0	512	714	556	ALIIXM A)
37	10653320		RSEL4	1	. 11J 059	12J 059	320	714	710	710	344	714	ALIIXM A	1
12	20652320		RSH1#	2	11J 047	12J 047	320	666	662	662	344	666	ALIIXM A	:
N	10652096		RMRCLR*	1	107 072	21J 073	96	_526_	518	518	_360_	_522_	ALIIXM A	
n n Mari	20652336		SELR37	2	09J 013	11J 074	336	530	526	526	392	530	ALIIXM A) 1
	10653320		\$H00	1	11J 050	12J 050	320	678	674	674	344	678	ALIIXM A	,
#1 #1	20652320		SHZERO	2	11J 026	12J 026	320	582	578	578	344	582	ALIIXM A	
.# 27	10652248		S10*	1	14J 041	15J 041	248	642	638	638		642	ALIIXM A)
y	20652224		510*	2	15J 041	16J 041	224	642		638	248		ALIIXM A	
:	10652200		\$10*	1	16J 041	17J 041	200	642		638	224		ALIIXM A	
w.	20652176		\$10*	2	17J 041	18J 041	176		638	638	200	642	ALIIXM A	ľ
<u> </u>	10652152		\$IO*	<u>l_</u>	18J_041_	19J 041	152					642	ALIIXM A	
25	20652128		510*	2 .	19J 041	20J 041	128	642	638	638	152	642	ALIIXM A	
3:	10652384		SRESET*	1	07J 006	09J 079	384				440		ALIIXM A	
	2065236		SRESET*	2	09J 079	10J 019	368	554	546	546	384		ALIIXM A	[;
	10622482		SRESET*	1	10J 019	11J 001	482	556	536	0	554		ALIIXM A	ŀ
	20652264		_SRESET*	2	117_001	14J 070	264_	514.	486_			_482_	ALIIXM_A_	
7	10652096		SRESET*	1	14J 070	21J 070	96	514	510	510	264	514	ALIIXM A	
47	20652296		STOP*	2	09J 074	13J 001	296	482	0	486	384	530	ALIIXM A	
	10652296		STOPCLK	1	10J 020	13J 020	296	558	554	554	368	558	ALIIXM A	
1	20652392		STORE*	2	08J 029	09J 029	392	594	590	590	416	594	ALIIXM A	
e e	10622530	•	STORE#	ī	09J 029	07J 074	530	468	0	472	594	508	ALIIXM A	
[* 	20622506		SWAKMRT	. 2	09J 039	13J 068	506	612	512	0	634	508	ALIIXM A	
	10652264		SWAKMRT	1	13J 068	14J 068	264	506					ALIIXM A	
[:	20652248		TASKA*	2	10J 013	15J 013	248		526	526		530	ALIIXM A	
a	10652224		TASKA*	1	15J 013	16J 013	224	530		526			ALIIXM A	•
:•	20652200		TASKA*	2	16J 013	17J 013	200	530			224		ALIIXM A	
22	10652176		TASKA*	1	17J 013	18J 013	176	530		526			ALIIXM A	·
= .	20652152		TASKA*	2	18J 013	19J 013	152				176		ALIIXM A	,
V	10652128		TASKAN	1	19J 013	20J 013	128	530	256	526	122	530	ALIIXM A	

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REFERENCE	MACH=	LIST=	NAME	ž	FROM	TO MI	ĄX	ĄΥ	VIAA	RAIN	BX	BY	SYSTEM F	REV
	20653288		WAKE CURT*	2	111 115	135	288	694	690	690	336	694	ALIIXM	A(
	10652288		WAKEDHT*	1	11J 077	13J 077	288	542	538	538	336	542	ALIIXM	A
	20652288		WAKEDVT*	_ 	11J 076	13J 076	288	538	534	534	336	538	ALIIXM	A
	10652296		WAKEDWT#	1	11J 021	13J 021	296	562	558	558	344	562	ALIIXM	Α
	20652264		WAXEET#	2	11J 103	14J 103	264	646	642	642.	336	546	ALIIXM	A
	10652104		WAKEKST*	- 1	11J 070	21J 008		510			336		ALIIXM	A
•	20652096	•	WAKEKWDT*	2	11J 079	21J 079	96	550	546	546	336	550	ALIIXM	A
	10652336		WAKEMRT*	 1	09J 010	11J 067	336	502	0	506	392		ALIIXM	•
			WAKEPART#		09J 118	11J 118		706		702	384		ALIIXM	Α
	20653336								646				ALIIXM	
	10652264		WAKE6*	_1_	11J 104	14J 104			040	328	622		ALIIXM	
• ,	20621522 1065246 <u>4</u>		WM(00)*	2 _1	01J 072 05J 036	06J 036	464		618	618	488	622	ALIIXM	Α
	20652440		WM(00)*	2	06J 036	07J 036	440 416	622 622	618 618	618 618	464 440	622 622	ALIIXM ALIIXM	
	10652416		WM(00)*	1	07J 036	08J 036								
	20621522		WM(01)*	2	02J 072	05J 097	522	348 622	0 618	352 618	622 480	420 622	ALIIXM ALIIXM	
	10652456		WM(01)*	1	05J 097	06J 1097 07J 097	456 432	622	618	618			ALIIXM	
	2065243 <u>2</u> 10652408		WM(01)*	1	06J 097 07J 097	08J 097	408	622		618		622	ALIIXM	
	20621522		WM(02)*	2	03J 072	05J 037	522	372	0	376	626	412	ALIIXM	
	10652464		WM(02)*	1	05J 037	06J 037	464	626	622	622	488	626	ALIIXM	
	20652440	•	WM(02)*	2	06J 037	07J 037	440 416	626 626		622 622	464	626 626	ALIIXM	
	10652416		WM(02)#		07J 037	08J 037								
	20621522		WM(03)*	2	04J 072	05J 098	522	396	0	400	626	420	ALIIXM	
	10652456		WM(031*	1	05J 098	06J 098	456	626		622	480		ALIIXM	
	20652432	٠,	WM(03)*	. 2	06J 09B	07J 098	432	626		622	432	626 626	ALIIXM	
	10652408		WM(03)*	1	07J 098	08J 098	408	626	044					
	20621530		WM(04)*	2	01J 074	05J 038	530	324			630		ALIIXM	
	10652464		WM(04)*	1	05J 038	06J 038	464	630		626	488	630	ALIIXM	
	20652440		WM(04)*	2	06J 038	07J 038	440	630			464	<u>630</u> 630	ALIIXM	
	10652416		WM(04)*	1.	07J 038	08J 038	416			, 626	440	030		
	20621530		WM(05)*	2_	02J 074	05J 099	530			352			ALIIXM	
	10652456		WM(05)*	1	05J 099	06J 599	456		626	626		630	ALIIXM	
	20652432		WM1051*	2	06J 099	07J 099	432			626			ALIIXM	
·	10652408		WM(05)#	1	07J 099	08J 099	40B	630	626	626	432	630	ALIIXM	
	20621530		WM(06)#	2	03J 074	05J 039		372			634 488		ALIIXM	
	10652464		WM(06)*	<u></u>	05J 039	06J 039		634 634				<u>634</u>	ALIIXM	
	20652440 10652416		#(60)MW #(60)MW	2	06J 039 07J 039	07J 039 08J 039	440 416			630	_	634	ALIIXM	
				<u> </u>	04J 074	05J 100	530	396	0	400	634	420	ALIIXM	A
	20621530 10652456		WM(07)*	2	05J 100	06J 100	456					634	ALIIXM	
	20652432		WM(07)*	2	065 100	07J 100	432			630			ALIIXM	
	10652408		WM(07)*	$-\frac{2}{1}$	07J 100	08J 100		634				634	ALIIXM	Α

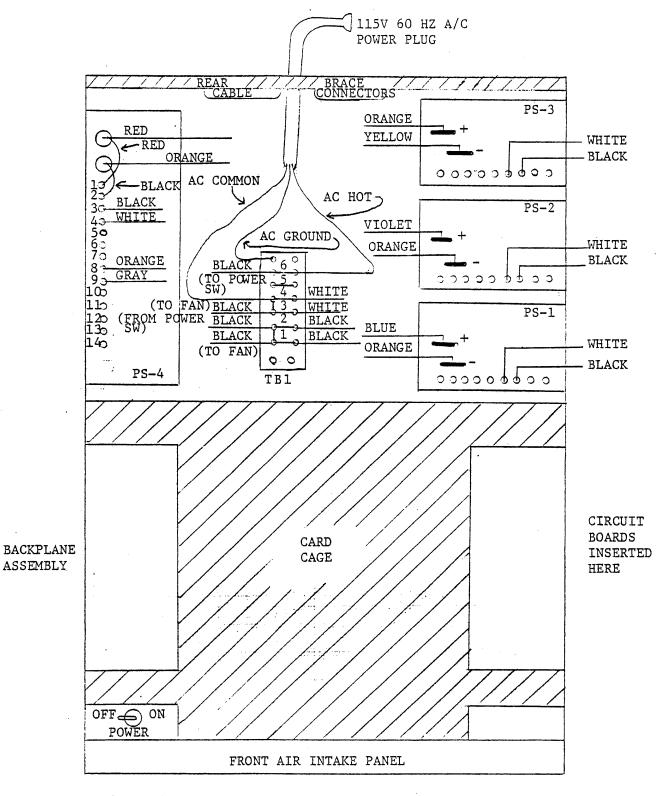
	20652488 10652464 20652440 10652416 20621554 10652456 20652432 10652408	WM10 WM10 WM10	8)*	2 1 2	01J 080 05J 040 06J 040	05 . 06.J 040 07.J 040	488	638_ 638	558 634	634		554 638	ALIIXM ALIIXM		_(-
	10652464 20652440 10652416 20621554 10652456 20652432	WW(0 WW(0 WW(0	8)*	1 2 1	05J 040 06J 040	061 040	464		634	634	488				•
	20652440 10652416 20621554 10652456 20652432	WM(O	81*	2 l	06J 040										
	20621554 10652456 20652432	, MW(0 MW(0		i			440	638	634	634	464	638	ALIIXM	A	
	20621554 10652456 20652432	WM(0	B FW		07J 040	08J 040				634			ALIIXM		
	10652456 20652432				_0/3_040_	083 040		_0.00	-654	_P 24	1.14.	020			
	20652432	LIMATA	91*	2	02J 080	05J 101	554					420	ALIIXM		
	20652432	WMILU	9)*	1	05J 101	06J 101	456	638	634	634	480	638_	ALIIXM	A	
		WMIO		2	06J 101	07J 101	432	638	634			638	ALIIXM	Α	
	10072400	WM(0		- ī	07J 101	08J 101	408	638		634	432	638	At HIXM	Α	
	20/2155/		~ ;		03J 080	05J 041	554	372	0	376	642	412	ALIIXM	Α	
	20621554	WM(1		2					_				ALIIXM		
	10652464	WM(1		1	05J 041	06J 041	464	642	638			642			
• ,	20652440	WM(1		2	06J 041	07J 041		642	638		464		ALIIXM		
	10652416	WM(1	.0)*	1	07J 041	08J 041	416	642	638	638	440	642	ALIIXM	A	
	20621554		1)*	2	04J 080_	05J 102	554	396	. 0	400	642	420	ALIIXM	A	
		WM()		1 .	05J 102	06J 102	456		638			642	ALIIXM		
•	10652456				06J 102	07J 102		642			456		ALIIXM		
	20652432	WM (]		4							432		ALIIXM		
	10652408	WM()	111*	1	07J 102	08J 102	400	044_	638	638	724	<u> </u>	VETTVIA		
	20652488	WM12	2 #	2	01J 083	05J 042	488	646	570		576		ALIIXM		•
1 ,	10652464	WM12		1	05J 042	06J 042	464	646	642	642	488_	646	ALIIXM	A	
	20652440	WM12		<u>2</u>	06J 042	07J 042		646	642			646	ALIIXM	· A	
	10652416	WM12		ĩ	07J 042	08J 042	416	646	642			646	ALIIXM		•
			-		221 000	051100	5//	210		352	646	1.20	ALIIXM		
•	20621566	WM13		2	02J 083	05J 103	566	348	. 0		-	420			
₹ .	10652456	WM13	3 *	1	05J 103	06J 103	456	646	642	642		646	ALIIXM		
•	20652432	WM13	3 #	2	06J 103	07J 103	432	_646_	642	642	456	646	ALIIXM	A	
	10652408	WM1	3 #	1	07J 103	08J 103	408	646	642	642	432	646	ALIIXM	A	
4	20/215//	WM14		•	03J 083	05J 043	566	272	0	376	650	412	ALIIXM	Δ.	
	20621566						464	650	646			650	ALIIXM		
•	10652464	WM14		1	05J 043	06J 043									
	20652440			2	06J 043	07J 043		650		646		650	ALIIXM		
	<u> 10652416</u>	WM14	· *	1	071 043	08J 043	416_	_650_	_646_	_646	440	6.50	ALIIXM	А	
	20621566	WM15	5 #	2	04J 083	05J 104	566	396	0	400	650	420	ALIIXM	Α	
	10652456	WM15		ī	05J 104	06J 104	456	650	646	646	480	650	ALIIXM	Α	
		WM1			06J 104	07J 104		650	646		456	650	ALIIXM		·····
	20652432			4							432		ALIIXM		
	10652408	WM15	*	1 .	07J 104	08J 104	408	650	646	646	434	650	VETTVO		
	20652416	WM1	5#	2	01J 091	08J 044	416	654	602	0	576	598	ALIIXM	Α	
					201 201	001.305	400	654	(03	0	552	5 00	ALIIXM		
 	10652408	WM1	<u> </u>	-	02J 091	08J 105	408	_0,74	602	. 	7,5 %	7 70	AE I I AII	<u> </u>	
	20652416	WM1	8 *	2	03J 091	08J 046	416	662	602	0	528	598	ALIIXM	A	;
	10/52/09	h(14.1)			04.1.001	08J 1J6	408	658	602	· 0	504	508	ALIIXM	Δ	
	10652408	WM1	y*	1.	04J 091	003 100	400	0.50	002	-			AG: IA		
	20652416	WM2	0 *	2	01J 093	08J 047	416	, 666	610	0	576	606	ALIIXM	A	
	10652408	WM2	: 1 #	1	02J 093	08J 107	408	662	610	0	552	606	ALIIXM	A	
,	10032400	WP12	1 -	•											
	20652416	WM2	2*	2	03J 093	08J 048	416	670	610	0	528	606	ALIIXM	A	•
	10652408	WM2	3*	1	04J 093	08J 108	408	666	610	0	504	606	ALIIXM	A	
	20652416	WM2	4#	2	017 101	08J 049	416	674	642	Ü	576	028	ALIIXM	A	
	10652408	WM2	5 W	1	02J 101	08J 109	408	670	642	n	552	63B	ALIIXM	A	

20652416 WH26* 2	REFERE	MACH=	LIST=	NAME	Z	FROM	ro	AX	AY	MAIV	VIAB BX	BY	SYSTEM REV	C'
10652408		20652416		WM26#	2	03J 101	081 05	416	678	642	0 52	8_638_	ALIIXM A	
10652416 MM29* 1 02J 104 08J 11				WM27*	1	04J 101	08J 110	408	674	642	0 50	4 638	ALIIXM A	
20652416		20652416		WM28#	2	01J 104	08J 051	416	682	654	0 57	6 650	ALIIXM A	
10652408		10652408		WM29*	1	02J 104	08J :11	408	678	654	0 55	2 650	ALIIXM A	
10627408 WH31* 1		20652416		WM30*	2	03J 104	08J 052	416	686	654	0 52	8 650	ALIIXM A	
20653416 WH32* 2 UJJ 112 08J 055 7 002 516 488 0 694 484 ALIIXM A 20653416 WH33* 2 02J 112 08J 055 695 686 0 552 682 ALIIXM A 20653416 WH33* 1 08J 055 09J 068 506 516 488 0 698 484 ALIIXM A 20653416 WH33* 2 03J 112 08J 056 416 702 686 0 528 682 ALIIXM A 20653416 WH33* 1 08J 056 09J 069 510 516 488 0 702 484 ALIIXM A 20653416 WH33* 1 08J 056 09J 069 510 516 488 0 702 484 ALIIXM A 20653416 WH33* 1 08J 056 09J 069 510 516 686 0 508 682 ALIIXM A 20653416 WH35* 2 04J 112 08J 057 416 706 686 0 504 682 ALIIXM A 20653416 WH35* 1 08J 058 09J 077 1416 706 686 0 504 682 ALIIXM A 20653416 WH36* 2 01J 114 08J 058 416 710 694 0 576 690 ALIIXM A 20653416 WH36* 1 08J 058 09J 071 518 516 488 0 710 484 ALIIXM A 20653416 WH36* 1 08J 058 09J 071 518 516 488 0 710 484 ALIIXM A 20653416 WH36* 1 08J 058 09J 071 518 516 488 0 710 484 ALIIXM A 20653416 WH36* 2 02J 114 08J 059 416 714 694 0 552 690 ALIIXM A 20653416 WH37* 1 08J 059 09J 072 522 516 488 0 714 484 ALIIXM A 20653416 WH38* 2 03J 114 08J 050 416 718 694 0 528 690 ALIIXM A 20653416 WH38* 2 03J 114 08J 050 509J 071 518 516 488 0 714 484 ALIIXM A 20653416 WH38* 2 03J 114 08J 060 416 718 694 0 528 690 ALIIXM A 20653416 WH38* 2 03J 114 08J 060 99J 073 526 516 488 0 718 484 ALIIXM A 20653416 WH38* 2 03J 070 04J 070 05J 071 518 510 508 514 ALIIXM A 20653416 WH38* 2 03J 070 04J 070 05J 070 506 514 510 510 528 514 ALIIXM A 2065346 WHITE 1 04J 070 04J 070 05J 070 506 514 510 510 508 514 ALIIXM A 2065346 WHITE 2 03J 070 04J 070 05J 070 480 514 510 510 528 514 ALIIXM A 2065346 WHITE 1 04J 070 05J 070 455 514 510 510 508 514 ALIIXM A 2065346 WHITE 1 05J 077 06J 077 07J 077		10652408		WM31*	1	04J 104	08J 112	408	682	654	0 50	4 650	ALIIXM A	
10622502 WB32*		20653416		WM32*	2									· · · · · · · · · · · · · · · · · · ·
20653416		10622502		WM32*	1	08J 054	09J 067	•			-			
10622506		20653416		WM33*	2	02J 112_								
20653416 WM354 2 UJJ 112 08J 057 070 1516 1516 488 0 702 484 ALIIXM A					1	08J 055	830 Leo	506	516	488	0 69	484	ALILAN A	
10622510 WH34# 08J 056 09J 069 510 516 488 0 702 484 ALIXM A 20652416 WH35# 08J 057 09J 070 514 516 488 0 706 484 ALIXM A 20652416 WH35# 108J 057 09J 070 514 516 488 0 706 484 ALIXM A 20652416 WH36# 20J 114 08J 058 416 710 694 0 576 690 ALIXM A 20652416 WH36# 108J 058 09J 071 518 516 488 0 710 484 ALIXM A 20652416 WH37# 20J 114 08J 058 416 710 694 0 576 690 ALIXM A 20653416 WH37# 20J 114 08J 059 09J 071 522 516 488 0 710 484 ALIXM A 20653416 WH37# 108J 059 09J 072 522 516 488 0 714 484 ALIXM A 20653416 WH38# 20J 114 08J 060 09J 073 522 516 488 0 718 484 ALIXM A 20652526 WH37# 108J 060 09J 073 526 516 488 0 718 484 ALIXM A 20652526 WH37# 108J 060 09J 073 526 516 488 0 718 484 ALIXM A 20652526 WH1TE 20J 070 03J 070 528 514 510 510 525 514 ALIXM A 20652526 WHITE 20J 070 03J 070 528 514 510 510 525 514 ALIXM A 20652400 WHITE 20J 070 04J 070 55J 070 480 514 510 510 528 514 ALIXM A 20652400 WHITE 20J 070 06J 070 445 514 510 510 528 514 ALIXM A 20652456 WHITE 20J 070 06J 070 456 514 510 510 528 514 ALIXM A 20652454 WHITE 20J 070 06J 070 456 514 510 510 528 514 ALIXM A 20652460 WHITE 20J 070 06J 070 456 514 510 510 528 514 ALIXM A 20652460 WHITE 20J 070 070 070 432 514 510 510 586 514 ALIXM A 20652454 WHITE 20J 070 070 070 432 514 510 510 486 514 ALIXM A 20652454 WHITE 20J 070 070 070 435 514 510 510 486 514 ALIXM A 20652464 WHITE 20J 070 070 070 432 514 510 510 486 514 ALIXM A 20652464 WHITE 20J 070 070 070 435 514 510 510 486 514 ALIXM A 20652464 WHAR(071) 20J 070 070 070 432 514 510 510 486 514 ALIXM A 20652464 WHAR(071) 20J 070 070 070 432 514 510 510 486 514 ALIXM A 20652464 WHAR(081) 20J 070 070 070 070 070 070 070 070 070 0		20(52/34		MM37-#	2	03J 112	08J 056	416	702	686				
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20653416 WM36* 1 08J 058 09J 071 518 516 488 0 710 484 ALIIXM A 20653416 WM37* 2 02J 114 08J 059 416 714 694 0 552 690 ALIIXM A 10622522 WM37* 1 08J 059 09J 072 522 516 488 0 714 464 ALIIXM A 20653416 WM38* 2 03J 114 08J 060 416 718 694 0 528 690 ALIIXM A 20653416 WM38* 1 08J 060 09J 073 522 516 488 0 718 484 ALIIXM A 20653416 WM38* 1 08J 060 09J 073 522 516 488 0 718 484 ALIIXM A 20653416 WM38* 1 08J 060 09J 073 522 516 488 0 718 484 ALIIXM A 2065256 WRITE 2 01J 070 02J 070 552 514 510 510 576 514 ALIIXM A 20652572 WRITE 1 02J 070 03J 070 528 514 510 510 528 514 ALIIXM A 20652504 WRITE 1 02J 070 03J 070 504 514 510 510 528 514 ALIIXM A 20652504 WRITE 2 03J 070 04J 070 504 514 510 510 528 514 ALIIXM A 20652460 WRITE 1 04J 070 05J 070 480 514 510 510 528 514 ALIIXM A 20652452 WRITE 2 05J 070 06J 070 436 514 510 510 504 514 ALIIXM A 20652432 WRITE 1 06J 070 07J 070 432 514 510 510 486 514 ALIIXM A 20652432 WRITE 1 06J 070 07J 070 432 514 510 510 486 514 ALIIXM A 20652434 WRITE 2 07J 070 09J 043 514 468 0 472 650 508 ALIIXM A 20652454 WRITE 2 07J 070 09J 043 514 468 0 472 650 508 ALIIXM A 20652454 WRITE 1 06J 070 77 07J 077 432 545 538 538 480 542 ALIIXM A 20652454 XIOREF 1 05J 077 09J 077 384 542 538 538 480 542 ALIIXM A 20652454 XIOREF 1 07J 077 09J 077 384 542 538 538 480 542 ALIIXM A 20652454 XMAR(07) 2 05J 008 06J 008 464 510 506 506 464 510 ALIIXM A 20652464 XMAR(07) 2 05J 008 06J 008 464 510 506 506 464 510 ALIIXM A 20652464 XMAR(08) 1 05J 007 06J 007 440 506 502 502 488 506 ALIIXM A 20652464 XMAR(08) 1 05J 007 06J 007 440 506 502 502 488 506 ALIIXM A 20652464 XMAR(08) 1 05J 007 07J 007 440 506 502 502 488 506 ALIIXM A 20652464 XMAR(08) 1 05J 007 07J 007 440 506 502 502 488 506 ALIIXM A 20652464 XMAR(08) 1 05J 007 07J 007 440 506 502 502 486 506 ALIIXM A 20652464 XMAR(08) 1 05J 007 07J 007 440 506 502 502 486 506 ALIIXM A 20652440 XMAR(08) 2 06J 007 07J 007 440 506 502 502 446 506 ALIIXM A 20652440 XMAR(15) 2 05J 030 06J 030 440 598 594 594 468 598 ALIIXM A 20652440 XMAR(15) 2		10055214			-		201.752	,	710	606	0 5	76 490	ALTIXM A	
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10622522 WM37* 1 08J 059 09J 072 522 516 488 0 714 484 ALIIXM A 20653416 WM38* 2 03J 114 08J 060 416 718 694 0 528 690 ALIIXM A 10622526 WM38* 1 08J 060 09J 073 526 516 488 0 718 484 ALIIXM A 20652552 WRITE 2 01J 070 02J 070 552 514 510 510 576 514 ALIIXM A 10652528 WRITE 1 02J 070 03J 070 528 514 510 510 576 514 ALIIXM A 20652504 WRITE 2 03J 070 04J 070 504 514 510 510 552 514 ALIIXM A 10652480 WRITE 1 04J 070 05J 070 480 514 510 510 528 514 ALIIXM A 10652480 WRITE 2 05J 070 06J 070 456 514 510 510 504 514 ALIIXM A 10652432 WRITE 1 06J 070 07J 070 432 514 510 510 480 514 ALIIXM A 20652514 WRITE 2 07J 070 07J 070 432 514 468 0 472 650 508 ALIIXM A 10652456 XIOREF 1 05J 077 06J 077 456 542 538 538 480 542 ALIIXM A 20652464 XMAR(07) 2 05J 008 06J 008 464 510 506 506 464 510 ALIIXM A 20652464 XMAR(07) 2 05J 008 06J 008 460 510 506 506 464 510 ALIIXM A 10652464 XMAR(07) 2 07J 008 09J 008 392 510 506 506 464 510 ALIIXM A 20652460 XMAR(07) 2 07J 008 09J 008 392 510 506 506 464 510 ALIIXM A 20652460 XMAR(08) 1 05J 007 06J 007 460 506 502 502 480 506 ALIIXM A 20652460 XMAR(08) 1 05J 007 06J 007 460 506 502 502 480 506 ALIIXM A 20652460 XMAR(08) 1 07J 007 09J 077 392 590 592 592 440 506 ALIIXM A 20652460 XMAR(08) 1 07J 007 09J 077 392 590 592 592 440 506 ALIIXM A 20652460 XMAR(08) 1 07J 007 09J 077 392 590 592 592 440 506 ALIIXM A 20652460 XMAR(08) 1 07J 007 09J 077 392 590 592 592 440 506 ALIIXM A 20652460 XMAR(08) 1 07J 007 09J 077 392 590 592 592 440 506 ALIIXM A 20652460 XMAR(08) 1 07J 007 09J 077 392 590 592 592 440 596 ALIIXM A 20652460 XMAR(08) 1 07J 007 09J 077 392 590 592 592 440 596 ALIIXM A 20652460 XMAR(15) 2 07J 008 09J 008 392 510 506 502 464 598 ALIIXM A 20652460 XMAR(15) 2 07J 007 09J 077 392 590 592 592 440 596 ALIIXM A 20652460 XMAR(15) 2 07J 007 09J 077 392 590 592 592 440 598 ALIIXM A 20652460 XMAR(15) 2 07J 007 09J 077 392 590 592 592 440 598 ALIIXM A 20652460 XMAR(15) 2 07J 007 09J 007 392 690 590 590 590 590 ALIIXM A		20652616		WM37#	2	02J 114	08J 059	416	714	694				
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20652464 XMAR(15) 2 053 030 065 030 075 030 440 598 594 594 464 598 ALIIXM A 10652440 XMAR(15) 1 06J 030 07J 030 440 598 594 594 464 598 ALIIXM A 20652392 XMAR(15) 2 07J 030 09J 040 392 638 602 0 440 598 ALIIXM A				VMAD ! 15		05 1 020	06.1-030	464	598	594	594	88 598	ALIIXM A	
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270 614 610 744 614 ALIIXM A													ALIIXM A	•
10/0000 7EU/IDIE 1 11.1 1140 17.1 1140 27.1 U.V VAV 27.1 WET		10652320		ZEROBUS	1	11J 034	12J 034	320	614	610	610	344 614	ALIIXM A	



WIRE-WRAP SIDE

BACKPLANE ASSEMBLY



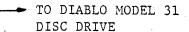
PS-1 = +12 VOLT POWER SUPPLY

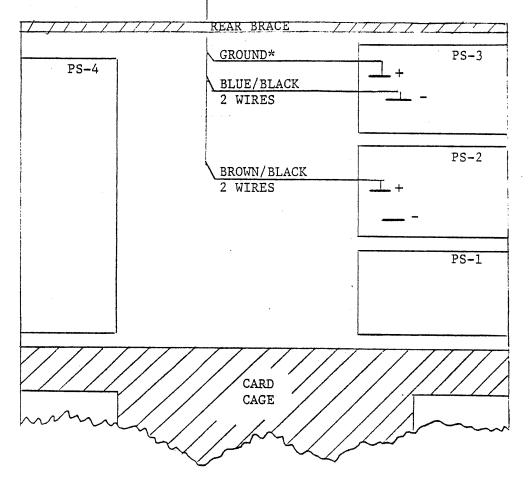
PS-2 = +15 VOLT POWER SUPPLY

PS-3 = -15 VOLT POWER SUPPLY

PS-4 = +5 VOLT POWER SUPPLY

ALTO II POWER WIRING DIAGRAM (WITHOUT DISC DRIVE CONNECTIONS)





*NOTE: GROUND WIRE MAY BE EITHER A BARE WIRE OR A BARE WIRE WITH BLACK SHRINK TUBING ON IT.

PS-1 = +12 VOLT POWER SUPPLY

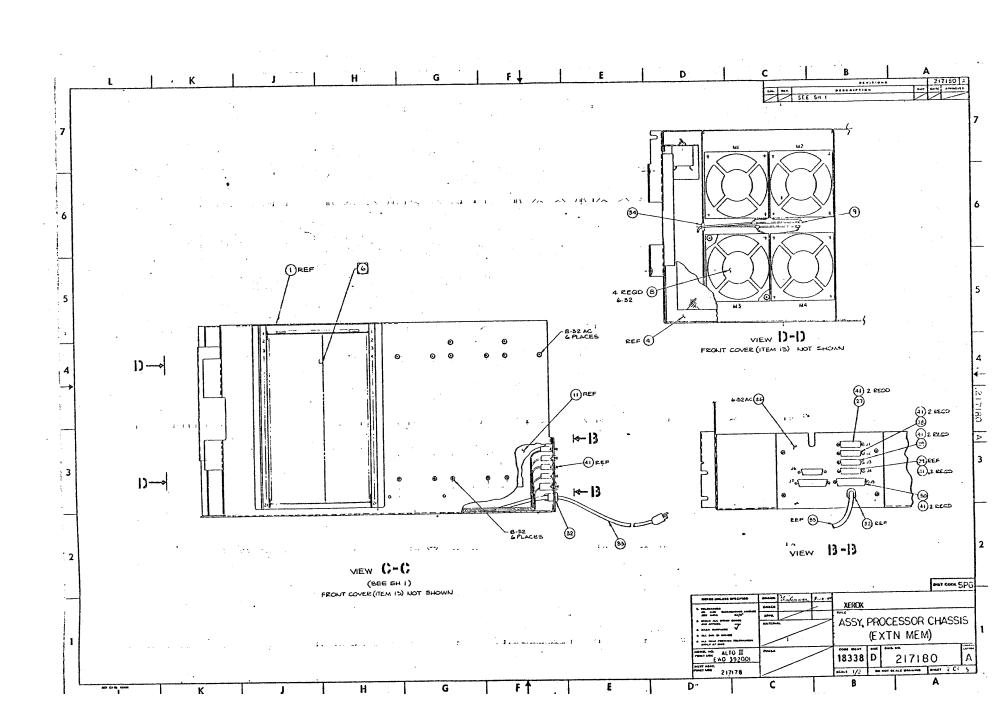
PS-1 = +12 VOLT POWER SUPPLY
PS-2 = +15 VOLT POWER SUPPLY
PS-3 = -15 VOLT POWER SUPPLY

PS-4 = +5 VOLT POWER SUPPLY

ALTO II POWER WIRING DIAGRAM

FOR DIABLO MODEL 31

DISC DRIVE



ALTO II EXTENDED MEMORY LAYOUT

																
		BANK	. 0			BANK	1			BANK	2			BANK	. 3	
BIT	0000			000-	0000			000-	000			000-	000			000-
DII	777		177		777		177		777		177	 	777		177	
	EVEN	ODD	EVEN	ODD	EVEN	ODD	EVEN	ODD	EVEN	ODD	EVEN	ODD	EVEN	ODD	EVEN	ODD
0	1-16	1-18	1-26	1-28	1-36	1-38	1-46	1-48	1-56	1-58	1-66	1-68	1-76	1-78	1-86	1-88
1	2–16	2-18	2-26	2-28	2-36	2-38	2-46	2-48	2-56	2-58	2-66	2-68	2-76	2-78	2-86	2-88
2	3-16	3-18	3-26	3–28	3-36	3-38	3-46	3-48	3-56	3-58	3-66	3-68	3-76	3-78	3-86	3-88
3	4-16	4-18	4-26	4-28	4-36	4-38	4-46	4-48	4-56	4-58	4-66	4-68	4-76	4-78	4-86	4-88
4)	1-11)	1-13	1-21	1-23	1-31	1-33	1-41	1-43	1-51	1-53	1-61	1-63	1-71	1-73	1-81	1-83
5	2-11	2-13	2-21	2-23	2-31	2-33	2-41	2-43	2-51	2-53	2-61	2-63	2-71	2-73	2-81	2-83
6 }	3-11	3-13	3-21	3-23	3-31	3-33	3-41	3-43	3-51	3-53	3-61	3-63	3-71	3-73	3-81	3-83
7)	4-11	4-13	4-21	4-23	4-31	4-33	4-41	4-43	4-51	4-53	4-61	4-63	4-71	4-73	4-81	4-83
8	1-17	1-19	1-27	1-29	1-37	1-39	1-47	1-49	1-57	1-59	1-67	1-69	1-77	1-79	1-87	1-89
9	2-17	2-19	2-27	2-29	2-37	2-39	2-47	2-49	2-57	2-59	2-67	2-69	2-77	2-79	2-87	2-89
10	3-17	3-19	3-27	3-29	3–37	3-39	3-47	3-49	3-57	3-59	3-67	3-69	3-77	3-79	3-87	3-89
11	4-17	4-19	4-27	4-29	4-37	4-39	4-47	4-49	4-57	4-59	4-67	4-69	4-77	4-79	4-87	4-89
12	1-12	1-14	1-22	1-24	1-32	1-34	1-42	1-44	1-52	1-54	1-62	1-64	1-72	1-74	1-82	1-84
13	2-12	2-14	2-22	2-24	2-32	2-34	2-42	2-44	2-52	2-54	2-62	2-64	2-72	2-74	2-82	2-84
14	3–12	3-14	3-22	3-24	3-32	3-34	3-42	3-44	3-52	3-54	3-62	3-64	3-72	3-74	3-82	3-84
15	4-12	4-14	4-22	4-24	4-32	4-34	4-42	4-44	4-52	4-54	4-62	4-64	4-72	4-74	4-82	4-84
		BANK	. 0			BANK	1			BANK	2			BANK	. 3	
														,	•	
но	1-2	20	1-1	30	1-4	40	1-:	50	1-0	60	1-7	70	1-	80	1-	90
H1	2-2		2-3		2-4		2-		2-0		2-		2-		2-	90
H2	3-2		3-3		3-4		3-5		3-0		3-		3-		3-	
Н3	4-2		4-		4-4		4-		4-0		4-		4-		4-	90
H4	1-1		1-2		1-3		1-4		1-:		1-0		1-		1-	85
Н5	2-1		2-		2-3		2-4		2-		2-0		2-		2-	85
P	3-1		3-2		3-3		3-4		3-		3-0		3-		3-	85

NOTE: LOCATIONS ARE CARD-CHIP

ALTOIL MEMORY

ADDRESS MAPPING

The mapping of addresses to memory chips can be altered by the setting of the "memory configuration switch". This switch is located at the top of the backplane of the AltoII. If the switch is in the alternate position, the first and second 32K portions of memory are exchanged.

The AltoII memory system is organized around 32-bit doublewords. Stored along with each doubleword is 6 bits of Hamming code and a Parity bit for a total of 39 bits:

bits 0-15	even data word
bits 16-31	odd data word
bits 32-37	Hamming code
bit 38	Parity bit

Things are further complicated by the fact that two types of memory chips are used: 16K chips in machines with extended memory and 4K chips for all others.

The bits in a 1-word deep slice of memory are called a group. A group contains 4K or 16K doublewords, depending on the chip type. The bits of a group on a single board are called a subgroup. Thus a subgroup contains 10 of the 40 bits in a group. There are 8 subgroups on a memory board. Subgroups are numbered from the high 3 bits of the address: for 4K chips this means MAR[0-2]; for 16K chips (i.e., an Alto with extended memory) this means BANK.MAR[0]:

Subgroup	Chip Positions				
7 6 5 4 3 2	81-90 71-80 61-70 51-60 41-50 31-40	• •			
0	21-30 11-20	Nearest	the	edge	connector

The location of the bits in group 0 is:

CARD 1	CARD-2;	CARD 3	photos d'annagen en anta and Malain en	-CA	RD	4	1
32 24 16 08 00	33 25 17 09 01	34 26 18 10 02			19		
36 28 20 12 04	37 29 21 13 05	38 30 22 14 06	XX	31	23	25	07
^	<u> </u>						
chip	position 11						

Chips 15, 25, 35, 45, 55, 65, 76, and 85 on board 4 aren't used. If you are out of replacement memory chips, you can use one of these, but then the board with the missing chips will only work in Slot 4.

ALTOII MEMORY

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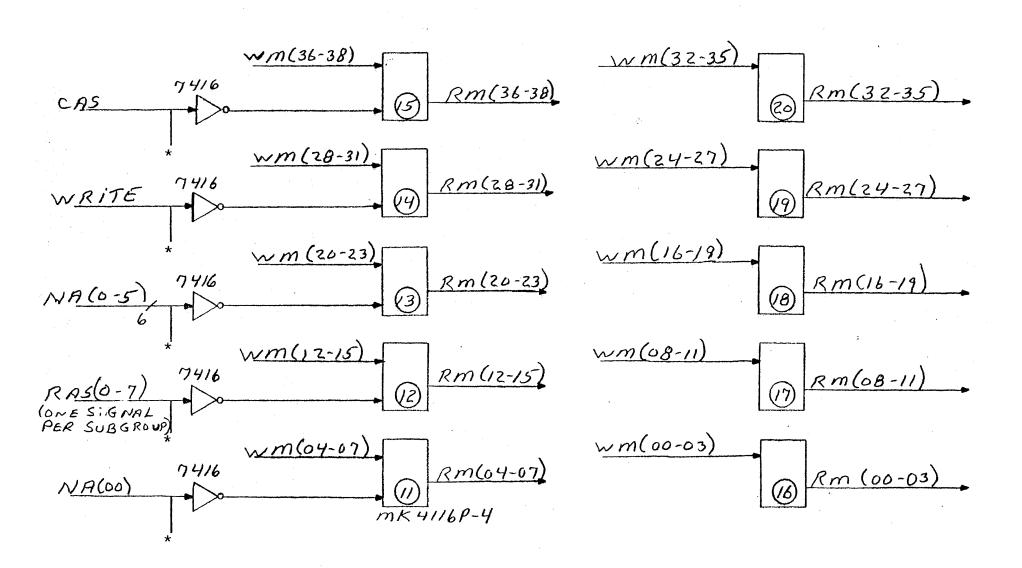
Subgroup	Chip Positions				
7	81-90				
6	71-80				
- 5	61-70				
4	51-60				
3	41-50				
2	31-40				
1	21-30				
0	11-20	Nearest	the	edge	connector

The location of the bits in group 0 is:

CARD 1	CARD 2	CARD 3		CA	RD	4	
32 24 16 08 00	33 25 17 09 01	34 26 18 10 02	35	27	19	11	03
36 28 20 12 04	37 29 21 13 05	38 30 22 14 06	XX	31	23	25	07
₺		<u> </u>					_4
ch	nip position 11						

Chips 15, 25, 35, 45, 55, 65, 76, and 85 on board 4 aren't used. If you are out of replacement memory chips, you can use one of these, but then the board with the missing chips will only work in Slot 4.

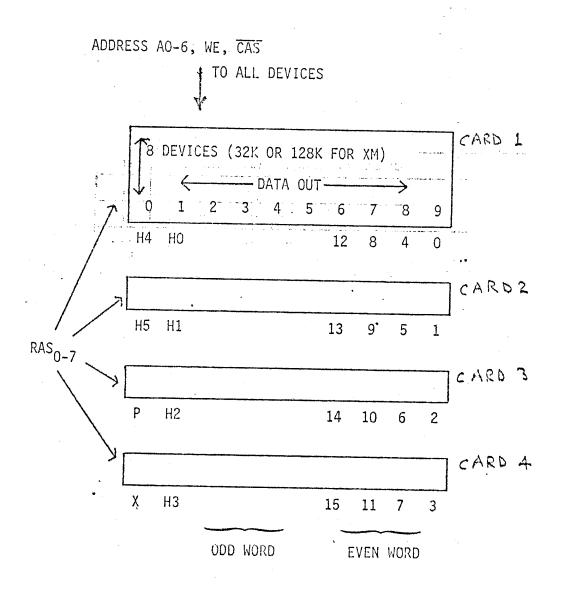
MEMORY STON JGE MODULE (ONE SUBGROUP) (ALL EigHT SUBGROUPS ARE IDENTICAL)



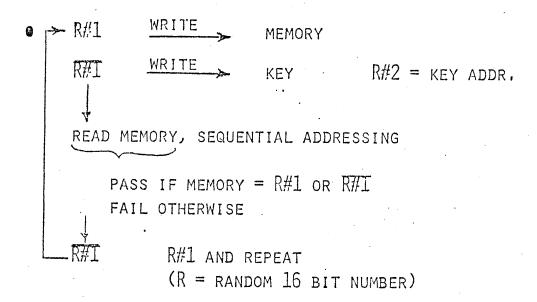
ALTO MEMORY

- WORD = 16 BITS
- ACCESS -> 2 WORDS AT A TIME 0 32 BITS + 6 BITS EC + PARITY + SPARE = 40 BITS
- 10 BITS/MODULE 80 DRAMS/MODULE 0

4 MODULES/ALTO 320 DRAMS/ALTO 0



DMT - DIAGNOSTIC MEMORY TEST



- EVERY OTHER COMPLETE TRIAL IS RUN WITHOUT ERROR CORRECTION AND RESULTS COMPARED.
- NUMBER OF ERRORS IS STORED IN 320 WORDS OF MEMORY (65K ERRORS MAX.).

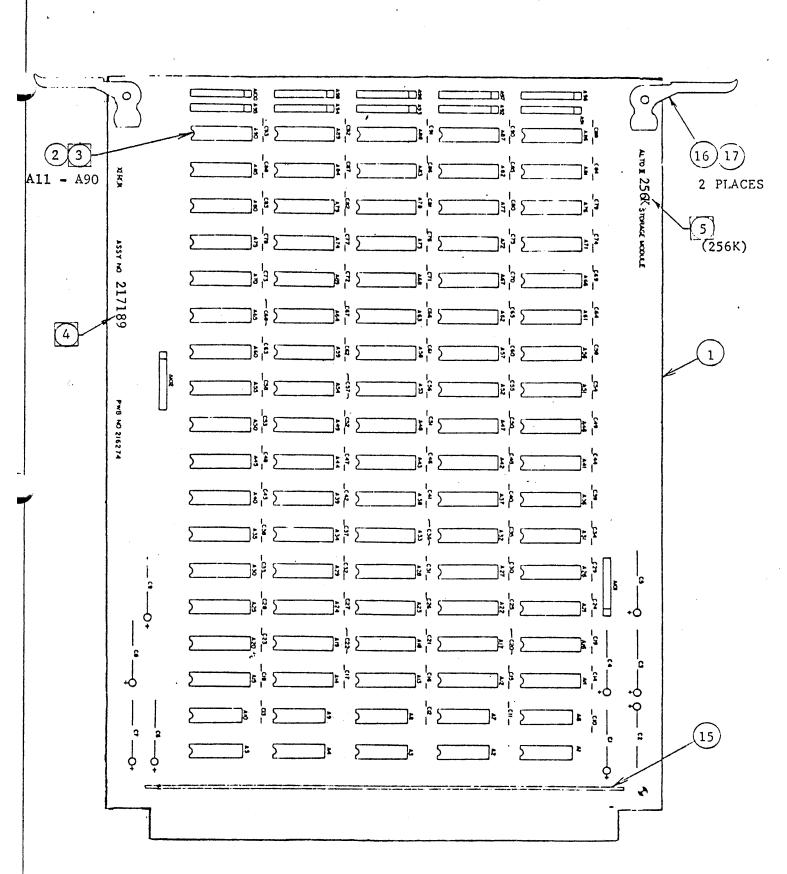
The state of the s

- ON SCREEN CURSOR JUMPS ONCE EACH TIME THROUGH
- S KEY CAUSES STATUS DISPLAY AND TRANSMIT ON NET.
 AUTO TRANSMIT EVERY 10K PASSES THROUGH DMT.
- PEEK LOOKS AT NET AND STORES ALL DMT TRANSMISSION ON THAT NET.

ALTO II EXTENDED MEMORY LAYOUT

			· · · · · · · · · · · · · · · · · · ·		<u> </u>		<u> </u>		I							
		BANK	. 0		'	BANK	1			BANK	2			BANK	3	
BIT	000	00-	100	000-	000	00-	100	000-	000	00-	1000	000-	000	00-	100	000-
BII	777	77	177	777	777	77	177	777	777	77	177	777	777	77	177777	
	EVEN	ODD	EVEN	ODD	EVEN	ODD	EVEN	ODD	EVEN	ODD	EVEN	ODD	EVEN	ODD	EVEN	ODD
0	1-16	1-18	1-26	1-28	1-36	1-38	1-46	1-48	1-56	1-58	1-66	1-68	1-76	1-78	1-86	1-88
1	2-16	2-18	2-26	2-28	2-36	2-38	2-46	2-48	2-56	2-58	2-66	2-68	2-76	2-78	2-86	2-88
2	3-16	3-18	3-26	3-28	3-36	3-38	3-46	3-48	3-56	3-58	3-66	3-68	3-76	3-78	3-86	3-88
3	4-16	4-18	4-26	4-28	4-36	4-38	4-46	4-48	4-56	4-58	4-66	4-68	4-76	4-78	4-86	4-88
4	1-11	1-13	1-21	1-23	1-31	1-33	1-41	1-43	1-51	1-53	1-61	1-63	1-71	1-73	1-81	1-83
5	2-11	2-13	2-21	2-23	2-31	2-33	2-41	2-43	2-51	2-53	2-61	2-63	2-71	2-73	2-81	2-83
6 /	3–11	3-13	3-21	3-23	3-31	3-33	3-41	3-43	3-51	3-53	3-61	3-63	3-71	3-73	3-81	3-83
7.	4-11	4-13	4-21	4-23	4-31	4-33	4-41	4-43	4-51	4-53	4-61	4-63	4-71	4-73	4-81	4-83
8	1-17	1-19	1-27	1-29	1-37	1-39	1-47	1-49	1-57	1-59	1-67	1-69	1-77	1-79	1-87	1-89
9	2-17	2-19	2-27	2-29	2-37	2-39	2-47	2-49	2-57	2-59	2-67	2-69	2-77	2-79	2-87	2-89
10	3–17	3-19	3-27	3-29	3-37	3-39	3-47	3-49	3-57	3-59	3-67	3-69	3–77	3-79	3–87	3-89
11	4-17	4-19	4-27	4-29	4-37	4-39	4-47	4-49	4-57	4-59	4-67	4-69	4-77	4-79	4-87	4-89
12	1-12	1-14	1-22	1-24	1-32	1-34	1-42	1-44	1-52	1-54	1-62	1-64	1-72	1-74	1-82	1-84
13	2-12	2-14	2-22	2-24	2-32	2-34	2-42	2-44	2-52	2-54	2-62	2-64	2-72	2-74	2-82	2-84
14	3-12	3-14	3-22	3-24	3-32	3-34	3-42	3-44	3-52	3-54	3-62	3-64	3-72	3-74	3-82	3-84
15	4-12	4-14	4-22	4-24	4-32	4-34	4-42	4-44	4-52	4-54	4-62	4-64	4-72	4-74	4-82	4-84
										•						
		BANK	. ^			BANK	1			BANK	2			BANK	. 2	
		DHIM	. 0			DANK	Т			DANK	۷			DAM		
															•	
													Ì			
									ļ				<u> </u>			l
НО	1-2		1-3		1-4		1-:		1-0		1-7		1-8		1-	
H1	2-2		2-:		2-4		2-		2-0		2-7		2-8		2-	
Н2	3-2		3-:		3-4		3		3-0		3-7		3-8		3-9	
Н3	4-2		4-:		4-4		4-		4-6		4-7		4-8		4-9	
H4	1-3		1-3		1-3		. 1-4		1-3		1-6		1-75		1-8	
Н5	2-1		2-:		2-3		2-4		2-5		2-6		2-1		2-8	
P	3-1	15	3-:	25	3-3	35	3-4	45	3-5	55	3-6	65	3-	75	3-8	35

NOTE: LOCATIONS ARE CARD-CHIP



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ASSY, PRINTED WIRING 256K STORAGE

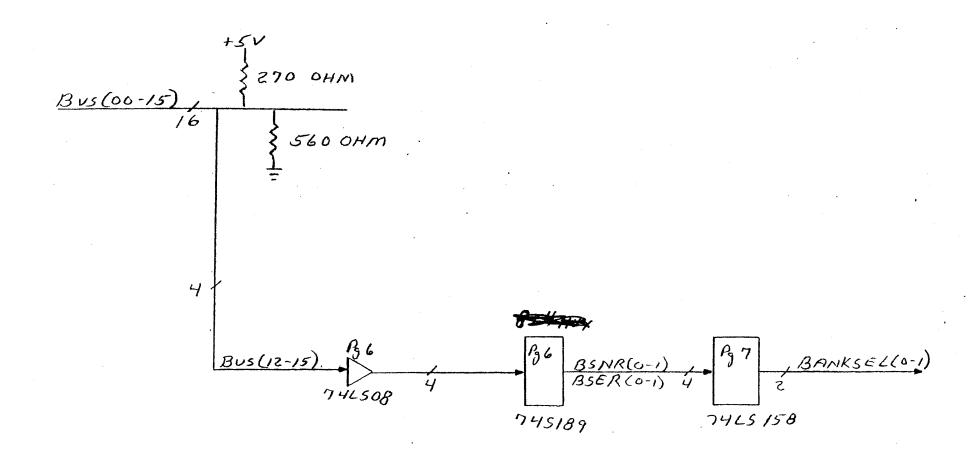
Xerox Corporation
El Segundo, California

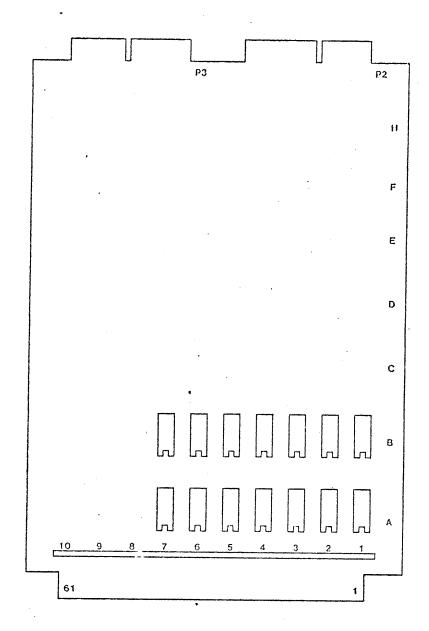
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ei s	ogimidas, C	alifornia 90245	A		- . .	Drawing No.	Re	ev.
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A Dwg. No.	Drawiing A !	SSEMBLY, PRINTED WIRING- 256K STORAGE		of Xerox Cor issued in stri the prior writ tion Bank Xe	proin, are poration a confiding tent confiding tent permited to purpose of articles	pecifications, and the exclusive pro and or Rank Xero ence and shalf no issIOn of Xerox Co or reproduced, co whatsoever, exce s for Xerox Corpo	operty ox,Ltd. ot, withou Corpora-	
7 1		XM256K-ML	Model N		Date	3-21-78 She	et 4 01	4
8 9	liem 140.	Drawing Title		Drawing No.	No. Rea.			
	1	BOARD, PRINTED WIRING		216274 B	1			
	2	MICROCIRCUIT MK4116P-4			80	A11 THRU 90		
ML.	3	MICROCIRCUIT 7416		213437	10	A1 THRU 10		
	4							
	5	RESISTOR, NETWORK 220 OHM		188600-003	12	A91 THRU 102		
	6	CAPACITOR, TANT. 18 UF, 50 V		123300-186	4	C6 THRU 9		
	7	CAPACITOR, TANT. 22 UF, 15 V		187720-005	5	C1 THRU 5		
I	- 8	CAPACITOR, CERAMIC .01 UF, 50 V		188483-001	85	C10 THRU 93		
	9							
	10	SOCKET, 16 PIN AUGAT #516-AG11D			80	A11 THRU 90		
	11	SOCKET, 14 PIN AUGAT #514-AG11D			10	A1 THRU 10		***********
-	12							
	13	STIFFENER, MODULE		216242	1			
	14	EXTRACTOR, MODULE		216250	2			
-	15	RIVET, CLIND, PULL-THRU		156111-005	2			
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MEMORY EXTENSION AND TERMINATOR MODULE (MEAT)





XEROX

Xerox Corporation 70†South Aviation Boulevard El Segundo, California 90245

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Title

ALTO R

ASSEMBLY, PRINTED WIRING -

MEMORY EXTENSION AND TERMINATOR

MODULE

Xerox Corporation

El Segundo, California

XEROX

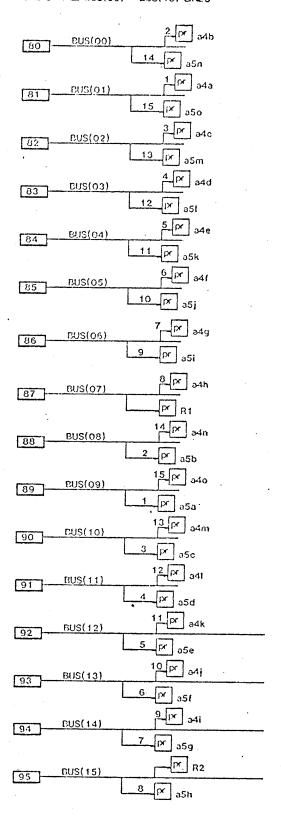
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В

Sheet

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	ERIAL I	X81CY Cotted				ML	216646	В		
Rev. B - Dwy ''o.	ASS	THE 701'South A TO II SEMBLY, PRINTED WIRING - MORY EXTENSION AND TERMINATOR MODULE	viation Bouley Catifornia 90:	and 345	These drawin contained the of Xerox Corpissued in strict the prior writt tion Rank Xero used for any manufacture or Rank Xero.	rein, are the poration and at confidence to permission by the repurpose who if articles for	exclusive pro or Rank Xero and shall no on of Xerox C reproduced, ed toocyer, exce	porty x,Ltd, t, willbout corpora- poied or pt the		
6 6				Model Ho.	ALTO II	Date 8	8-26-77			
4	Item No.	Drawing Title			Drawing No.	tto. Req.	Rema	4 Of 7		
6	1	BOARD, P.WMEMORY EXTENSION AND T	ERMINATOR		216548	1				
	2									
ML	3	RESISTOR, NETWORK, DIP 270	·····			1	A4 (A-	B #316-A-271)		
	4	RESISTOR, NETWORK, DIP 560				1	A5 (A-	B #316-A-561)		
	5	RESISTOR, FILM, 560 OHM, +5% -5%, 1/4	ı w		116447-561	1	R1			
	6	RESISTOR, FILM, 270 OHM, +5% -5%, 1/4	ı w		116447-271	1	R2			
	7	RESISTOR, FILM, 1 K OHM, +5% -5%, 1/4	w		116447-102	1 .	R3			
	8									
and the second	9	CAPACITOR, .01uF, 50V CERAMIC			188483-001	12	C2-C13			
	10	CAPACITOR, 22uF, 15V TANTALUM	•		187720-005	1	C1			
	11				•					
-	12	MICROCIRCUIT, 74S02	T.I.			1	A1			
	13	MICROCIRCUIT, 74S04	T.I.			2	A3, A7			
}	14	MICROCIRCUIT, 74LS08	T.I.			1	B5			
-	15	MICROCIRCUIT, 74530	T.I.			1	A2			
-	16	MICROCIRCUIT, 74S32	T.I.			1	B1			
-	17	MICROCIRCUIT, 7438	T.I.			1	A6			
-	18	MICROCIRCUIT, 74LS158	T.I.	-		2	84, 67			
-	19	MICROCIRCUIT, 74S189	T.I.			1	B6			
-	20	MICROCIRCUIT, 13205	INTEL			2	B2, B3			
-	21									
-	22									
-	23	STIFFENER			216242	. 1				
-	24	EXTRACTOR, MODULE		·	216250	2		,		
-	25	RIVET, BLIND PULL THRU	·		. 156111-005	2		1		
-	26	. ` .	-							
-	27	SOCKET, MICROCIRCUIT	16 [1415	516-AG11D	7	AUGAT			
_	28	SOCKET, MICROCIRCUIT	14 1	101	514-AG11D	7	AUGAT			
-	29 .									
1	30	ALTO IL MODULE ASSY SPECIFICATION			216207	REF				



XEROX

Xerox Corporation 701'Qouth Aviation Beulevard প্র Segundo, California 90043

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Title

ALTO II

ASSEMBLY, PRINTED WIRING
MEMORY EXTENSION AND TERMINATOR

MODULE

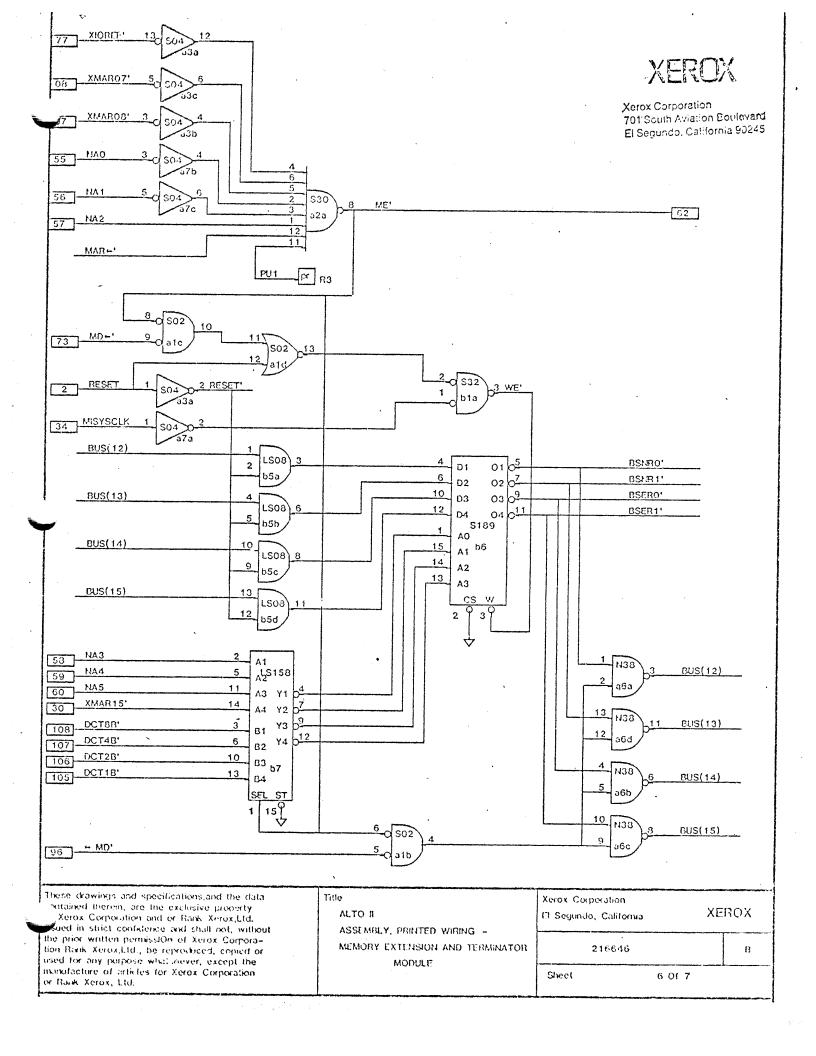
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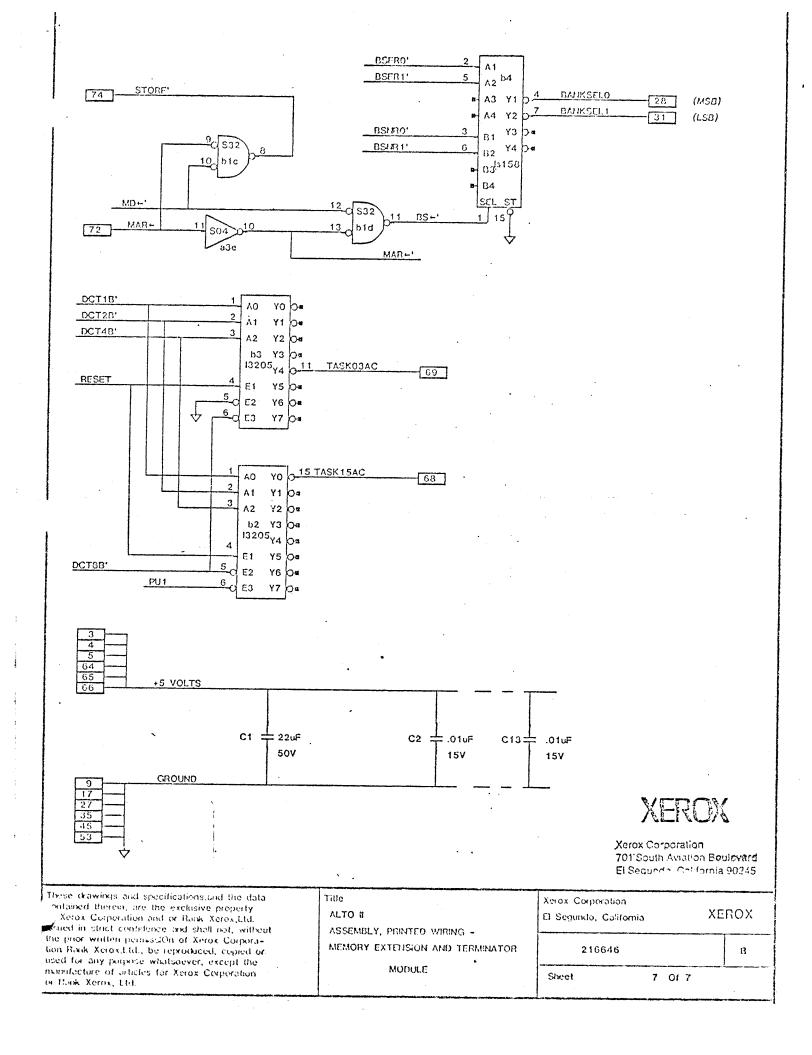
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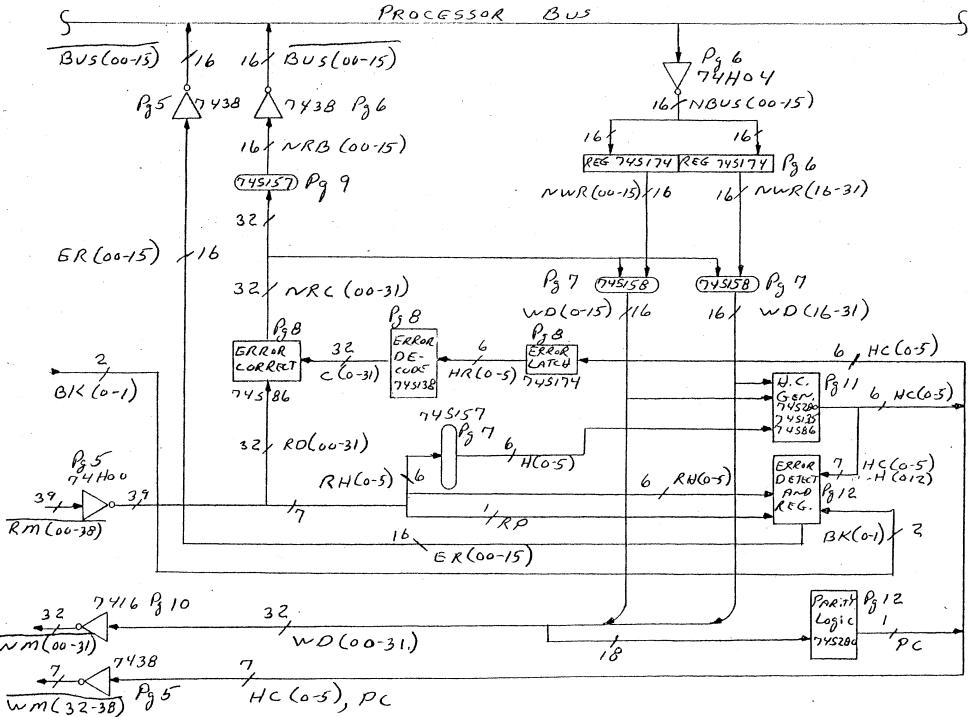
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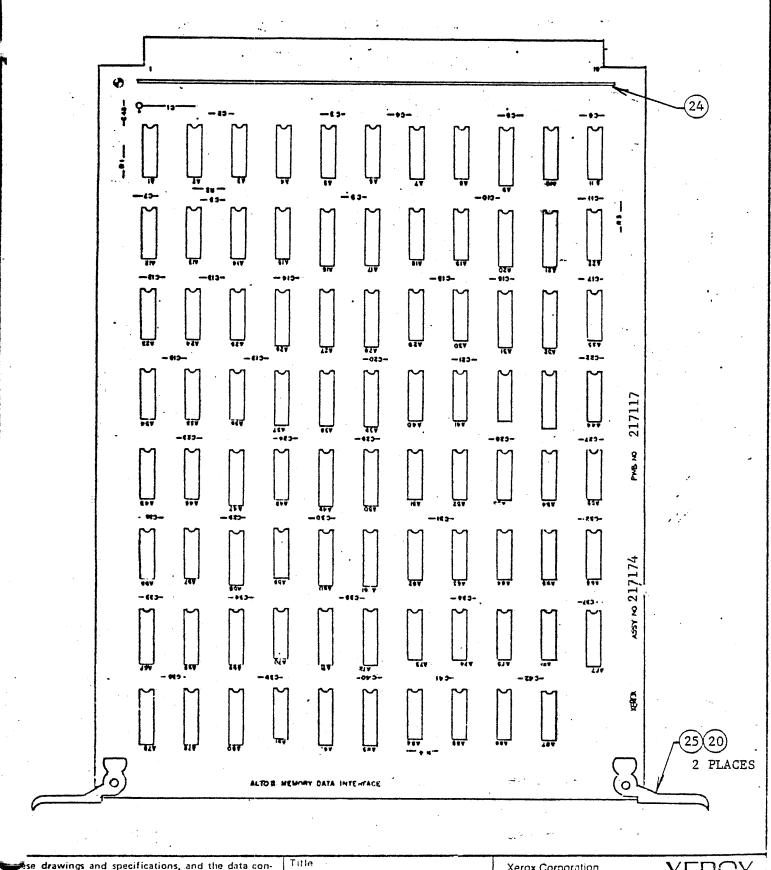
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EXTENDED MEMORY GATA ENTERFACE MODULE (DIM) (M)





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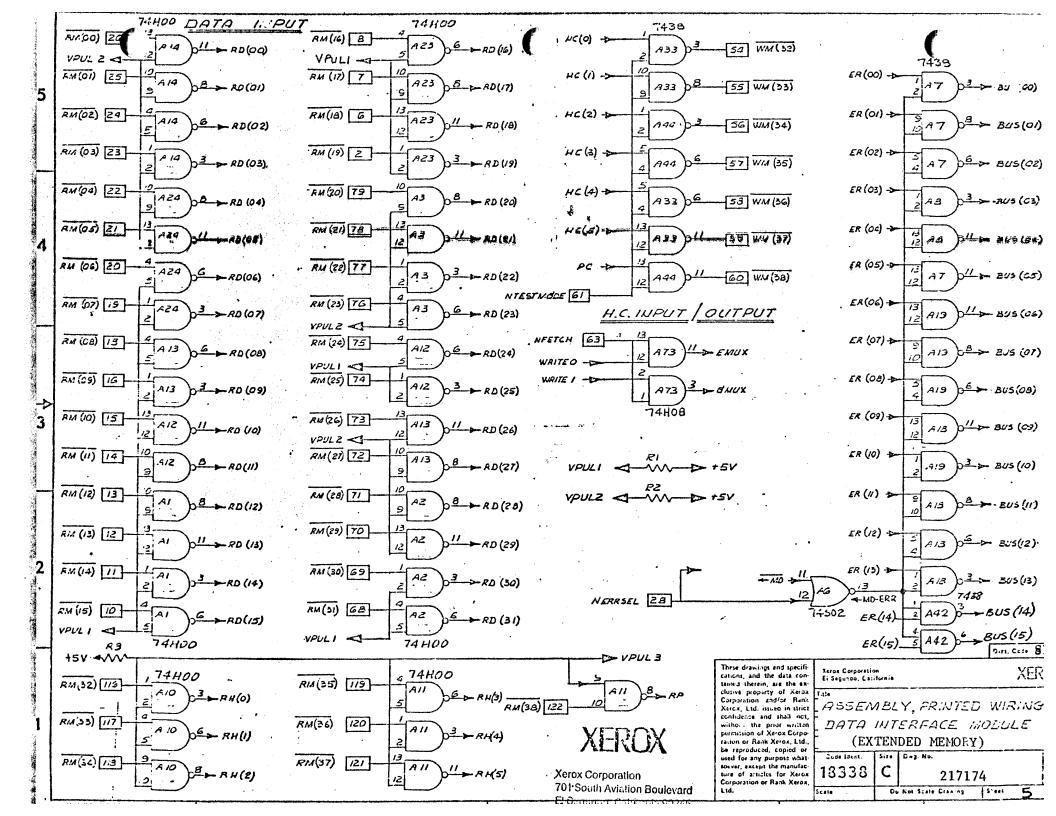
ASSY, PRINTED WIRING-MEMORY DATA INTERFACE (EXTENDED MEMORY) Xerox Corporation
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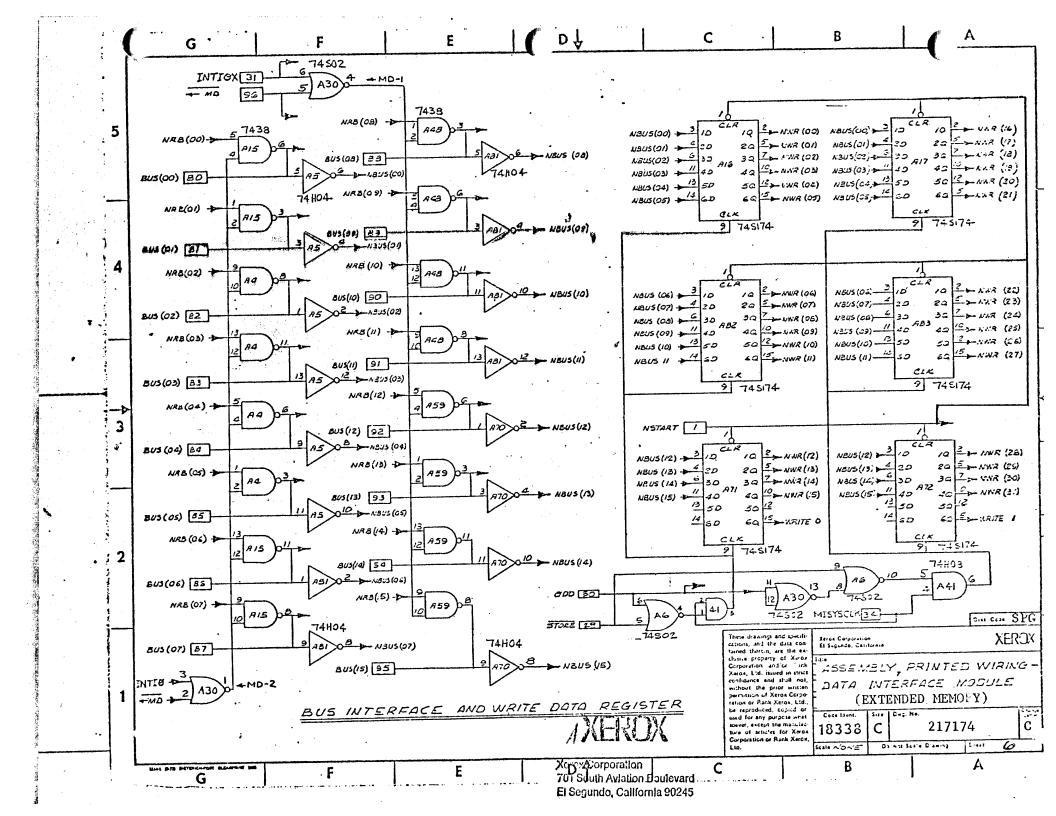
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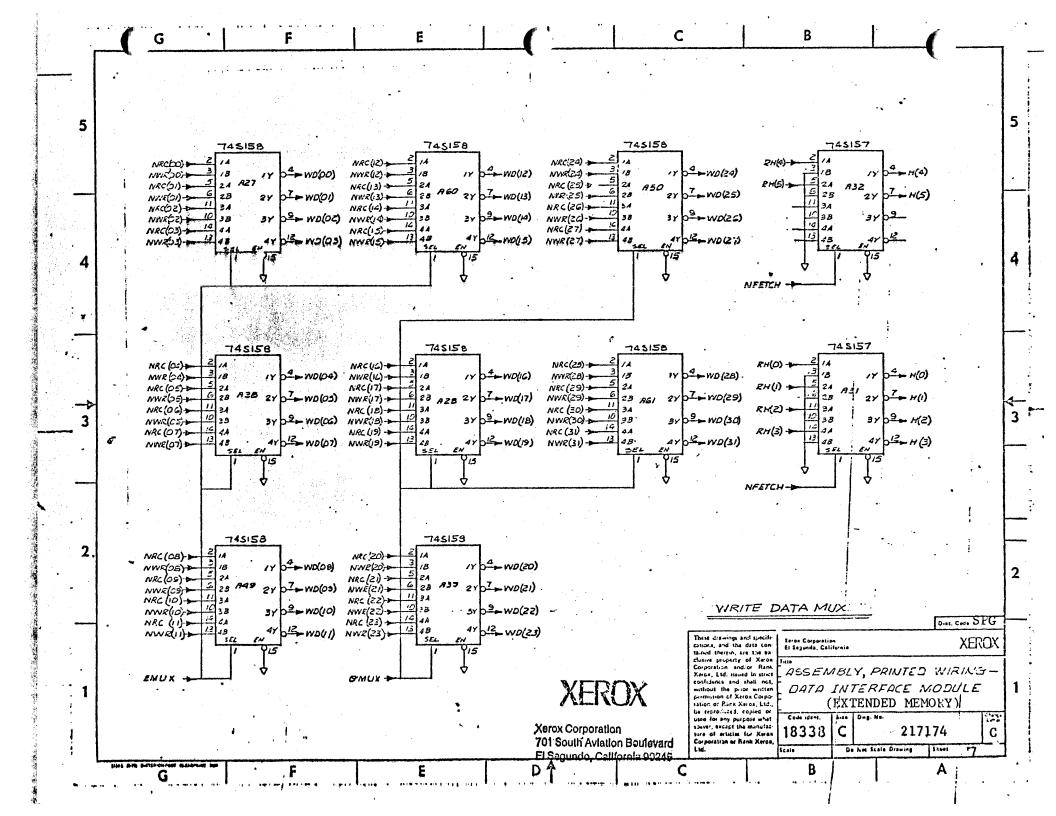
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Sheet 3 Of

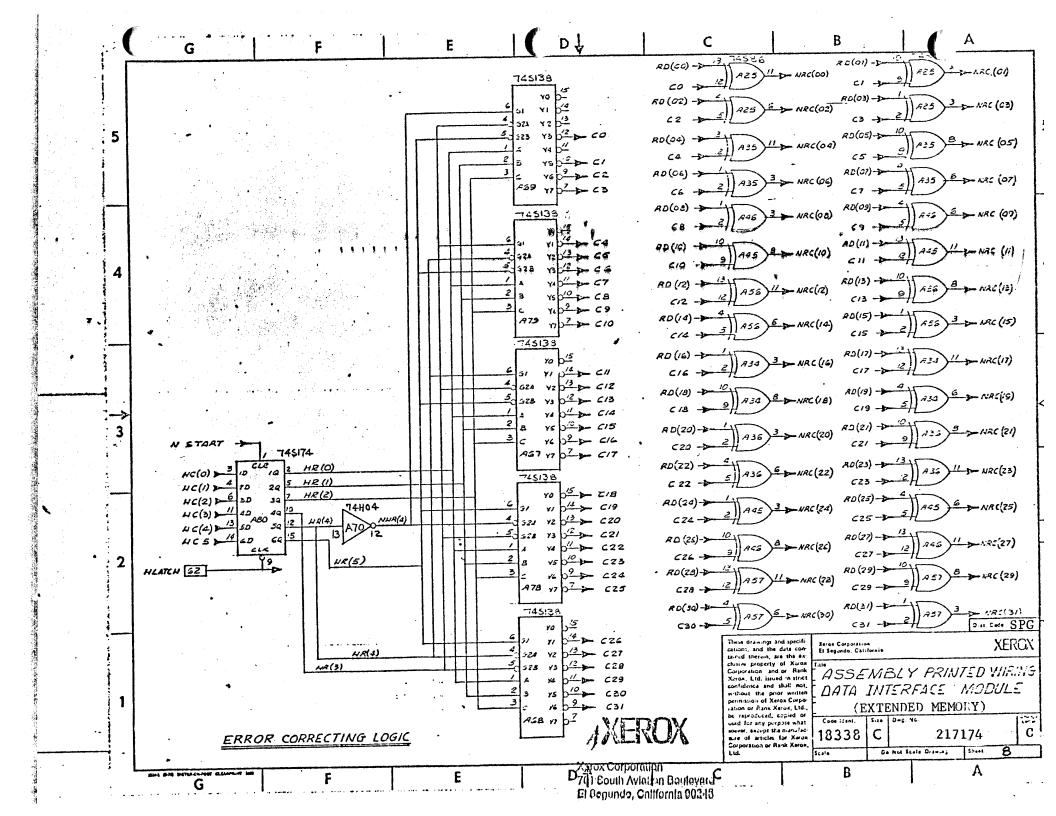


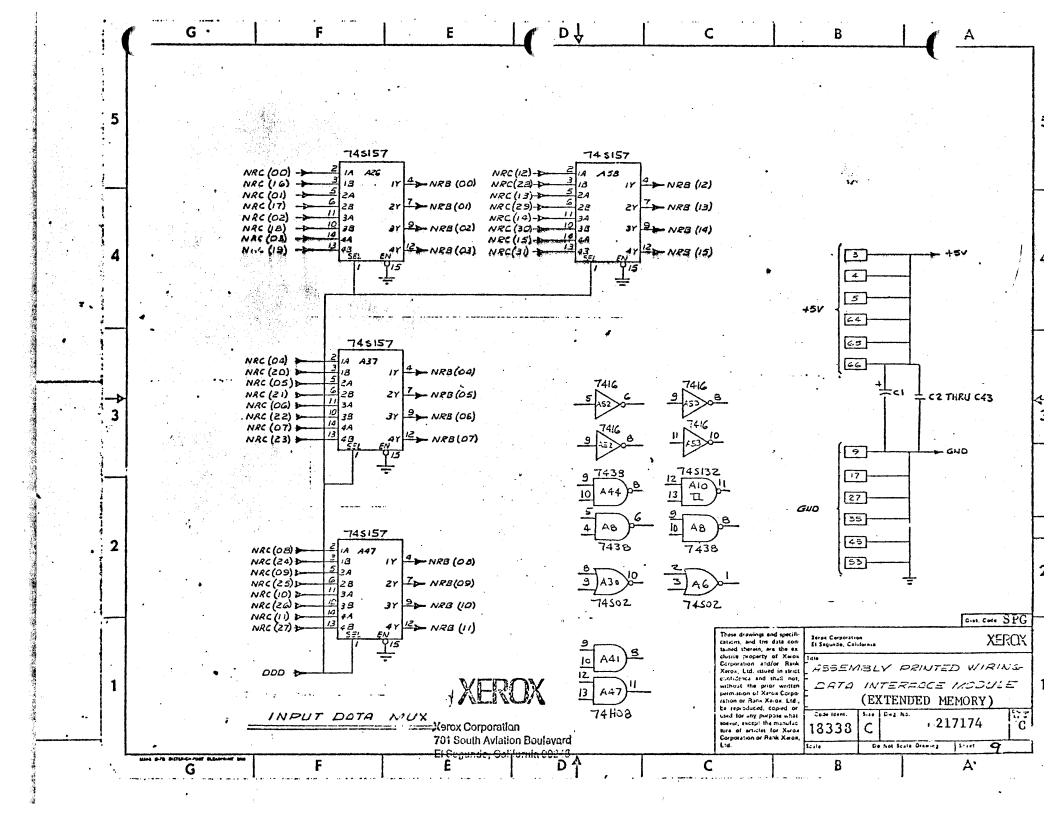
	al Lis		7		ML	Drawing No. 217174	Rey.
^^ A	ASSEM	BLY, P.W.	These drawings and speci in, are the exclusive pro Xerox, Ltd. issued in stric prior written permission o be reproduced, copied or u the manufacture of articles	perty of at confident f Xerox (ased for a a for Xero	Xerox ence a Corpor env ou	Corporation and and shall not, wit ation or Rank Xer rpose whatsoever poration or Rank Xe	or Rani hout the ox. Ltd. , excep rox. Ltd
ten	MEMO	RY DATA INTERFACE (EXTN MEM.)	Model No. ALTO II XM	Date 3	/10/-	78 Sheet	Of
Item	n No.	Drawing Title	Drawing No.	No. Req.		Remarks	
Item		Board, P.W.	217117	1 .	ļ		
1 2	<u>, </u>	Procedure, Test	216313	Ref.	<u> </u>		
3	3	Spec, Module Assembly	2 :16207	Ref			**
4	<u> </u>	Microcircuit 7416		6	A25	, 40,51,52,	53,62
5	5	74H04		3	1	70, 81	-
6	5	7438		11	A4,	7,8,15,18,1	9,33,
			·		1	59,42	
7	,	. 74ноо		10		2,3,10,11,1	2 12
				1111	1	23.24	•
8	3	74H08		2	1	3, 41	······································
9		74586		9	1	,34,35,36,4	5 16
.					1	56.57	<u>, TU, </u>
7	0	74\$138		5	1	, 68, 69, 78, 7°	9
	1	74S280		12	¢ .	,63,64,65,6	
		13-33			4	76,84 thru 87	
1	2	74H30		1	A22	•	
	3	74\$158		8	1	,28,38,39,4	9 50
			·		60,		,,50,
1	4	74\$157		6	1	.26.32.37.4	7 58
	5	745135		1	A77		,,,,,
	6	74\$174		8	1	16,17,71,72	80
				-	82,		,,,,
1:	7	√ 74S175		3	1	,21,43	
1:	8	Microcircuit 74502		2	A6,		
-	9				1		
2	0	Rivet	156111-005	2			*********
2		Resistor, Film, 1K, +5%, 1/4w	116447-102	. 3	RI	R2. R3	
2:		Capacitor, Tant, 22µ F , +20%, 15V	187720-005	1	C1	114, 110	
2		Capacitor, Ceramic, .01µF, 50V	188483-001	42	i	thru 43	
	4	Stiffener	216242	1			
	5	Extractor	216250	2			
26		Socket, Microcircuit 14 Pin		55	j		
27		Socket, Microcircuit 16 Pin	516- AG11D	30			

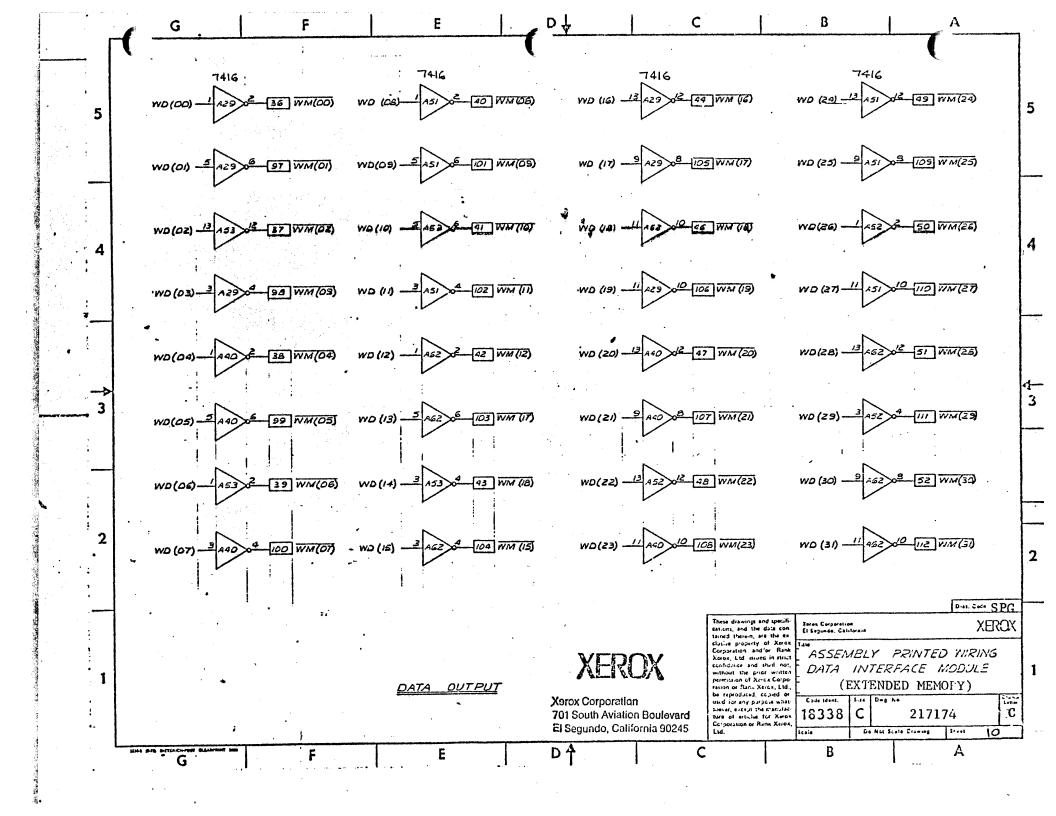


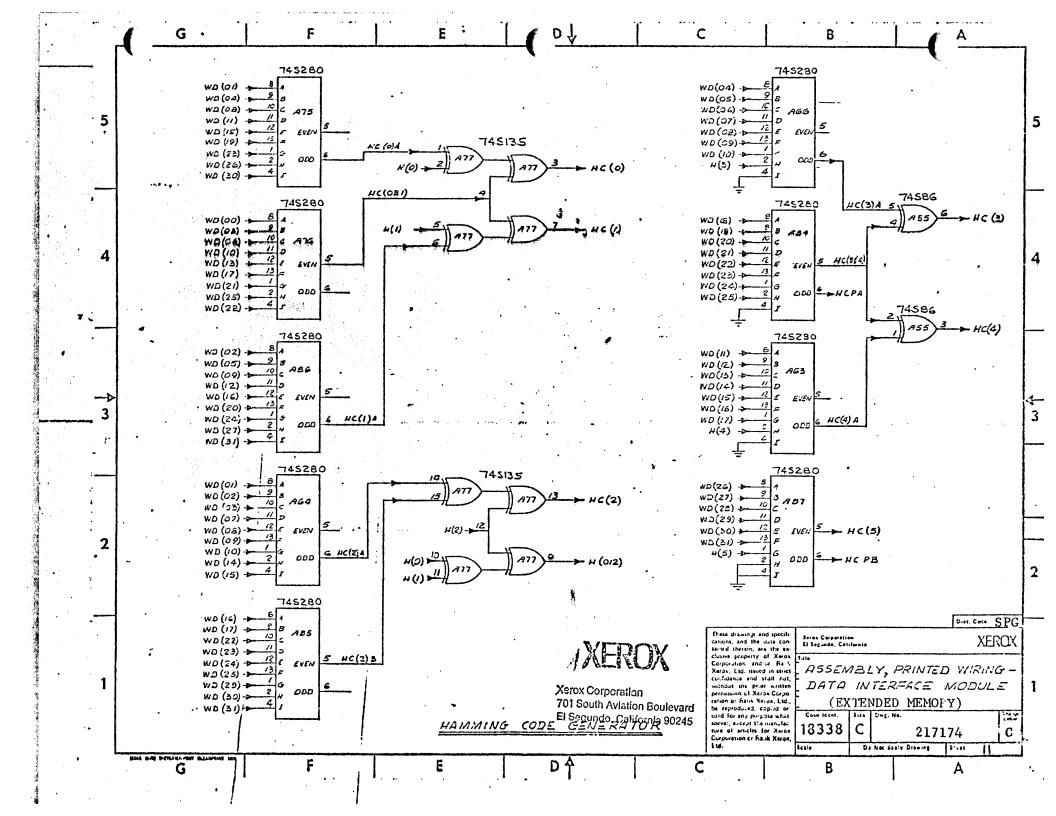


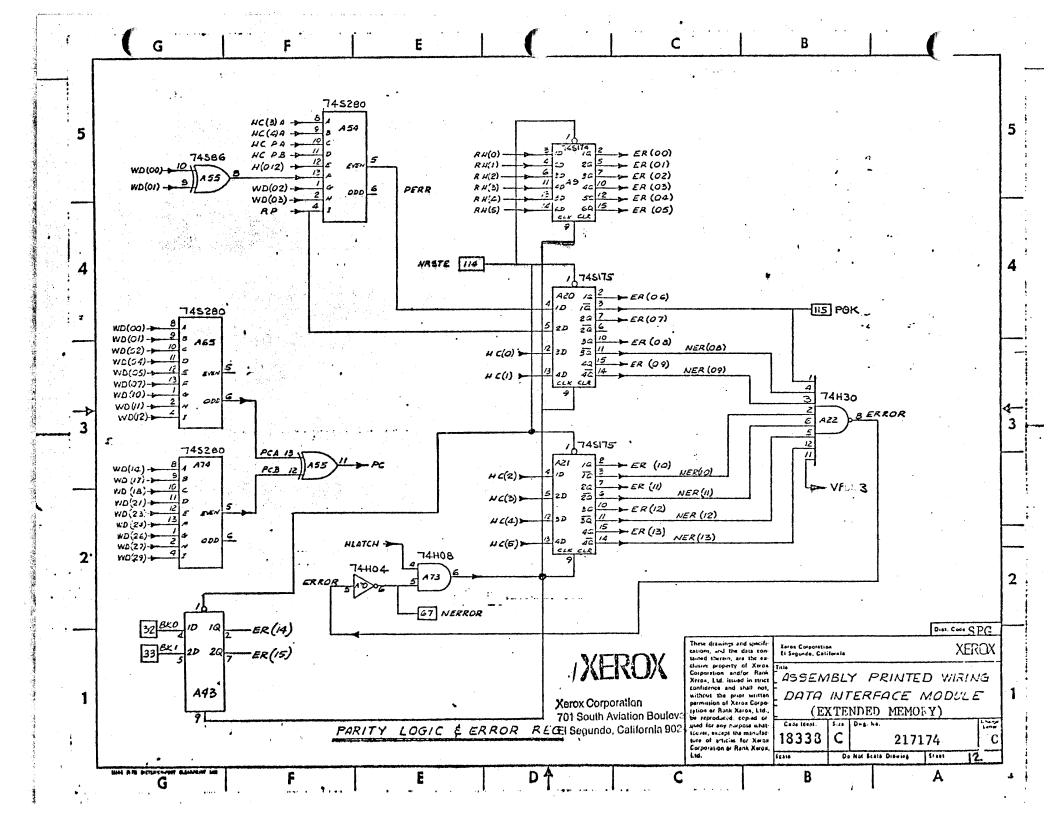


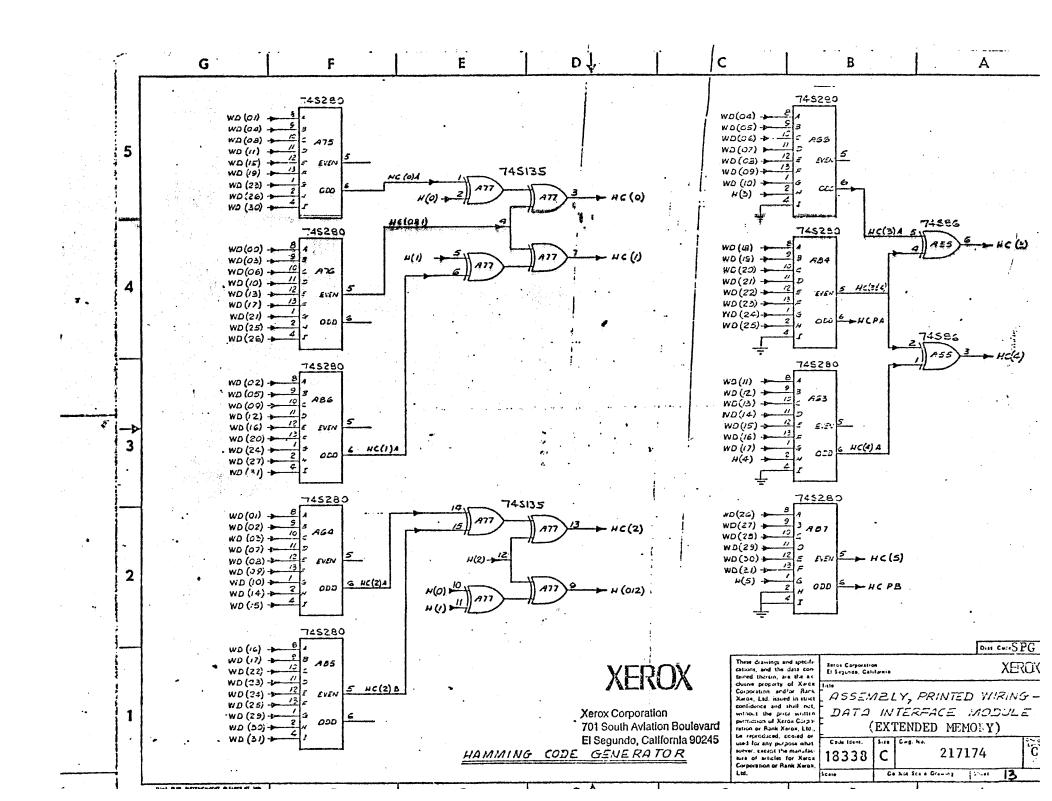


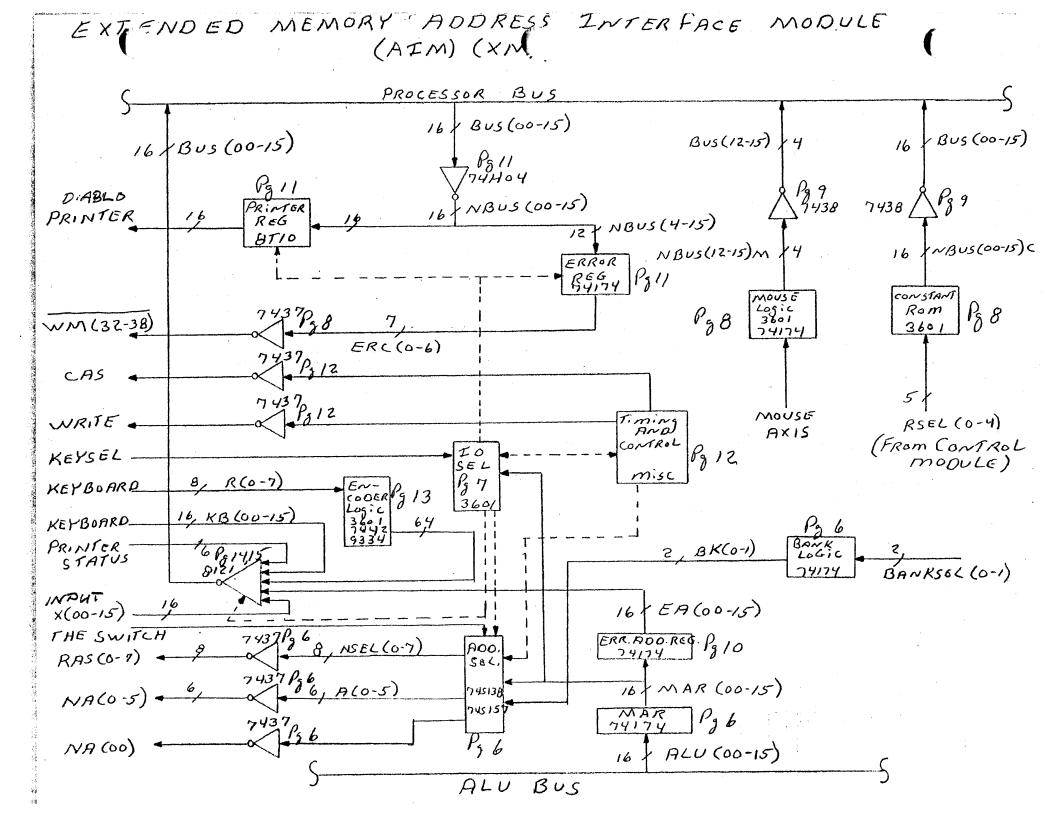


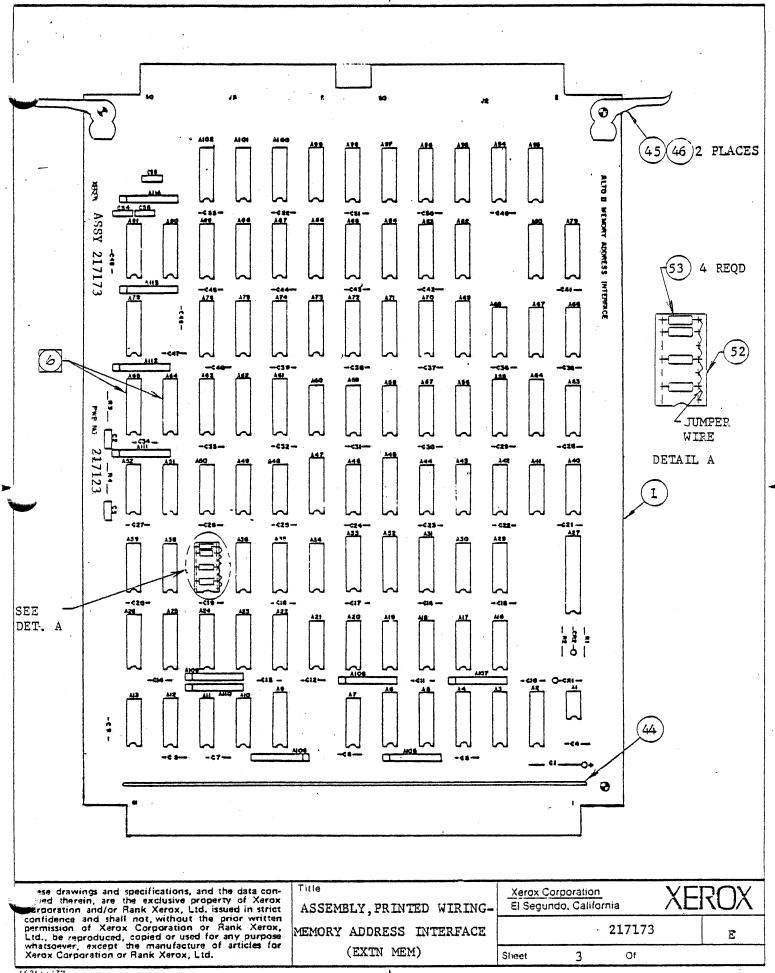












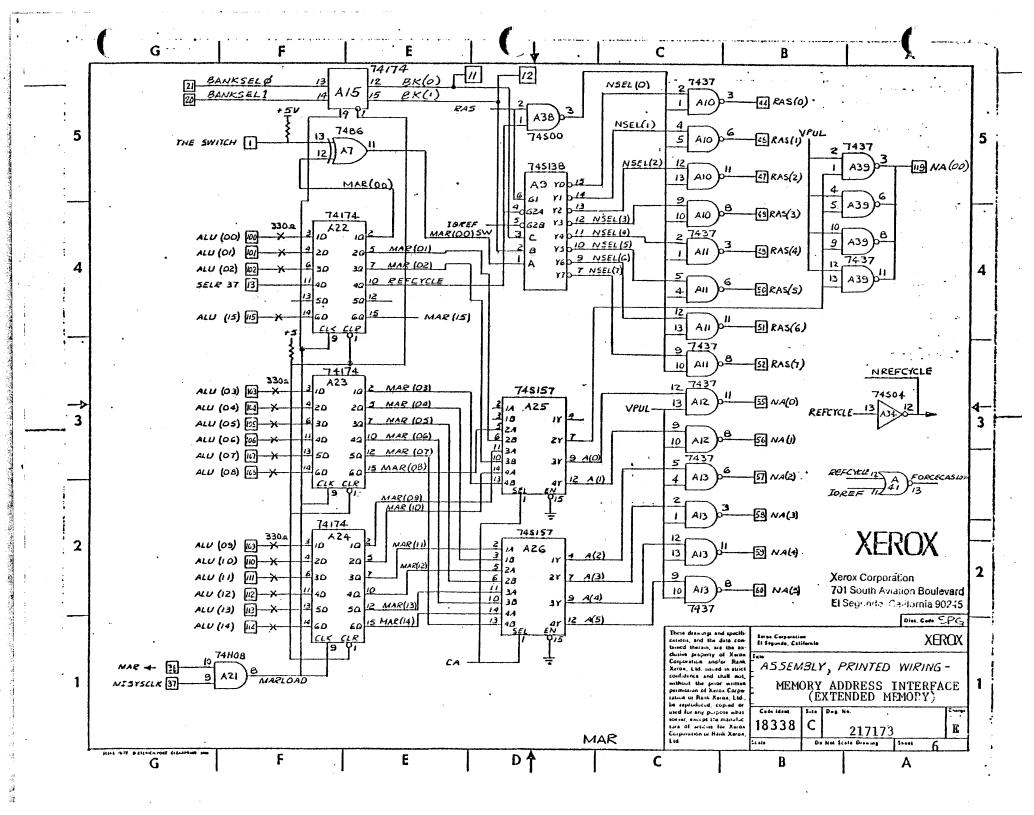
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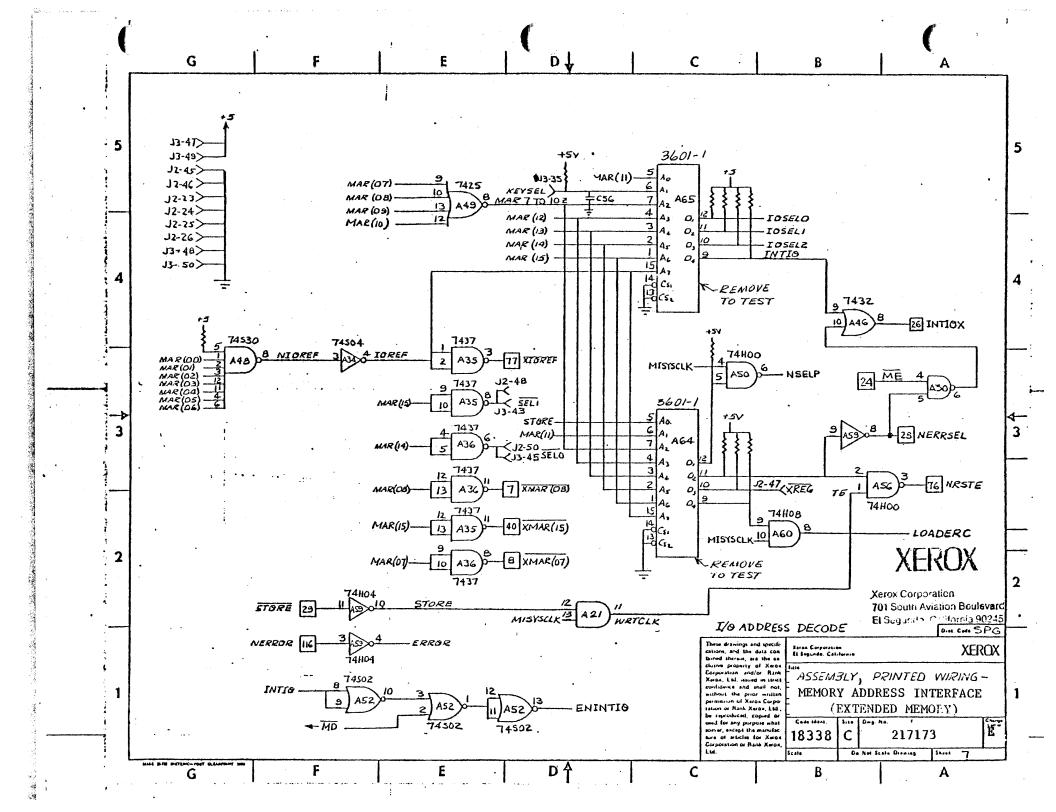


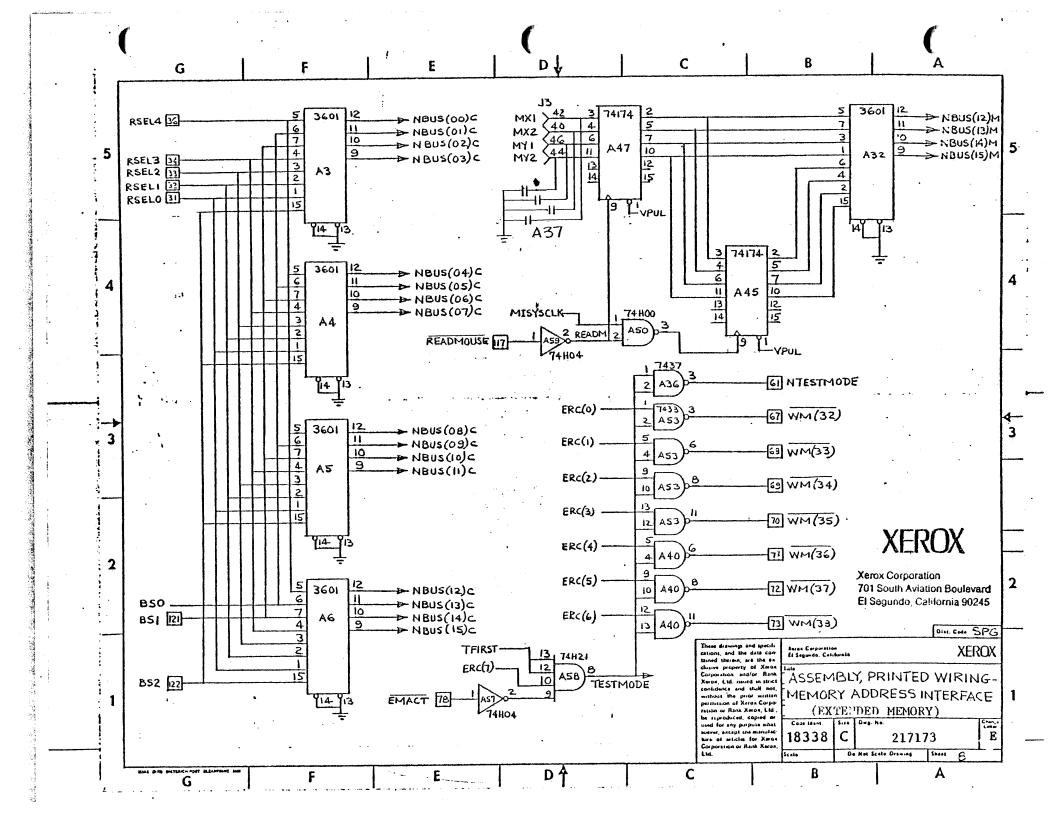
terial L		Y		ML	Drawing No. 2171	73	Rev
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MEM	ORY ADDRESS INTERFACE (EXTN MEM)	Model No. ALTO II XM	luare	3/10/		Sheet	
Item No.	Orawing Title	Drawing No.	No. Re		Remar	4 0)f
2	Board, P.W.	217123.	1		. Nettar	13	
2	Procedure, Test	216348	Ref.				
3	Spec, Module Assembly	2.16207	Ref				
4	Microcircuit 3601 - 1		9	۸3 :	A 6 A	20 1	
					hru A6, A	<u>32, A</u>	<u>.64</u>
5	7442		1	1	, A90, 91		
6	9334		8	A78	.1		
7	7438		7		thru A76		
8	7437		7		thru 20, 4		
				A36,	thru 13, A	.35	
9	74H04		 				
10	74504		5	1	59.66, c	57, 68	}
11	74H00		+	A34			
12	74500		2	A50,			
13	74H21		3	1	38, 44		
14	74H10		+	A58			
15	74H08		1	A29			
16	74\$138		2	A21,	60		
17	74164		1	A9			
18	74174		1	A42			
			1.1	A22,2	3,24,45,	1 7,54	,
19	74279			55,61	.62,63.1	5	
20	75451			A31,3			
21	74\$260		1	A1			
22	74109		1	A43			
23			1 .	A2			
24	7486		1 /	47			
25	8T10		4 /	479,80	0,93,94		\neg
26	74\$157		1	A25, A			\exists
27	74530		1 4	48			\exists
~	74502			A41,5	2		\exists
	7425	·	1 4	49			\dashv
.9	Microcircuit 8121				u A89		\dashv
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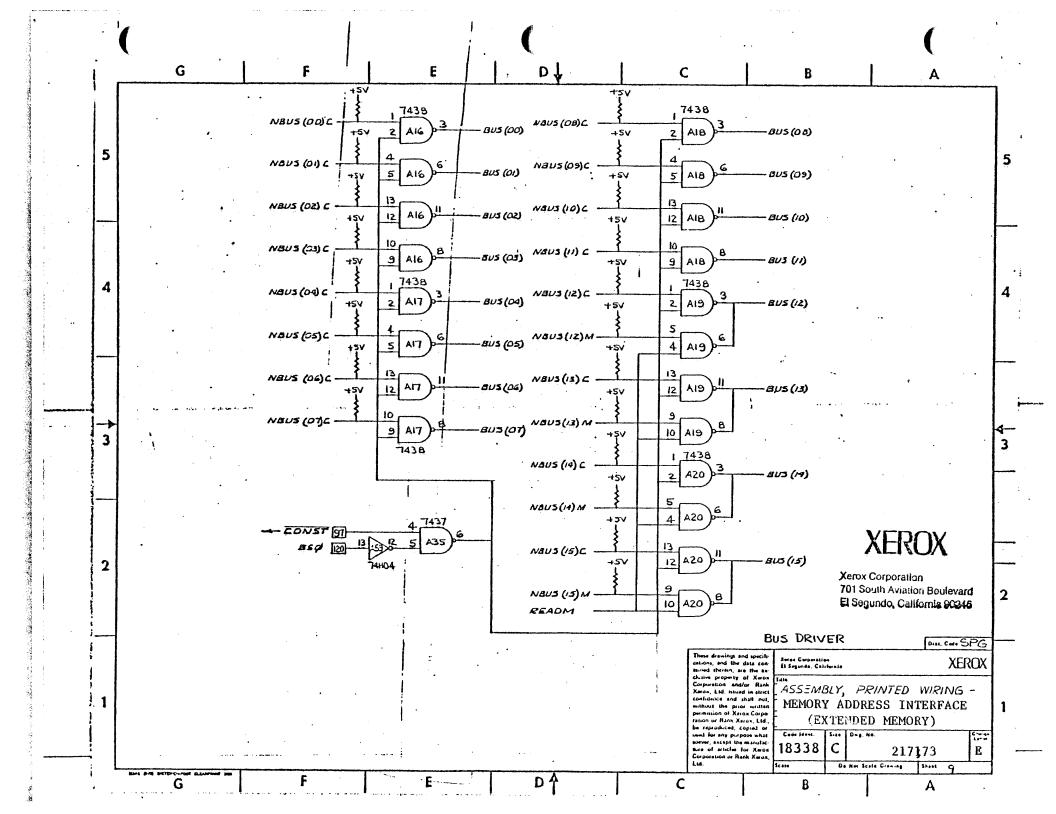


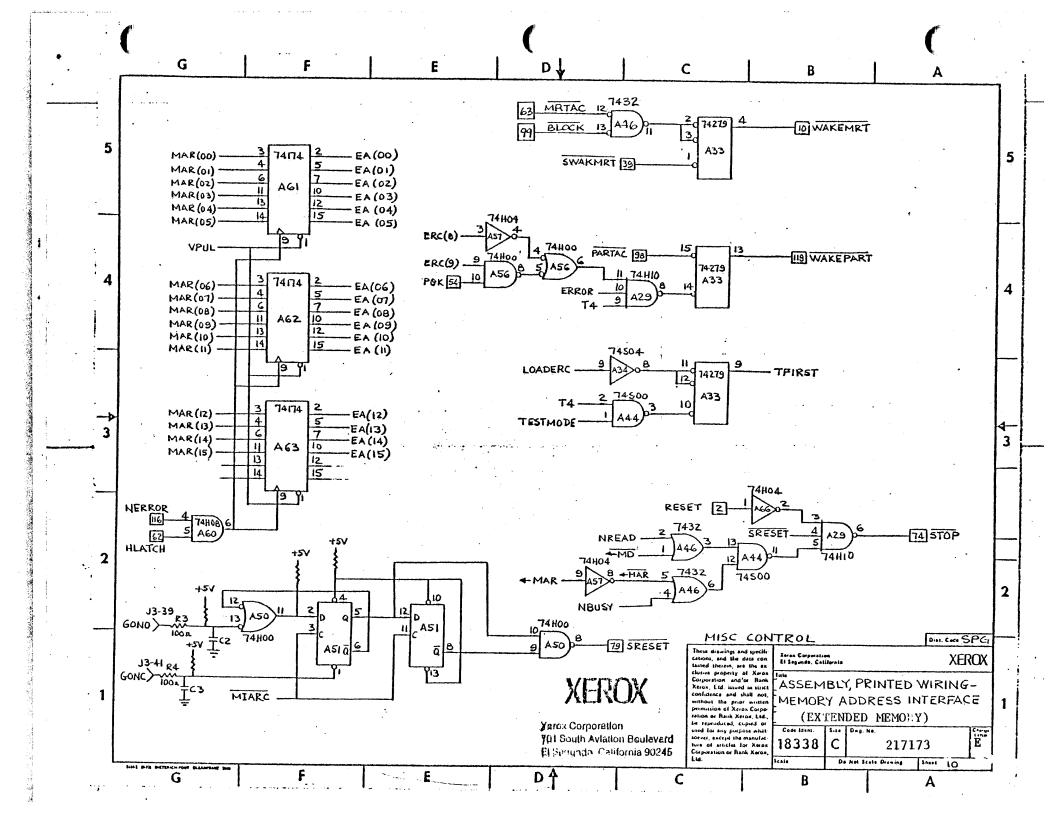
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217173		(EXTN MEM)	Model No. ALTO II XM	Date	3/10/78	Sheet	
2171	Item No.	Orawing Title	Drawing No.	No. Re		5 Remarks	Of
<u> </u>	30	Microcircuit 74H74		1	A51		
¥	31	Microcircuit, Delay Line	168865-002	1	A27		
	32						
	33	Resistor, Network, $330 \Omega + 2\%$	(750-81-R33	0) 4	A106,109	2.110 92	·
}	34	Resistor, Network, 1K + 2.5%, .16W	188600-007	7	A105,107		
-					A112, 11		
-	35	Resistor, Film, 100 Ω + 5%, 1/4W	116447-101	3	R2,3,4	~,,	-
-	36	Resistor, Film, 91 Ω + 5%, 1/4W	116447-910	1	R1		
}	37						
-	38	Diode, TN 123	111516	2	CR1, CR2		~
-	39				GRI CRZ	·	
-	40	Capacitor, Cer047 _u F ⁺ 20%, 50V	159487-009	2	C2, C3		
7	41	Capacitor, Tant, 22u F, + 20%, 15V	187720-005	1	C1 C1		
Y	42	Capacitor, Cer01µ F, 50V	188483-001	50	C4 thru C	-	
_	43	Capacitor, Cer022µF, ±20%, 50V	159487-002	3	C54, 55, 50		
_	44	Stiffener			(34, 33, 30) 	
	45	Extractor	216242 216250				
	46	Rivet	156111-005	2			
	47	Socket, Microcircuit 16 Pin		 			
	48	Socket, Microcircuit 14 Pin		54 39			
	49	Socket, Microcircuit 8 Pin					
	50	Socket, Merocircuit 22 Fin		1			
	51	Microcircuit 7432		1			
5	2	Adapter, Component (Inline Comp Co.#II	2.1/00)		A46		
5	3	Capacitor, 820 PF		1	·		5
			184344-011	4			(5)
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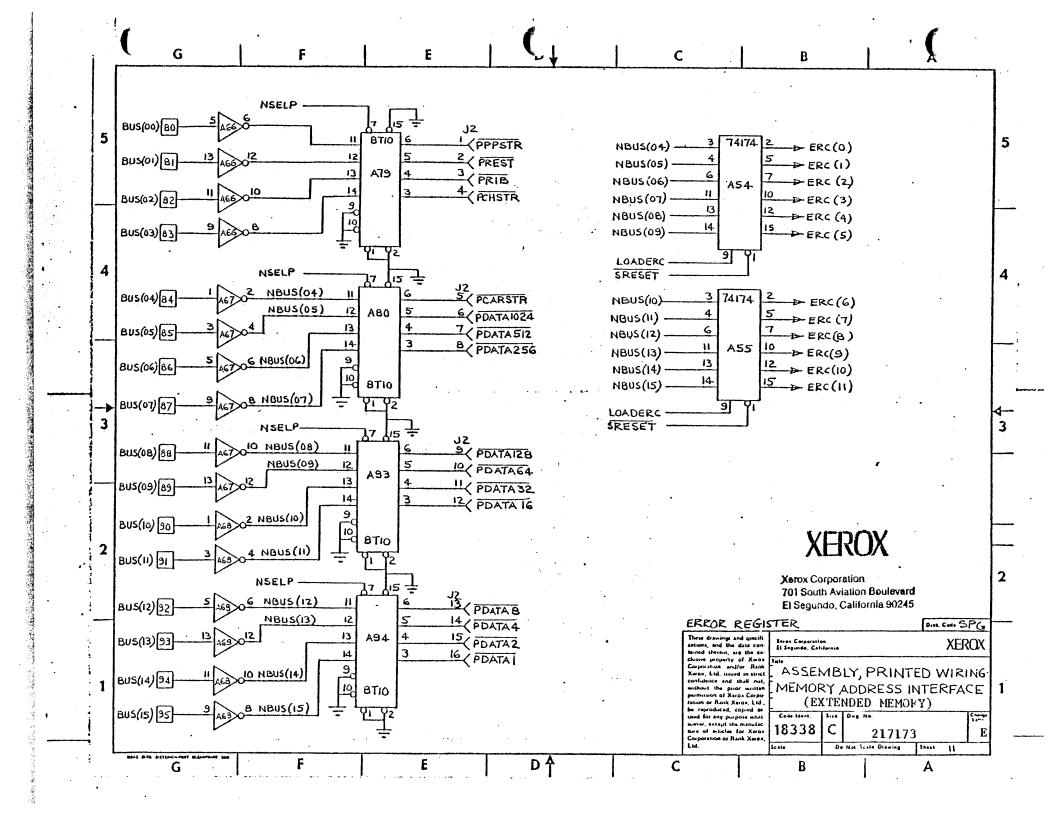


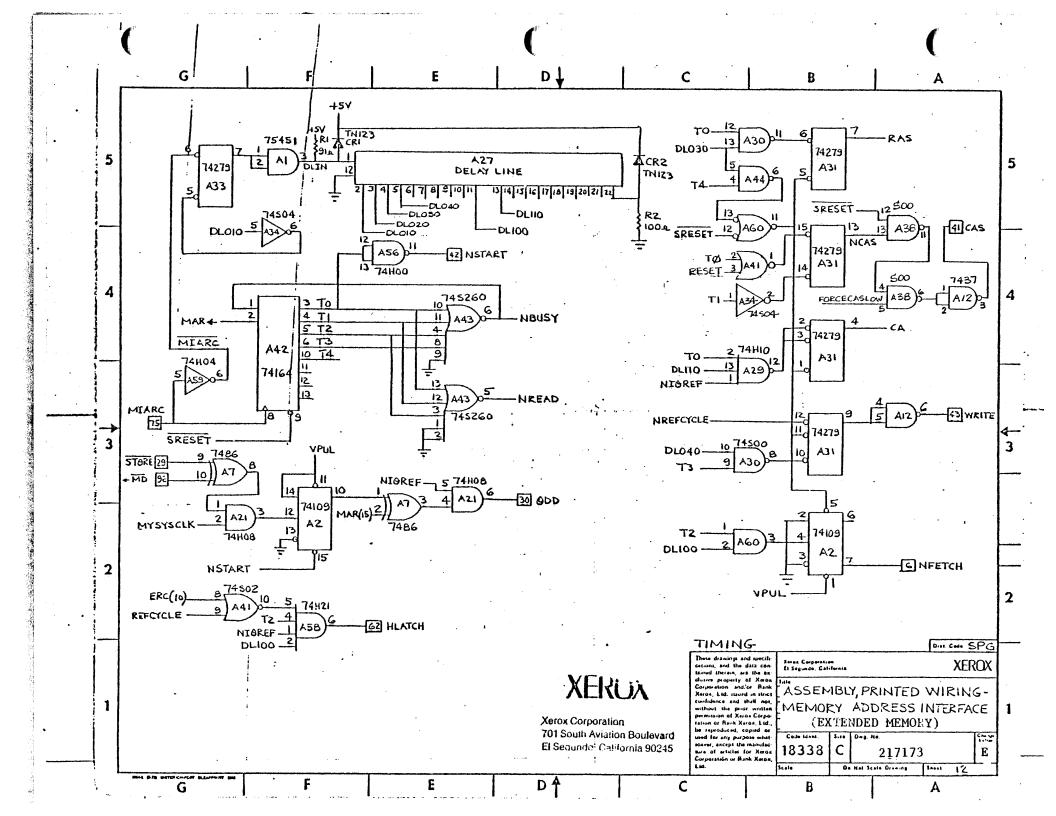


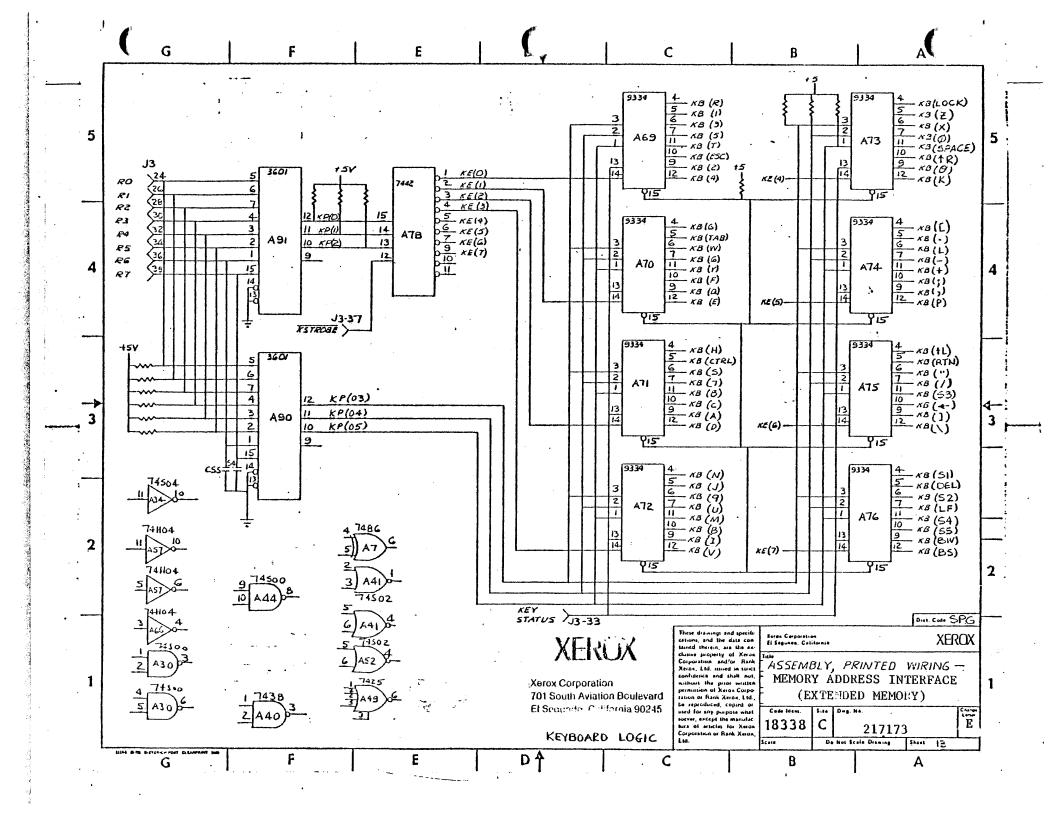


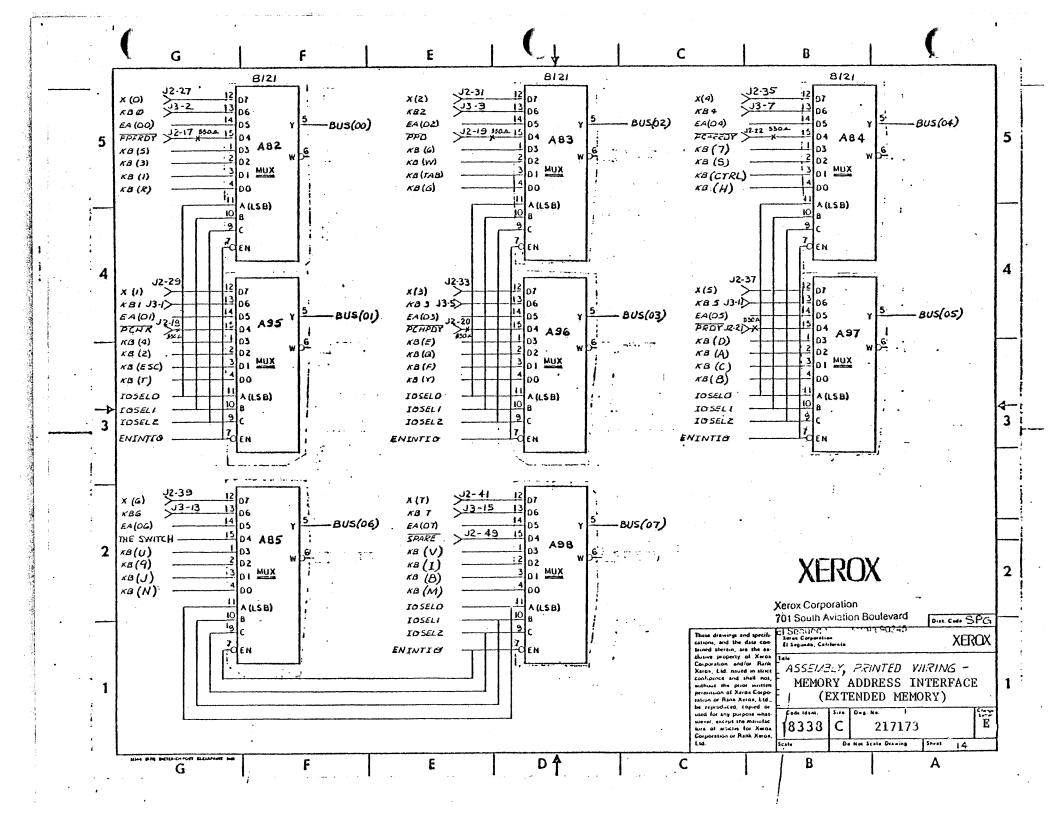


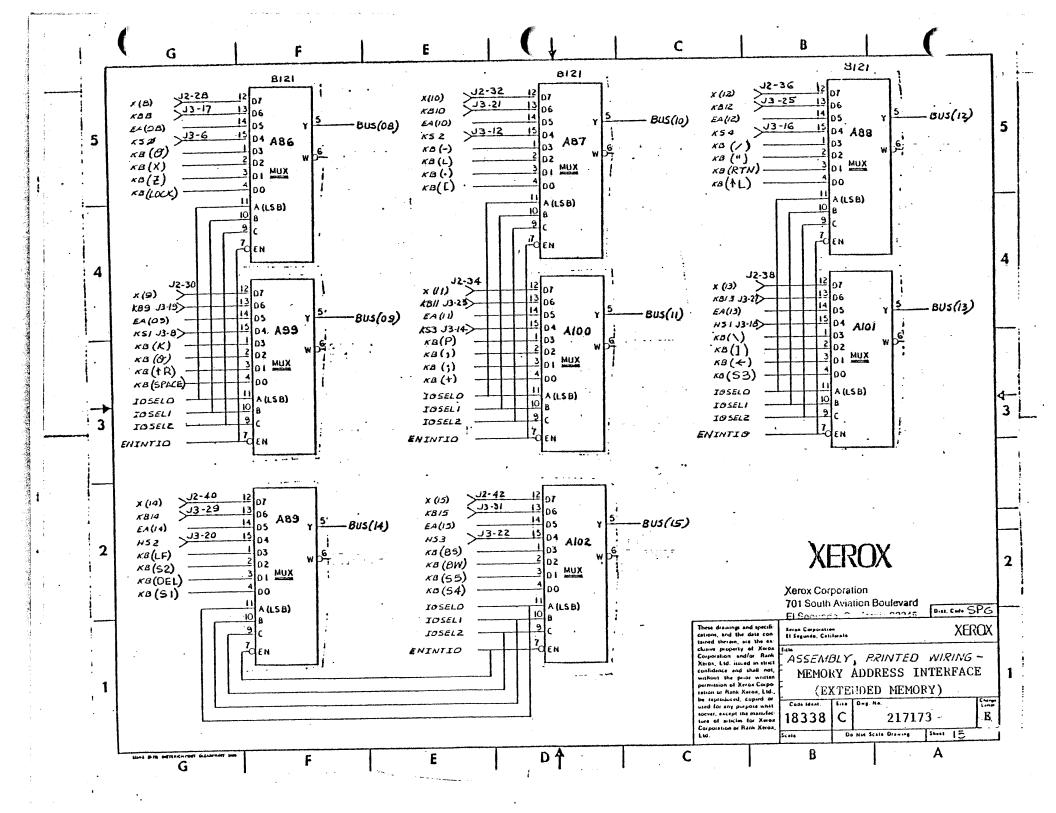


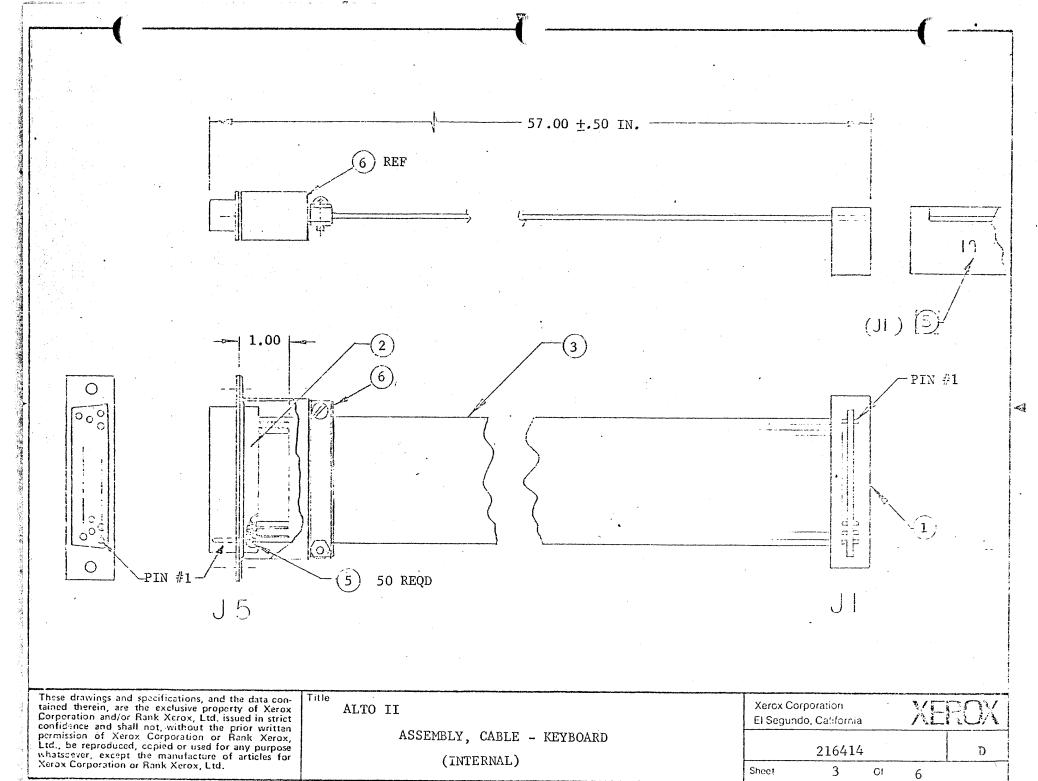












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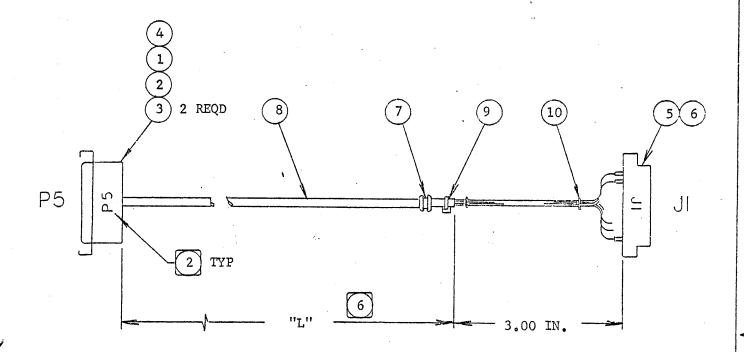
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	•					E		<u> </u>	• •
	Aire No.	Term	From	То	Term	Wire Type	Notes	Signal	Chg. Let.
	1		J1 - 1	J5 -50		3		KB1	
	2		2	17				КВО	
	3		3	49				KB2	
	4		4	33				SPARE	
	5	·	5	16			•	KB3	
	6		6	48				KS0	
	7		7	32				KB4	
	8		8	15			·	KS1	
	9 .		9	47				KEY SLOT	
	10		10	31	•			KEY SLOT	
	11		11	14				KB5	
	12		12	46				KS2	
	13		13	30				. KB6	
	14		14	13				KS3	
~	15		15	45				KB7	
	16	í	16	29				KS4	
	17		+ 17	12				KB8	
	18		18	44				MS1	
	19		19	28				KB9	
	20	Ì	20	11				NS2	
	21		21	43				KB10	
	22		22	27				MS3	
	23		23	10				КВ11	
	24		24	42				RO	
	25	ļ ļ	25	26				КВ12	
	26		J1 - 26	J5 - 9		3		R1	
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Wire No.	Term	From	То	Term V	Wire Typo	Notes	Signal	•	Ong. Let
27		J1 - 27	J5 -41		3		КВ13		
28		28	25				R2		
29		29	8				KB14		
30		. 30	40				R3		
31		31	24				KB15		_
32		32	7				R4		
33		33	39				KEYSTATUS		
34		34	23		Ì		R5		
35 .		35	6				KEYSEL		
36		36	38	•			R6		
37		37	22				KEYSTROBE		
38		• 38	5				R7		
39		39	37			,	GONO		
40		40	21				MX2		
41	İ	41	4				GONC		
42		42	36				MX1		
43		43	20				SEL1		ļ
44		44	- 3				MY 2		
45		45	35				SELO		ļ
46		46	19			¥.	MY1		
47		47	2				+5V		
<u> </u>		48	34				GND		ļ
49		49	18				+5V		ļ
50		J1 - 50	J5 -1		3		GND		ļ
							· · · · · · · · · · · · · · · · · · ·		
Those drawings tolored therein,	and specifica are the exclu	tions, and the data con- isive property of Xerox ox, Ltd. issued in strict	Ref Item No's in Apmaterial List.		Title	ALTO II	Xerox Corporation El Segundo, California	XE	RO
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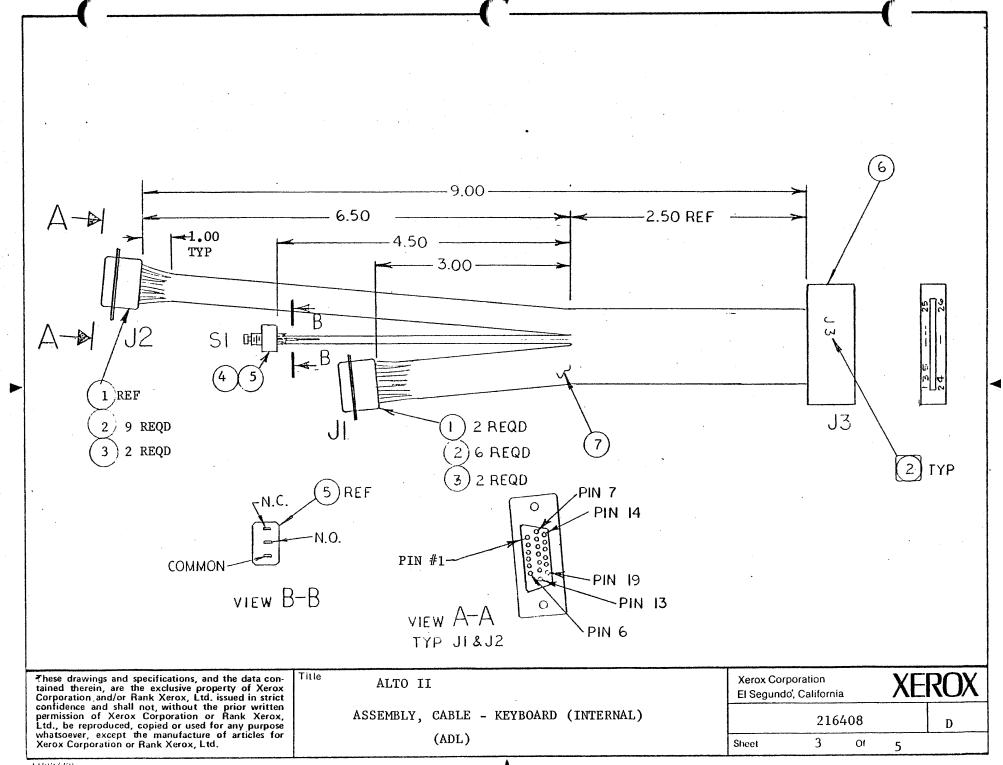
ASSY, CABLE - KEYBOARD

(ALTO II/MICRO SW)

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Sheet	3	Of	5	

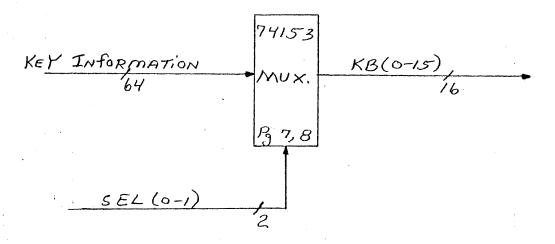
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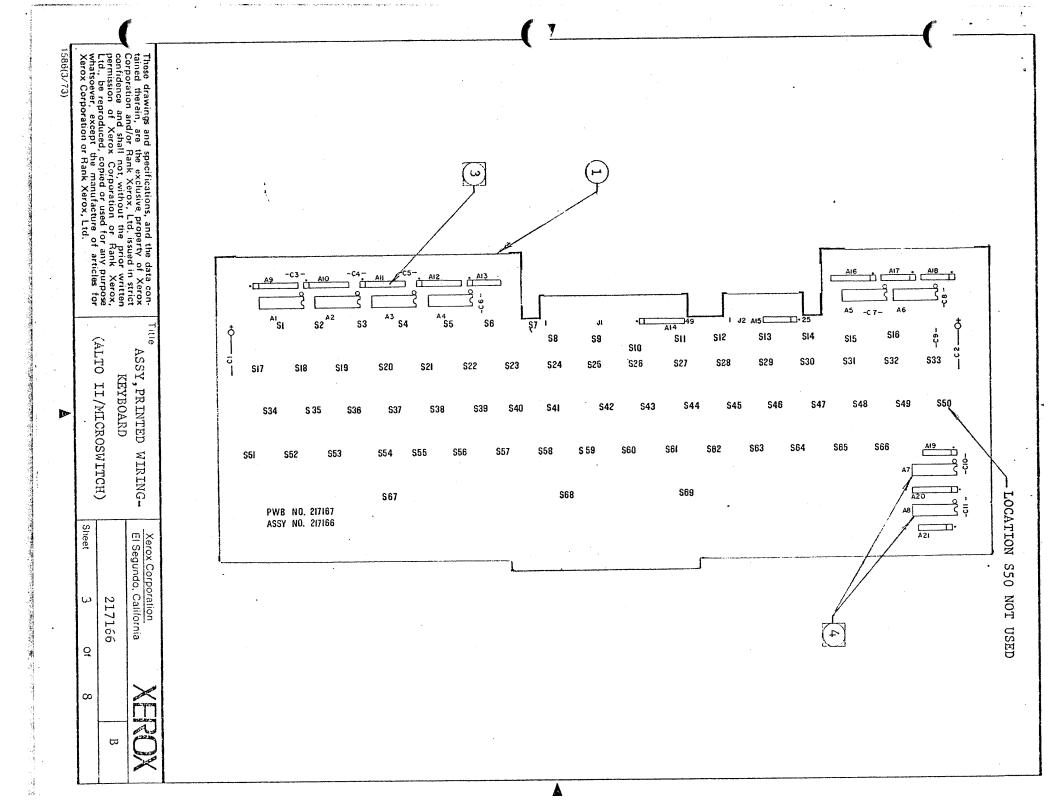
WIRE NO	TERM		FROM		то	***************************************	TERM	WIR	E	NO	res	SIGNA	L	CHG
1			P5-37		J1- 1	7				BLU/E	3LK	GON	0	
2			17		В	<u> </u>				PUR		KBO		
3			50		A	***************************************				YEL/E	BLK	KB1		
4			49		1					BLU		KB2		
5			16		3					YEL/E	RN	КВ3		
6			32		C					GRN		KB4		
7			14		4					YEL/R	ED	KB5	············	
8			30		2					YEL		KB6		
9			45		D					YEL/C	RG	KB7	*******	_
10			12		8				,	ORN	****	KB8		
11			28		J					YEL/G	RN	· KB9	·····	
12			43		Н					RED	***************************************	KB10		
13	_		10		7					YEL/BI	_U	KB11		
14			26		E					BRN		KB12	·····	
15			41		5					YEL/PL	JR	KB13	~	
16			8		F					BLK		KB14		
17	\ <u> </u>		24		6					YEL/GI	?Y	KB15		
18			35		K					WHT		SELO'		
19	ļ		20		9					GRY		SEL1'		
20	ļ		48		14					ORN/BI	_K	KS0	-	
21	ļ		15		16					ORN/BI	RN	KS1		
22	<u> </u>		46		s					ORN/RE	ED .	KS2	·	
23			13		15					ORN/BL	.U	KS3	'	
24			29		13				•	ORN/PL	JR	KS4		
25			44		Z					WHT/BL	.K	MS1	•	
26			11		25					. WHT/BR	3N	MS2		
27			27		Y					WHT/RE	.D	MS3		
28			36	_	Х	···				WHT/OF	RN	MX2		
29			21		24					WHT/YE	L	MX1	***************************************	
30			19		23					WHT/GF	BN.	MY1		
31			3		W	·				WHT/BL	U	MY2		
32			4		Т					BLU/BR/	1	GONC		
33			2		Ų				\perp	18 AWG	WHT	+5V		
34			34		N					18 AWG	BLK	GND		
35			18		V					GRN/OR	N	+5V		
36		F	P5-1	J1	- P			ļ	\perp	PUR/WH	Г	GND		
37						····		<u> </u>						
38										4				
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40				<u></u>	**************************************									
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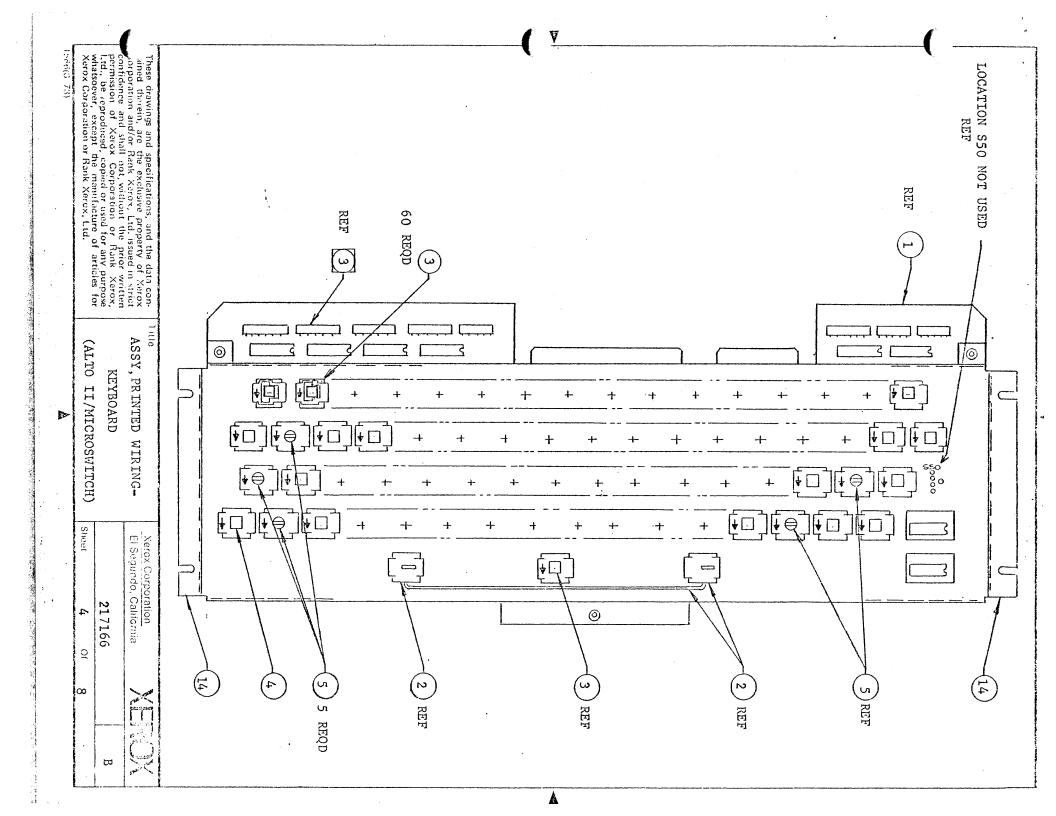


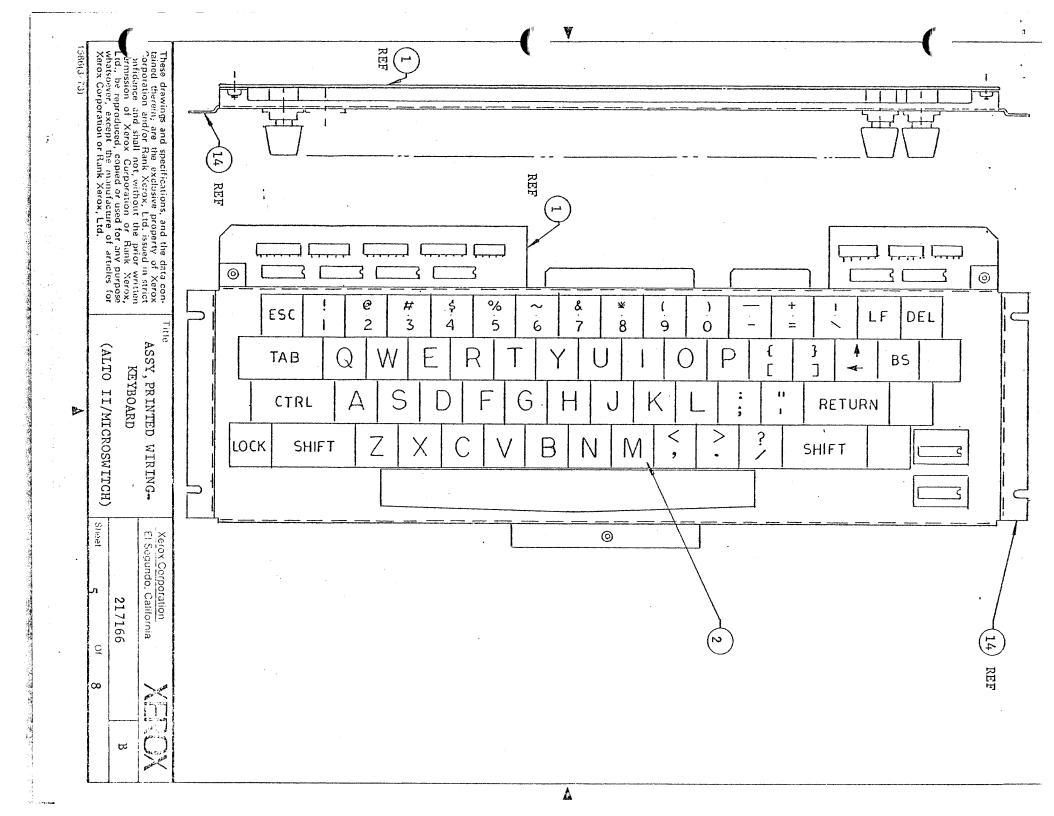
	•									1 _	
ſ	Wire No.	Term	From	То	Term	Wire Type	Notes		Signal		Chg. Let.
f	1		J3 - 18	J2 - 11		7			KS0		
ŀ	2		17	10					KS1		
ŀ	3		20	9					KS2		
ŀ	4		19	8		•			KS3		
ł	5	·	22	J2 - 7					KS4		
f	6		4	J1 - 14					MS1		
t	7		3	16					MS2		
ľ	8		6	15					MS3		
	9		5	1 .					MX1		
ł	10		8	2					MX2		
f	11		. 7	- 3					MY1		
-	12		10	J1 - 4					MY2		
ŀ	13		14	S1 - N.O.					GONC		
ŀ	14		11	S1 - N.C.					GONO		
>	15		9	J1 - 6					+5 VDC		
-	16		2	J1 - 19					DC GND		
	17		25	J2 - 19					DC GND		·
	18		J3 - 12	S1 - C		7					
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	tained therein,	are the exclu	ions, and the data con- sive property of Xerox ox, Ltd. issued in strict	1. Ref Item No's in Appli Material List.			ALTO II	AUV DU	Xerox Corporation El Segundo, California	XE	ROX
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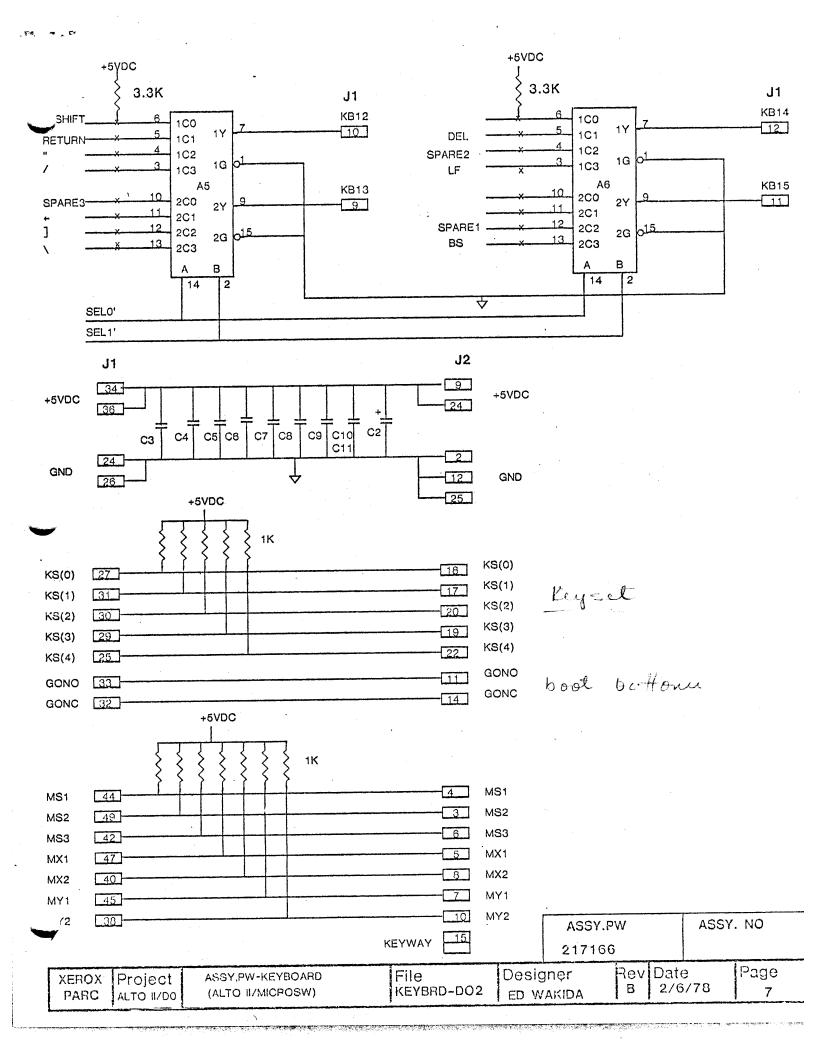


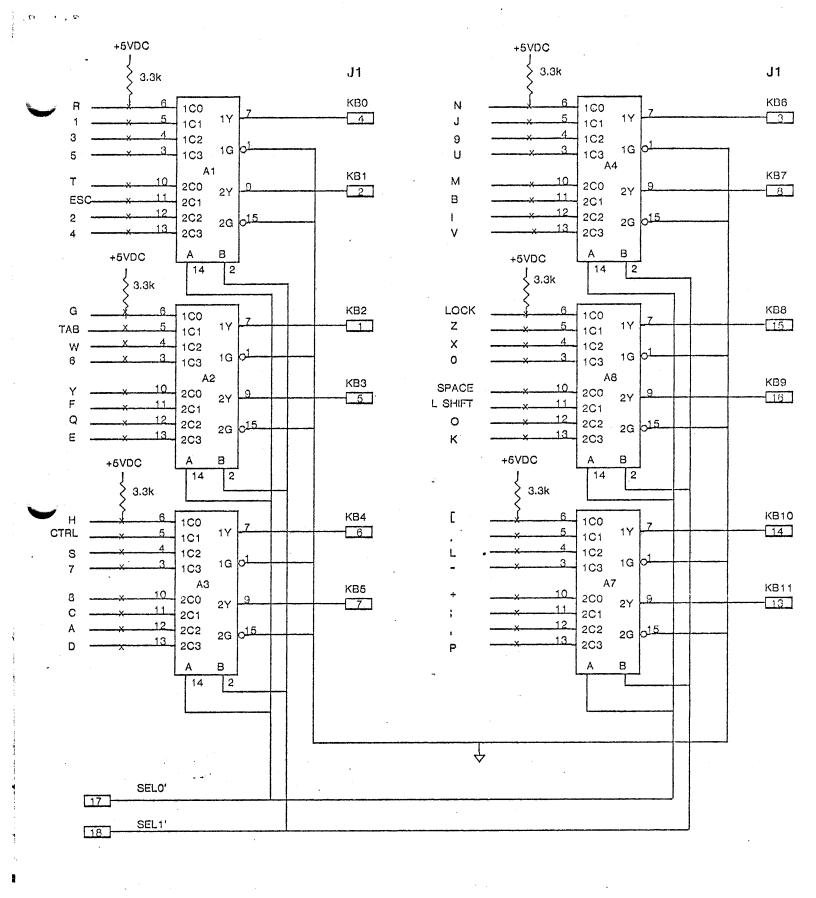






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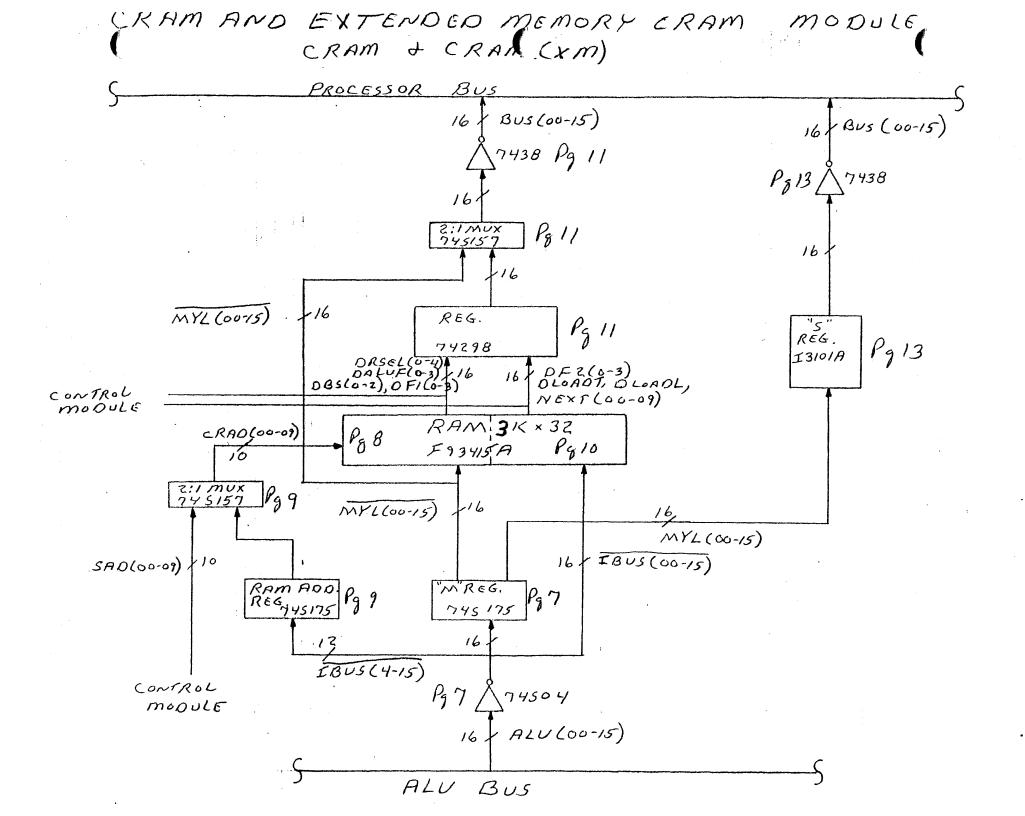
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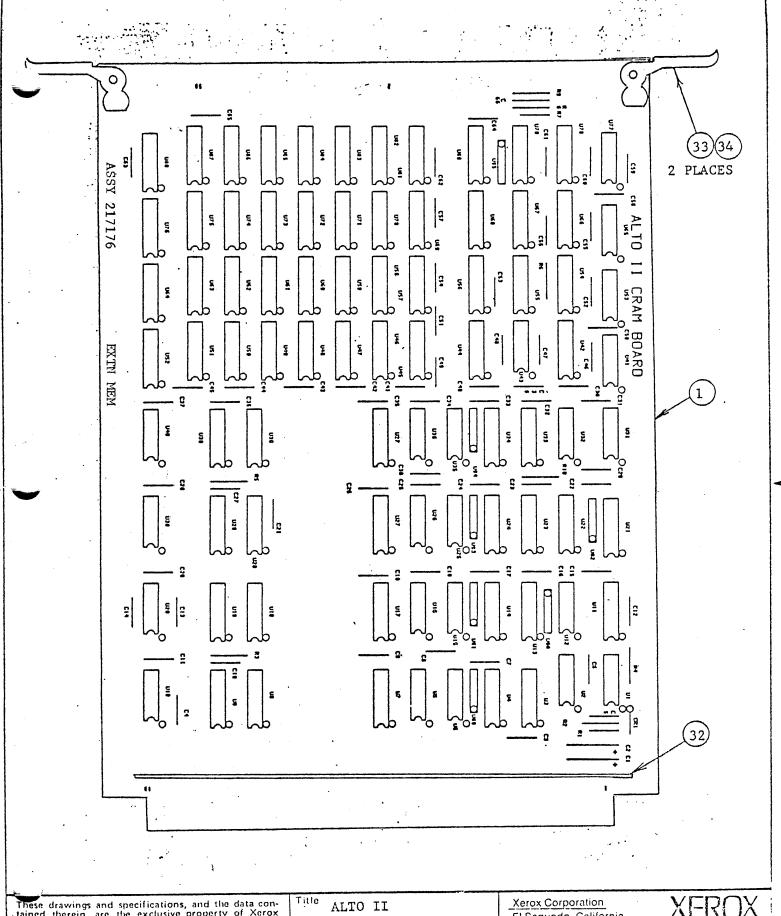
CONTROL RAM

The Control Ram is a standard logic card containing a fast (90 nsec.) 1024-word by 32-bit read/write microinstruction memory, an even faster (40 nsec.) 32-word by 16-bit read/write memory (S registers), and logic to interface those memories to the Alto's microinstruction bus, processor bus, and ALU output. Unlike other microinstruction memories in the Alto, the larger memory of the Control Ram can hold microinstructions and/or data.

RAM-RELATED TASKS

The Control Ram performs data manipulation (as distinct from microcode fetching) functions in reponse to the microinstruction. Not all tasks are likely to be interested in these functions. Moreover, not all tasks will have the appropriate values for the microinstruction uncommitted. A Ram-related task is defined as one during whose execution the Control Ram card will respond to microinstructions. The standard Alto is wired so that the CPU emulator task is the only Ram-related task.





ASSEMBLY, PW-

CRAM (EXTN MEM)

Xerox Corporation El Segundo, California	XE	ROX
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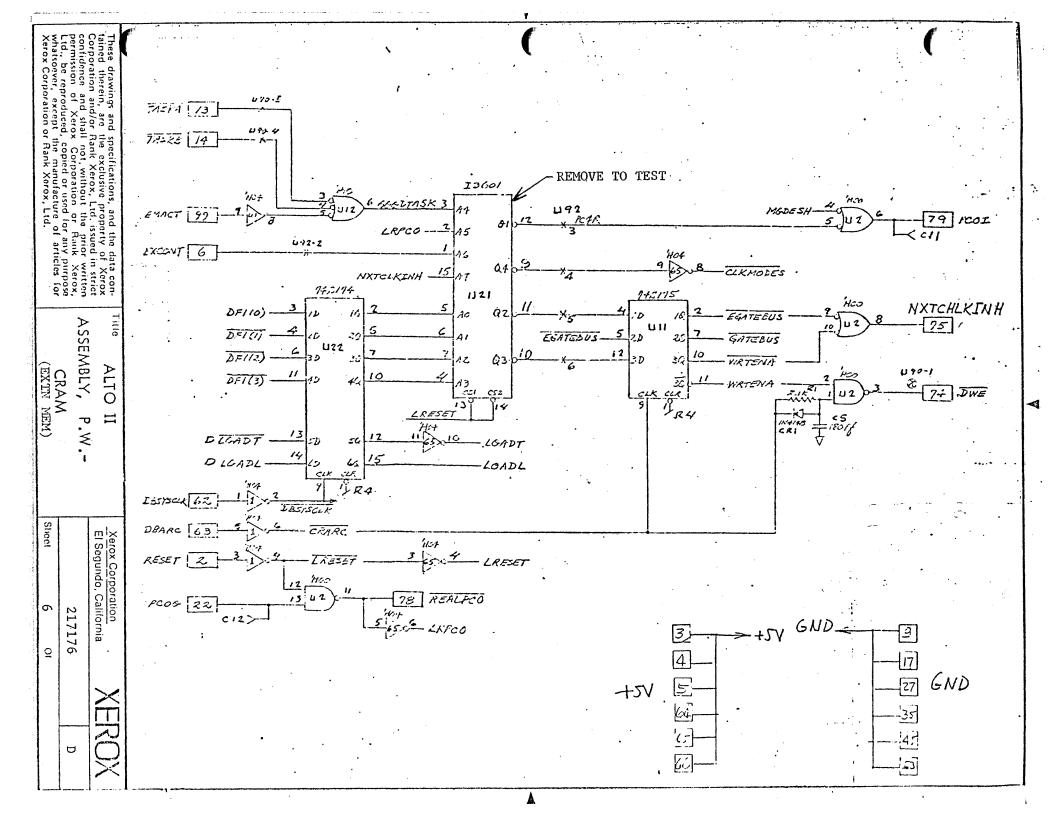
Drawing No. Material List 217176 rawing Title These drawings and specifications, and the data contained there-ALTO II in, are the exclusive property of Xerox Corporation and/or Rank ASSEMBLY, PRINTED WIRING-Xerox, Ltd. issued in strict confidence and shall not, without the prior written permission of Xerox Corporation or Rank Xerox. Ltd., be reproduced, copied or used for any purpose whatsoever, except the manufacture of articles for Xerox Corporation or Rank Xerox, Ltd. CRAM Date 3/10/78 Model No. 217176 ALTO II XM (EXTN MEM) No. Req. Drawing Title Drawing No. 'tem No. Remarks 1 BOARD, P.W, 217135 PROCEDURE, TEST REF 216366 Z REF SPEC, ASSEMBLY 216207 4 U2, 53-2 5 MICROCIRCUIT, SN 74H00 T.I. U1,65 SN 74H04 6 4 U10, 20, 30, 40 7 SN 74S04 U12,41 8 SN: 74H 10 U32 9 SN 74H21 2 U31,43 10 SN 74H30 U5, 6, 15, 16, 25, 26, SN 7438 11 35,36 U54 SN 74153 12 U7, 17, 27, 37, 56, 68, 80 13 SN 74S157 2 U66,78 14 SN 74166 2 U22,44 15 SN 74S174 U9, 11, 19, 29, 39, 42, 55, 16 SN 74S175 67,79 17 T.I. SN 74298 U8, 18, 28, 38 **U77** SN 74S03 1 18 19 INTEL U3, 4, 13, 14, 23, 24, 33, 34 I 3101A 20 21 INTEL U21 I 3601-1 FAIRCHILD(3) U45-52, 57-64, 69-76, MICROCIRCUIT, 22 F93415A 81-88 SOCKET, 14 PIN DIP, AUGAT#514-AG 11D 23 22 SOCKET, 16 PIN DIP, AUGAT#516-AG11D 24 66 25 DIODE, IN4148 26 CR1 · 27 28 29 30

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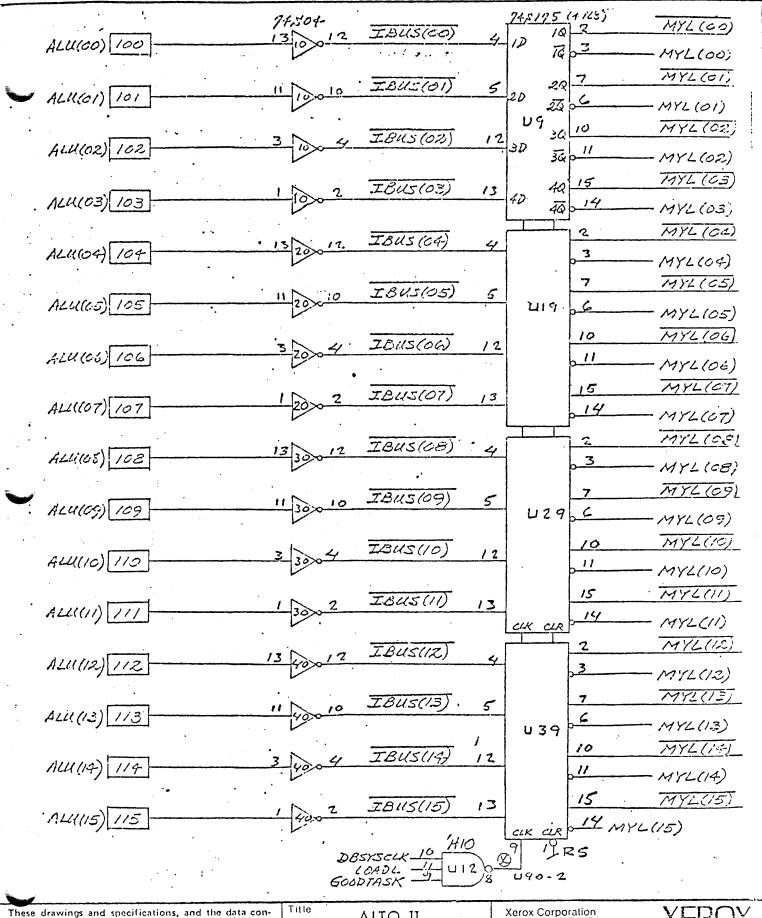
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Drawing No. Mararial List 217176 These drawings and specifications, and the data contained there-in, are the exclusive property of Xerox Corporation and/or Rank Xerox, Ltd. issued in strict confidence and shall not, without the rawing Title ALTO II prior written permission of Xerox Corporation or Rank Xerox, Ltd., be reproduced, copied or used for any purpose whatsoever, except the manufacture of articles for Xerox Corporation or Rank Xerox, Ltd. ASSEMBLY, PRINTED WIRING-**CRAM** Date 3/10/78 Model No. ALTO II XM (EXTN MEM) Drawing Title Drawing No. No. Req. Remarks Item No. C1, C2 187720-005 CAPACITOR, 22µF ±20%, 15V 31 C3, C4, C6 THRU 65 188483-002 62 CAPACITOR, .05µF 32 C66-- 2 33 -CAPACITOR, .. 022µF 1 C5 34 CAPACITOR, 180PF 35 R7-116447-201 RESISTOR, FILM, 200 Ω , $\pm 5\%$, 1/4W36 RESISTOR, FILM, 1K Ω , ±5%, 1/4W R3 thru 6, 8, 9 116447-102 6 37 RESISTOR, F1LM, 2.7K, ±5%, 1/4W 1 R 1 116447-272 38 RESISTOR, NETWORK, 330 Ω , CTS 750-61-R33 ϕ U89 THRU U95 30 R 10 31 RESISTOR, FILM, 330 Ω , ±5%, 1/4W 116447-331 32 STIFFENER 216242 2 33 EXTRACTOR 216250 34 RIVET 156111-005 2







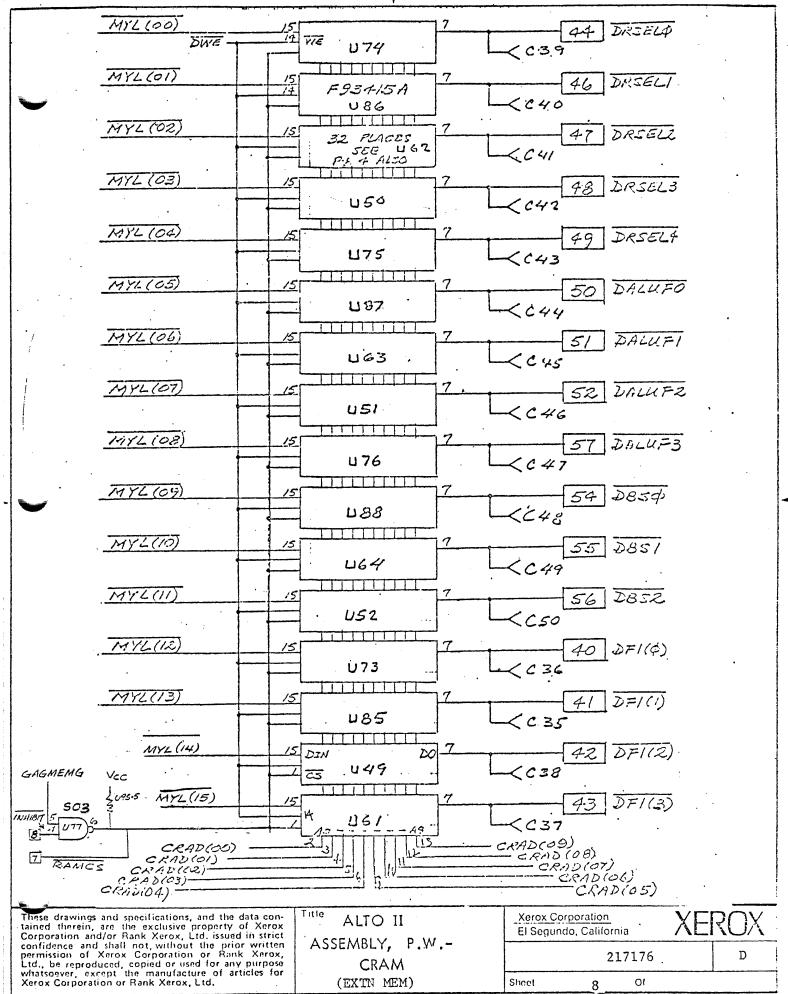
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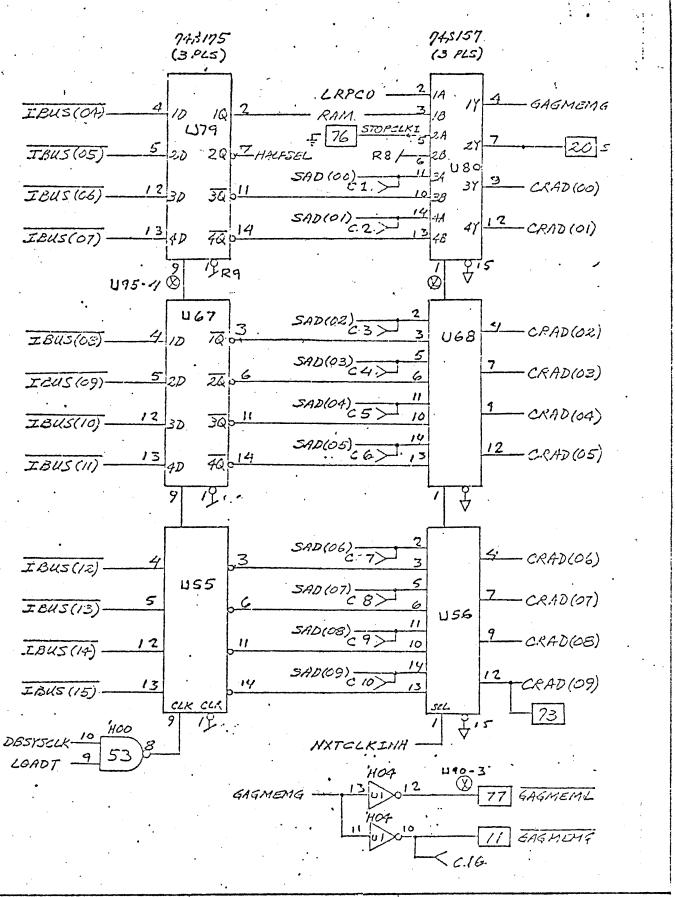
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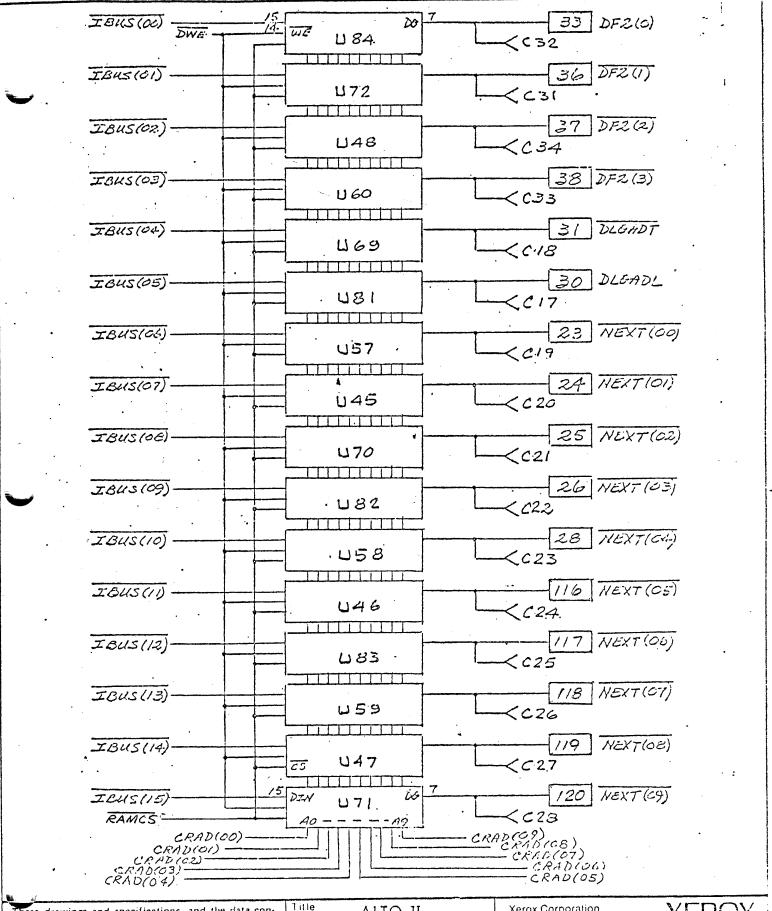


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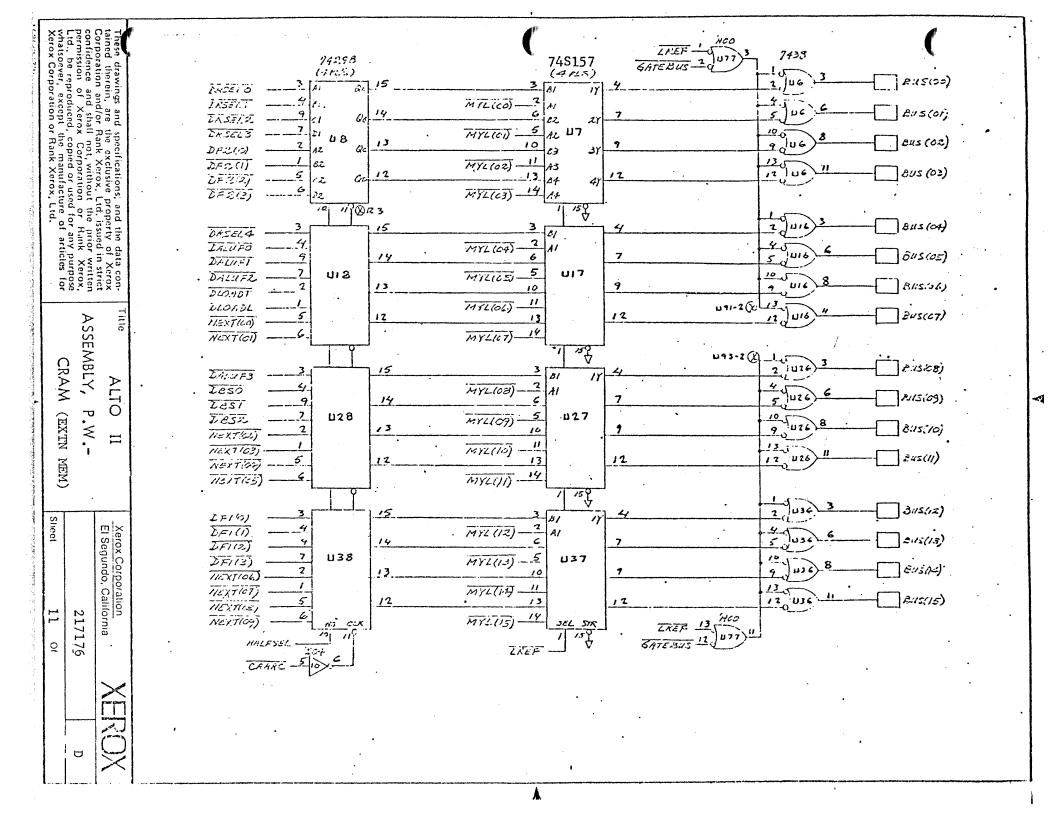
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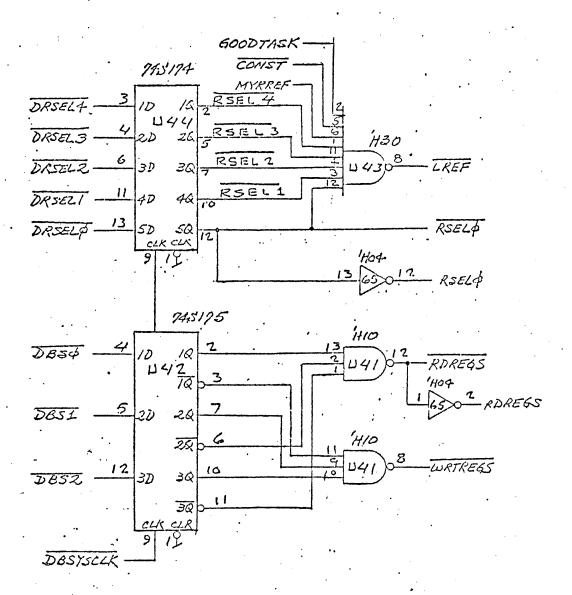


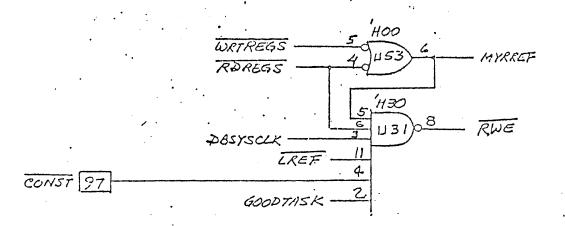


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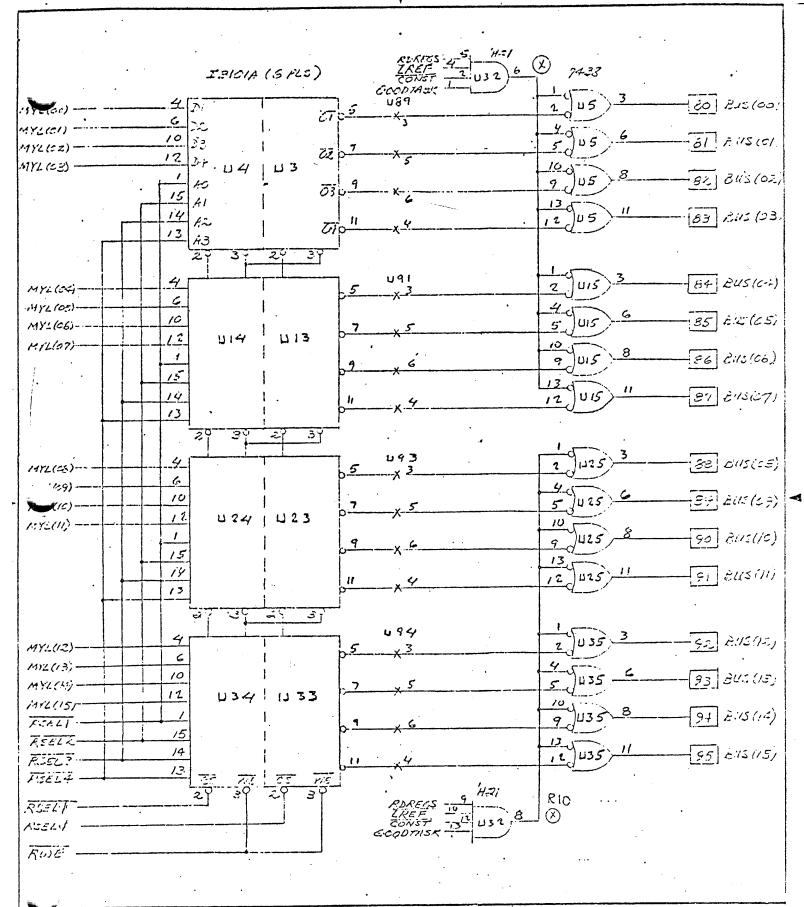




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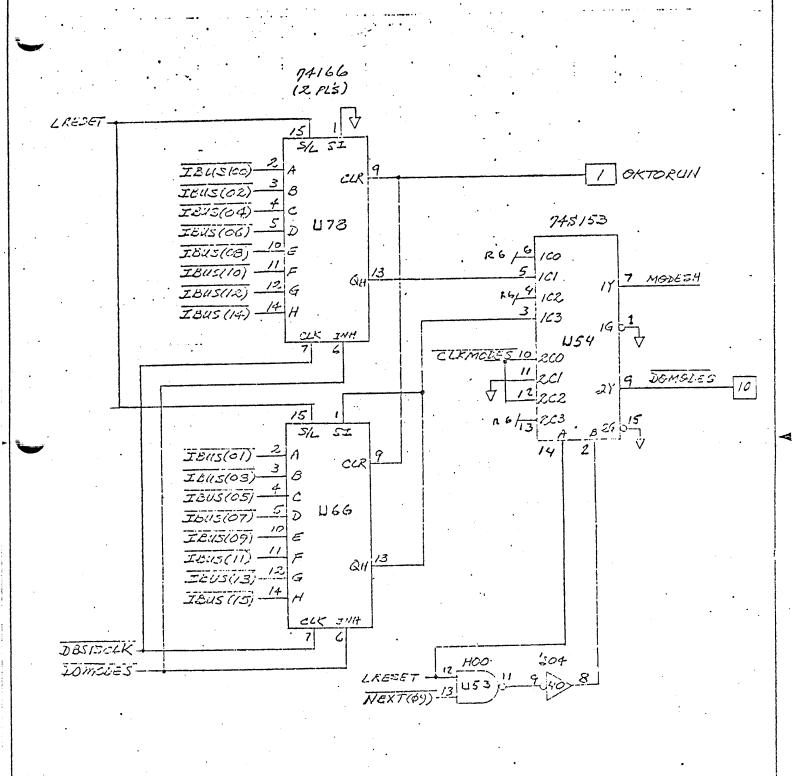
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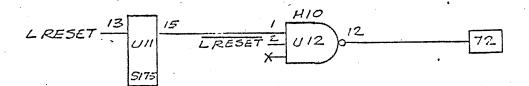
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MICROPROCESSOR CONTROL

Control of the Alto microprocessor is shared among 16 "tasks" arranged in a priority order. The tasks are numbered 0 to 15: 0 is the lowest priority and 15 is the highest. The lowest priority task is the emulator task which fetches instructions and executes them.

BRANCHING

The microprocessor offers a limited branching capability which, although somewhat cumbersome, has proven adequate for chores undertaken by Alto microcode. The basic idea is that special microprocessor functions may modify the NEXT field, and consequently, after the flow of control. Modification is accomplished by ORing various bits into the NEXT field.

TASK SWITCHING

Only one of the 16 tasks is executing microinstructions at any one time. Once a task begins execution, it continues to execute until it invokes a task switch function that enables switching to another task.

A task is considered eligible for execution if its hardware-generated "wakeup signal" is asserted (these signals are not accessible to the microprogram). The wakeup signals enter a priority encoder that calculated the number of the highest-priority eligible task. When a running task invokes a task switch, control will switch to another task only if a higher priority task has a wakeup signal held true, or if the current task no longer has a wakeup signal true. In the latter case, control goes to a lower priority task. The lowest priority task is the CPU emulator, which is always requesting wakeup.

CONTROL, EXTENDED MEMORY CONTROL, ZK CONTROL AND EXTENDED MEMRY ZK CONTROL MODULES (xm) CRAM CPROCESSOR BUS MODULE BUS (00-15) NEXT (00-09) 10 , NEXT (00-09) moo. 10 NEXT(00-09) 745280 REG 2,5Rom(0-PROM IK×3Z 10, NEXT (0-9) 18 MUX 745157 745174 SAD(0-9) CUR. TASK WAKEUP En-CooER V28 SigNALS 745174 RAM REG. 10 ZSZ3 74504 F9318 I3101A MODULE 120 CT 1,2,4,8 RAD 1, 2,4,8 2:1 MUX 745157 0F7(0-3), DF1(0-3) OGEODER DECOD & O REG. FUNCTIONS 13205 745174 OBYUDER CONTROL L3205 085(0-2) SIGNALS DCT1,2,4,8 REG. ALUF (1-4) REG 748174 7441) SELR 37 Active TASK 05000 3205 FOAIM ACTIVE MODULE TASKS

NOTES: UNLESS OTHERWISE SPECIFIED

- 1. ASSEMBLE PER ALTO II MODULE ASSY SPEC, DWG NO. 216207.
- THIS ASSEMBLY REPLACES ASSY NO. 216642 WHICH IS OBSOLETE.
- 3. SPECIAL INSTRUCTIONS TO INSTALL 2ND K OF PROMS
 - A. THE ALTO II CONTROL MODULE (2K) IS SHIPPED WITH THE 1ST K OF PROM INSTALLED ONLY AND THE MODULE IS OPERATED WITH THE REST OF THE ALTO II SYSTEM EXACTLY THE SAME WAY AS THE OLD CONTROL MODULE.
 - B. A SET OF VIRGIN PROMS FOR THE 2ND K ADDRESS LOCATIONS ARE PROVIDED WITH THE MODULE AND PLACED IN THE IC LOCATION WHERE THE 2ND K PROM IS NORMALLY INSTALLED.
 - C. WHEN THE 2ND K OF PROM IS INSTALLED, THE "SW MODE. 1K" SWITCH MODE CONTROL PROM (U50) MUST BE REPLACED WITH THE CONTROL PROM "SW MODE. 2K" WHICH IS ALSO PROVIDED AND PLACED IN THE SPARE LOCATION U76.
 - D. THE PLACEMENT OF THE 2ND K PROM IS AS FOLLOWS:

BITS	LOCATION
0 - 3	U54
4 - 7	U74
8 - 11	U75
12 - 15	u73
16 - 19	
20 - 23	U70
24 - 27	U71
28 - 31	U72

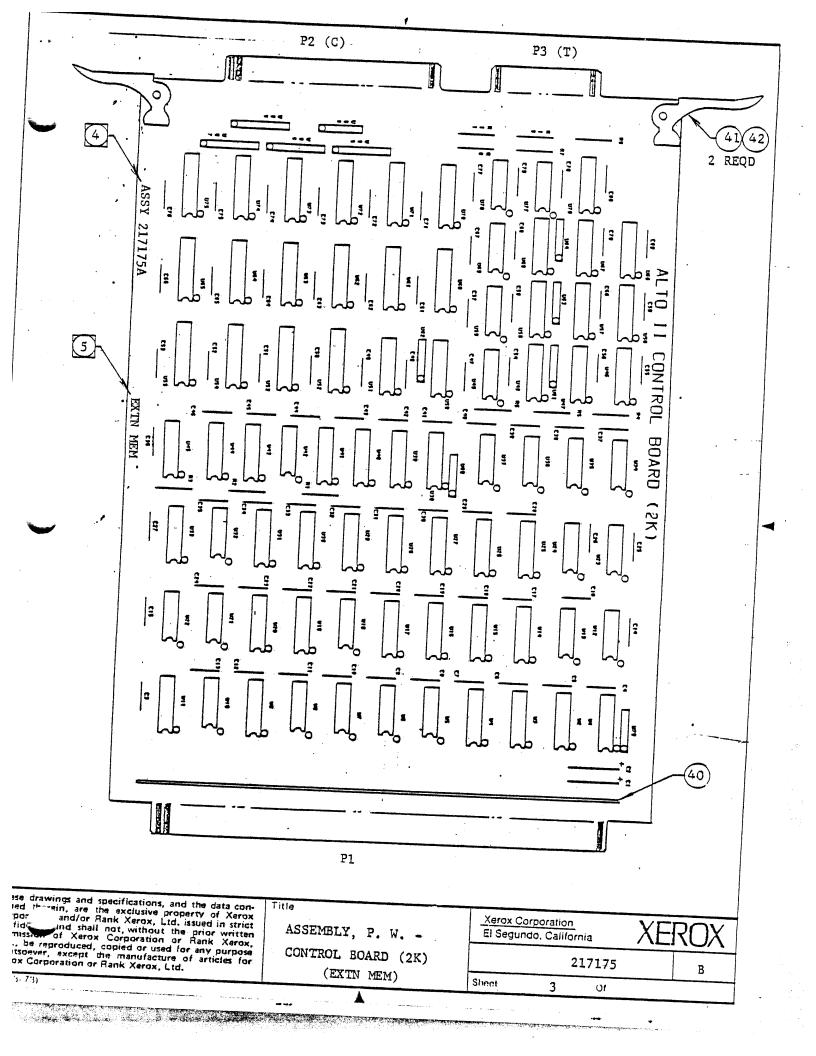
- 4 RUBBER STAMP "ASSY 21715A" .12 HIGH, WHITE CHARACTERS.
- 5 RUBBER STAMP "EXTN MEM".12 HIGH, WHITE CHARACTERS.
- 6. THE FOLLOWING MODIFICATIONS ARE REQUIRED USING REV "B" FW BOARD TO ALLOW FOR TESTING ON TERADYNE PWBA TESTER:
 - A. CUT ETCH FROM U35-2 TO U35-4 (ETCH SIDE).
 - B. ADD JUMPER FROM U35-4 TO U81-6 (ETCH SIDE).
 - C. ADD JUMPER FROM U81-6 TO P3(T)-10.

ASSY, PRINTED WIRING-CONTROL BOARD (2K) (EXTN MEM) Xerox Corporation
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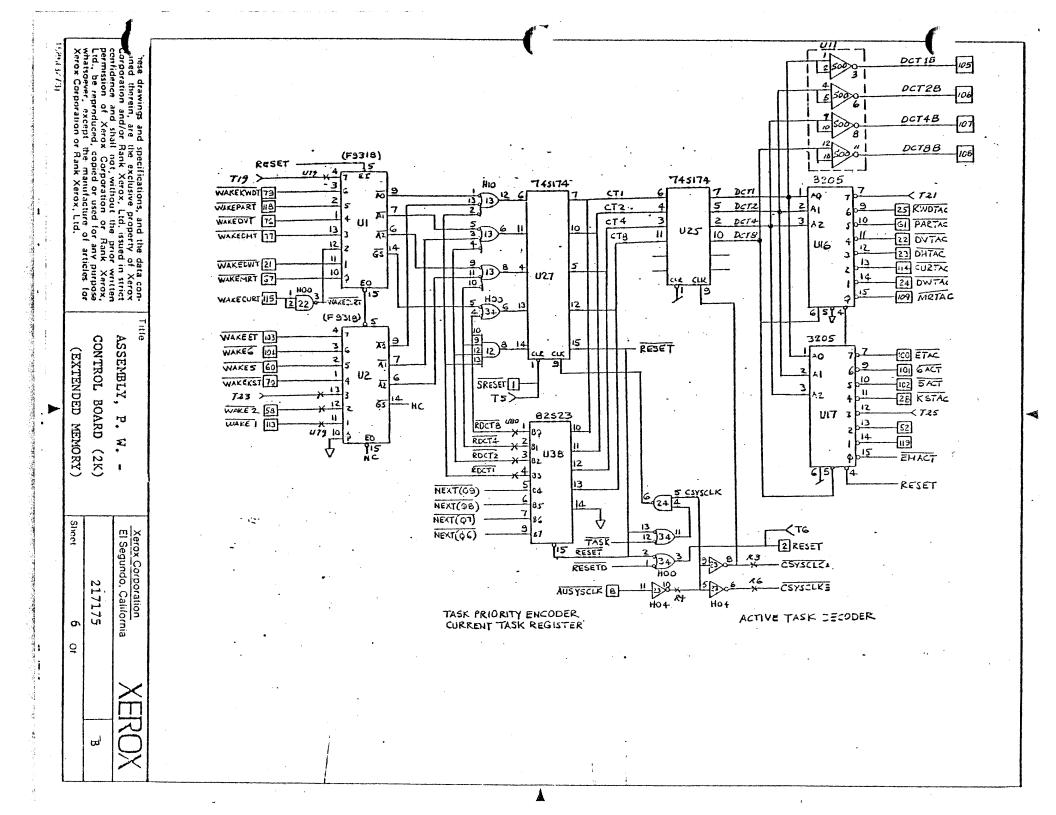




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1 2	(EATH MEM)	ALTO II XM	I Date	3/10/78 Sheet
2	Orawing Title	Drawing No.	No. R	U
	Board, Printed Wiring- Control Board ((2K) 217129=	1	
_ ·	Spec, Module Assembly	216207	REF	•
4				
5	Microcircuit, 74H00			
6	74H01		3	U22, 24,34
7	74802		1	U37
8	74H04		3	U18,21,78
9	74508		3	U7,8,23
10	74H10		1	U32
11	74H11		1	U13
12	74H21		1 1	U10
7 7	74H4O		1	U12
<u> </u>	74585		1	U35,77
15	748157		4	U26
16	748153		1	U14,48,58,68
17	748174		7	
18	748175		5	U15, 25, 27, 39, 40, 41,
19	748260		3	U44,45,46,56,66
20	I3101A (Intel)		3	U5,6,36 U47,57,67
21	I3205 (Intel)		6	
22	I3601-1 (Intel)		2	U16,17,19,20,30,31 U3,51
23	74800		1	U11
24	7425		1	U42
25	82S23 (Signetic)		1	U38
26	82S31 (Signetic)		1	U9
27	82S34 (Signetic)			U4,28,29
28	F9318 (Fairchild)			U1,2
29	74504			U49,59,69
	dicrocircuit, 828136 (Signetic)			U52 thru 55,60 - 65, 70 - 75
	apacitor, .05µ.F, 50 V	188483-002		C3 thru 80
32 C.	apacitor, 22µF, 15 V, Tant.	187720-005		C1,2



Material List Drawing No. Drawing Title ML 217175 These drawings and specifications, and the data contained therein, are the exclusive property of Xerox Corporation and/or Rank ASSEMBLY, PRINTED WIRING-Xerox. Ltd. issued in strict confidence and shall not, without the Action, Ltd. Issued in strict confidence and shall not, without the prior written permission of Xerox Corporation or Rank Xerox, Ltd., be reproduced, copied or used for any purpose whatsoever, except the manufacture of articles for Xerox Corporation or Rank Xerox, Ltd. CONTROL BOARD (2K) Model No. (EXTN MEM) ALTO II XM Item No. Drawing Title 3/10/78 Drawing No. 34 Resistor, 1K, ±5%, 1/4W No. Req. Remarks 116447-102 Resistor Network, (SIP) CTS-750-81-R330 35 11 R1, thru 11 36 Resistor Network, (SIP) CTS-750-61-R330 3 U86,87,89 37 Socket 5 U79, thru 84 Augat #514-AG11D 38 24 Socket Augat #516-AG11D 39 Socket 38 Augat #518-AG11D 40 Stiffener 16 216242 41 1 Extractor 216250 42 2 Rivet 156111-005 43 Resistor Network, 2 (SIP) CTS-750-61-R220 44 Resistor Network, 1 **U88** (SIP) CTS-750-81-R220 U85

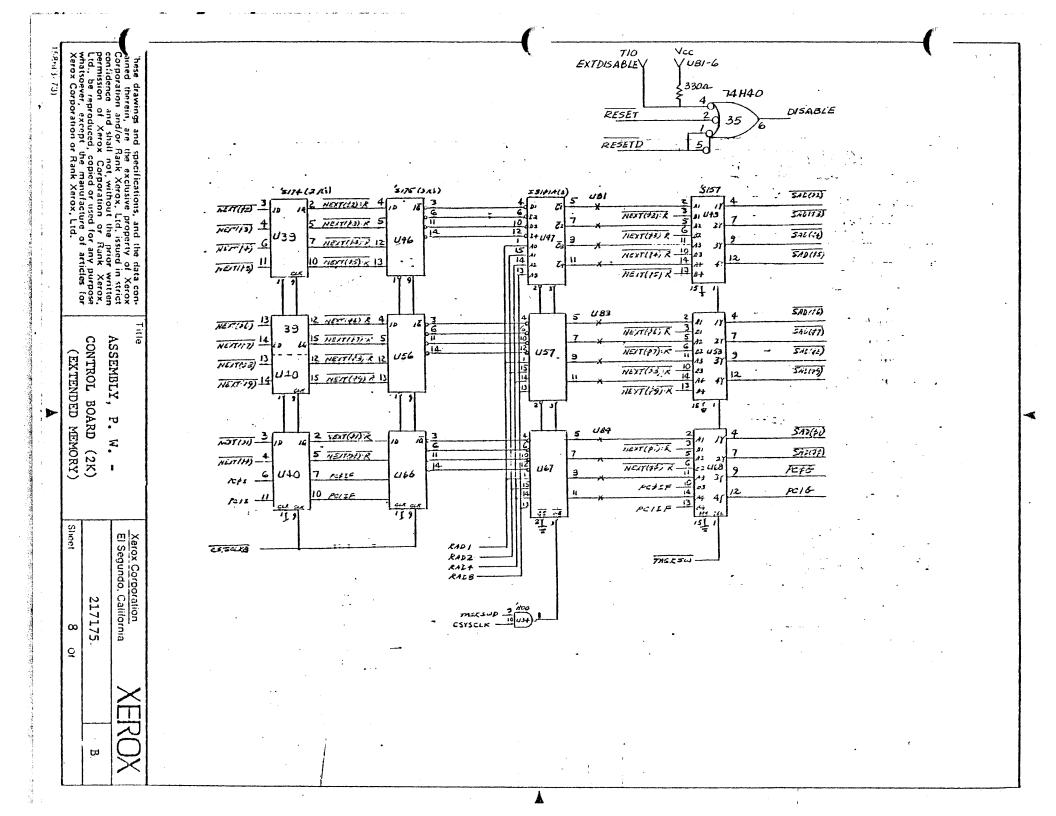


CONTROL BOARD (2K) ASSEMBLY, (EXTENDED MEMORY) P. W. Sheet Xerox Corporation
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CONTROL BOARD ASSEMBLY, ď ž.

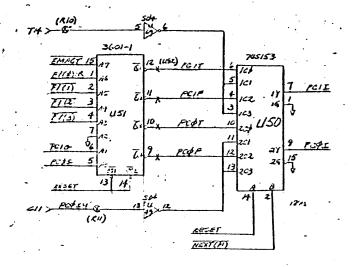
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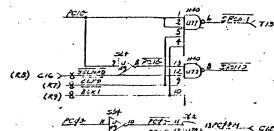
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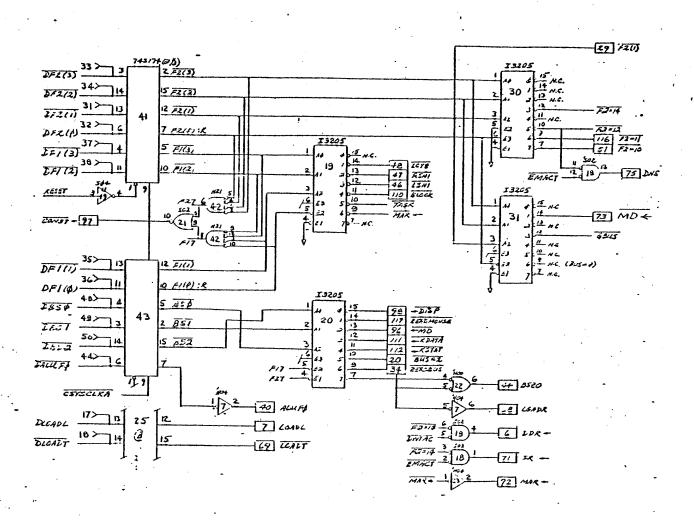
X EACH SHAML FULL-UP WITH 330 SL RESISTOR D EACH SIGNAL PULL-UP WITH IKIL RESISTOR

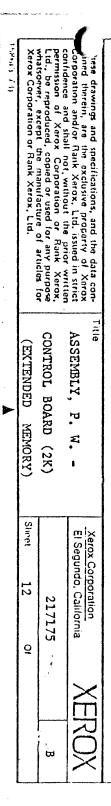


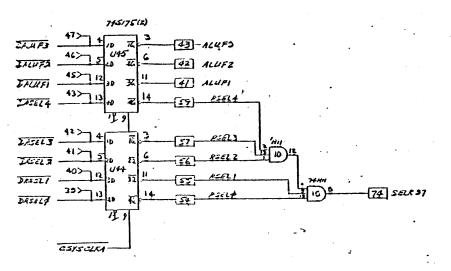


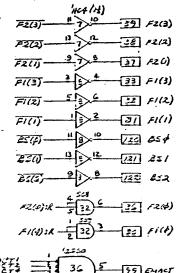
SIGNETICS . 825/36 (16 PLS) 5AD 001 34110 16 Serie 34575 342 73 306115 4-1777 \$42.61 U65 1 13 US5 U63 U64 U53 460 461 462 SAGAR 2,0 2021) 2021) Title B . 337 DISANE SIGMO 10 132 244 ASSEMBLY, CONTROL 6540 (EXTENDED BOARD ָט'. 3 14 454 ± //3 U70 MEMORY) 474 U75 U73 U52 U71 U72 £ 200-3777 In (2K) 6.11 SPEMI (-- 62 G. O. 6.6,6,5 A COUNTY OF THE PARTY OF THE PA "/4"# | | | | | | | | # 12:13 IF XXXX 11 A A IA 11-12 13 11 11 12 13 14 629 > Divis 29 Sheet Xerox Corporation El Segundo, California Secret Se 217175 PULL-UP WITH BELIE RENERY BICH SISNAC BILL UP 10 or Ø

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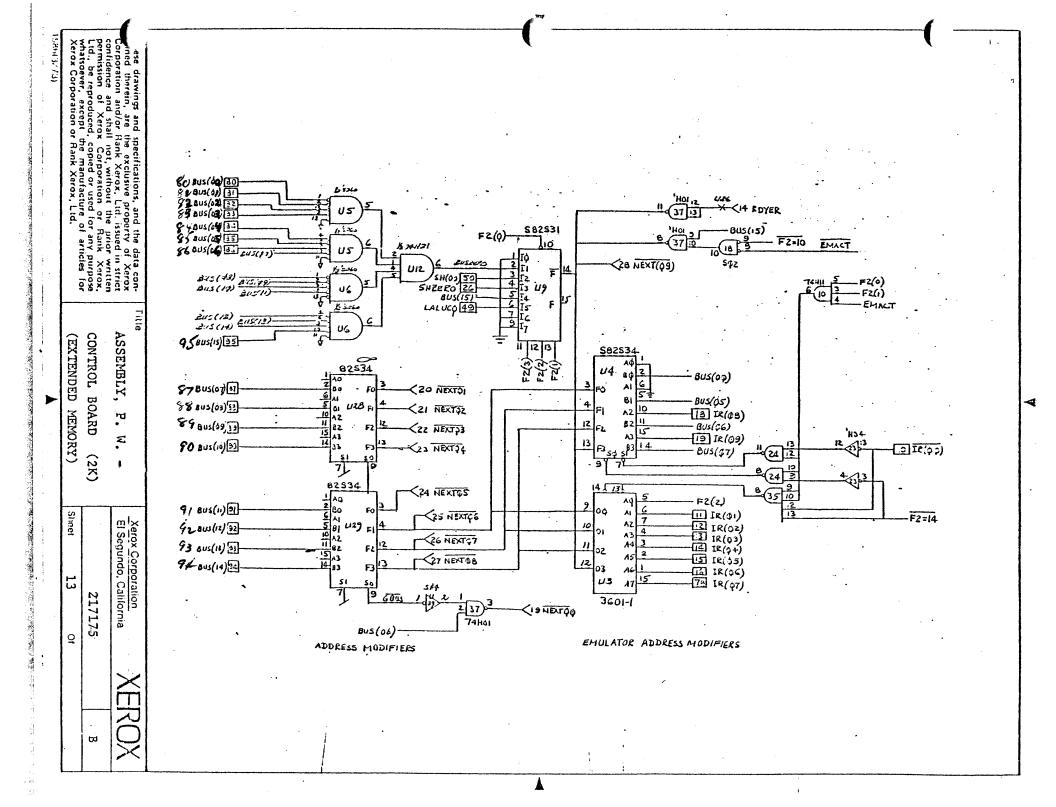






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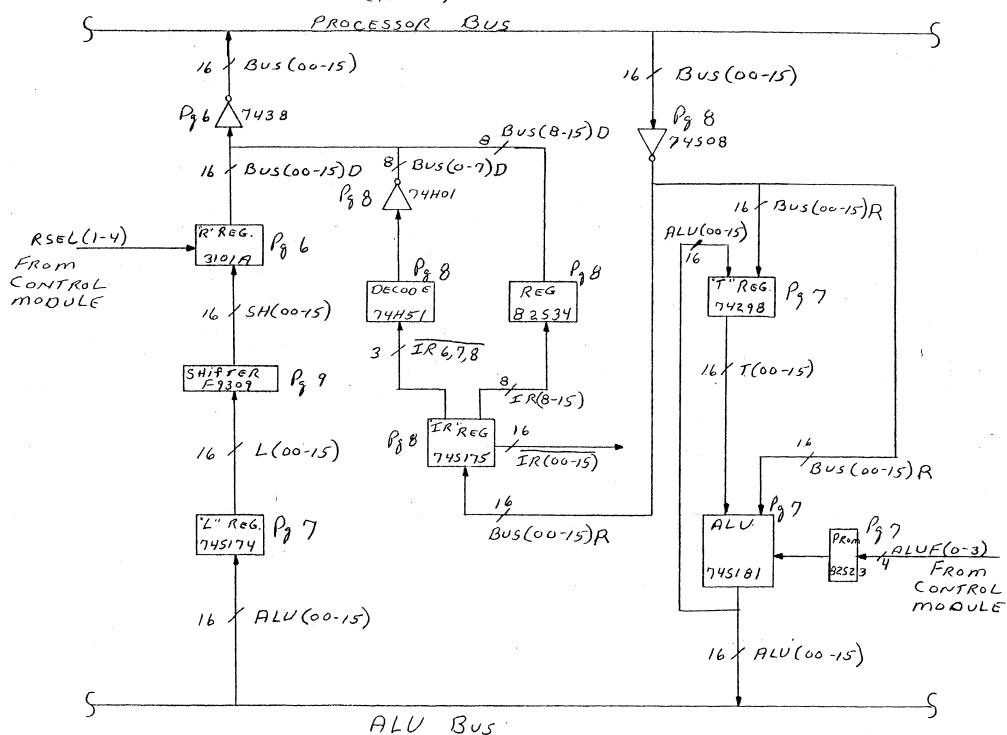
ARITHMETIC SECTION

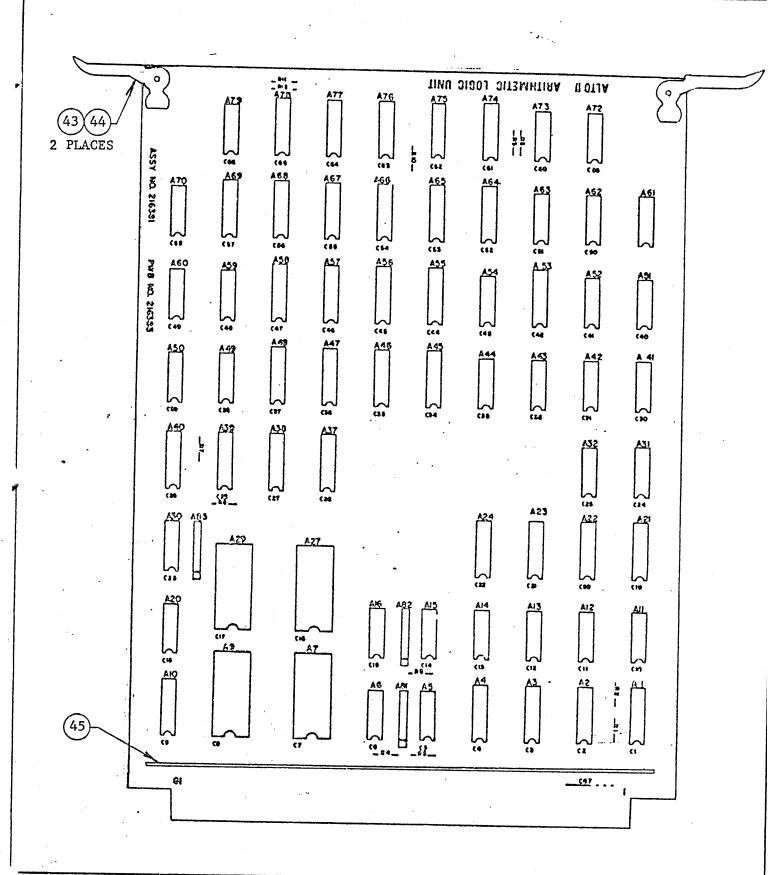
The arithmetic section of the processor consists of two 32-word by 16-bit register files R and S. and five registers, T, L, M, MAR, and IR. The registers are connected to the memory and to an ALU with a 16-bit parallel bus.

The ALU is a SN74181 type, restricted so that it can do only 16 arithmetic and logical functions. The ALU output feeds the L, M, and MAR registers. T may also be loaded from the ALU output under certain conditions. L is connected to a shifter capable of left and right shifts by one place, and cycles of 8. It has a mode in which it does the peculiar 17-bit shifts of the standard instruction set, and a mode which allows double-length shifts to be done.

The IR register is used by the emulator to hold the current emulated instruction.

ARITHMETIC LAGIC UNIT





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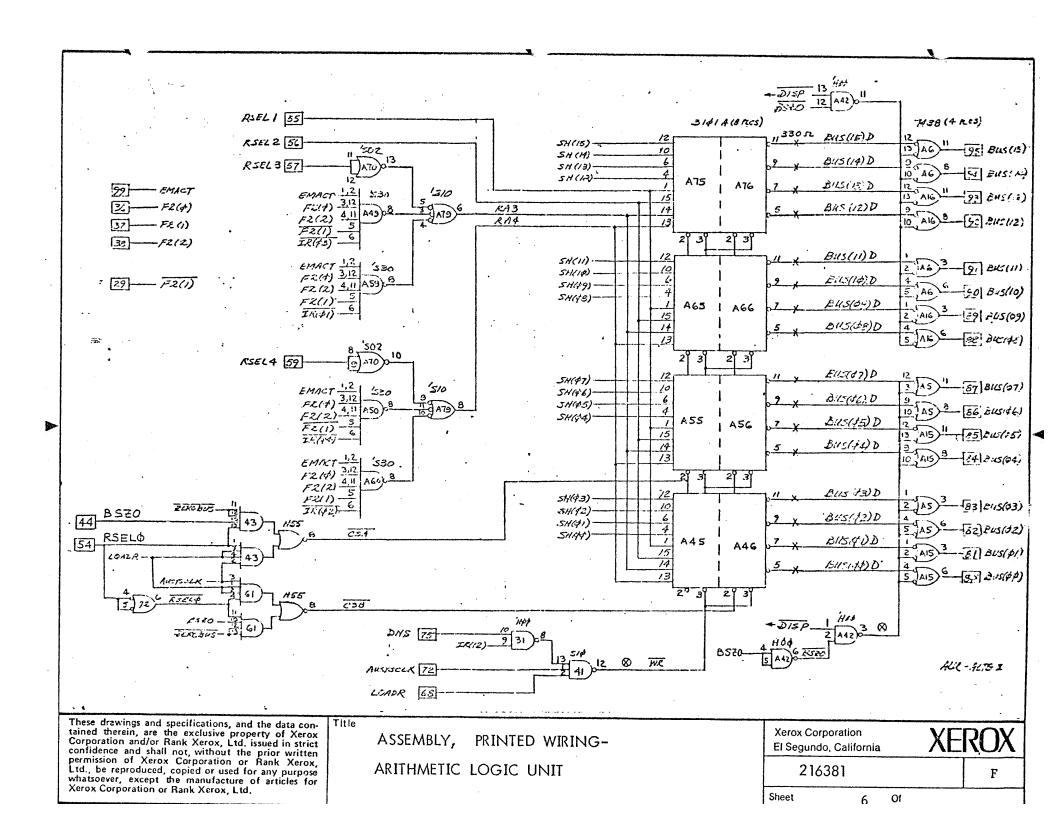


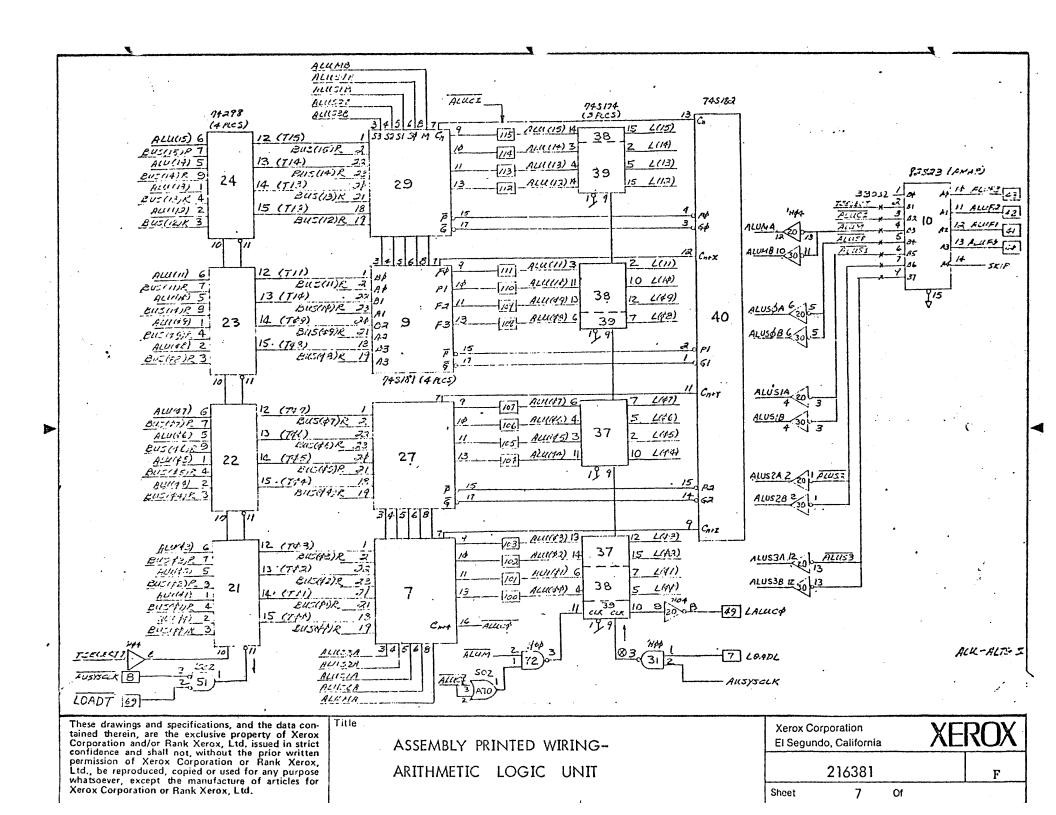
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2		Procedu	re, Test .		216382	REF			
3	3	Spec, Module Assembly			216207	REF			
4	4	Microci				4	A31,42,52,72		
5	5		74H55		``	2	A43, 61		
6	5		74H01			2	A44,54		
7	7		74802	·		2	A51,70		
8	3		74H04			2	A20,30		
9)		74808			4	A11,12,13,14		
1	LO		74S10			2	A41,79		
1	L1		74830			4	A49,50,59,60		
1	L2		7438			4	A5,6,15,16		
1	.3		74H50	·		1	A63		
1	4		74H51			1	A32		
1	L5		74H74			1	A73		
1	L6		7486			1	A62		
1	.7		748133	· .		1	A69		
1	18		74S174	<u>.</u>		3	A37,38,39		
1	L9		748175			4	A1,2,3,4		
2	20		74S181			4	A7,9,17,19		
2	21		74S182			1	A40		
2	22		74298			4	A21,22,23,24		
2	23								
2	24								
2	25	·	I3101A Intel			8	A45,46,55,56,65,66,75,76		
2	26		F9309 Fairchild			9	Á47,48,53,57,58,67 68,77,78		
2	27	·	82S23 Signetics		·	1	A10		
2	28	Microci	rcuit, 82834 Signetics			2	A64,74		
2	29								
3	30								
3	31	Capacit	or, .05 uF, 10V			66	Centralab #UK10-503		
3	32						C1 thru C66		
3	34	Capacit	or, 22uF, 15V, Tantalum		187720-005	1	C67		

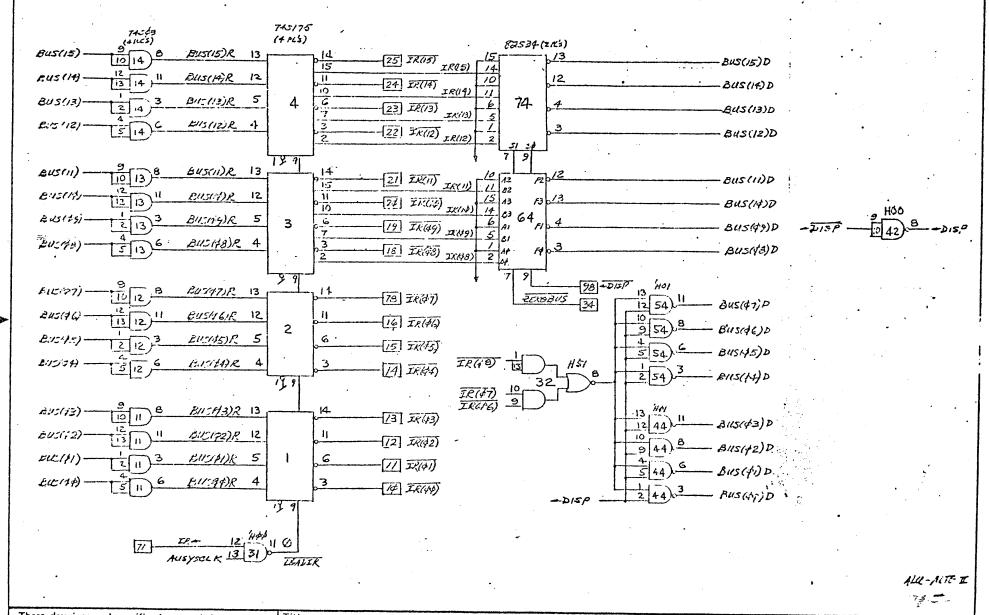
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ji j		ALTO II ASSEMBLY, PRINTED WIRING-	These drawings and specifications, and the data contained the in, are the exclusive property of Xerox Corporation and/or Rexerox. Ltd. issued in strict confidence and shall not, without prior written permission of Xerox Corporation or Rank Xerox, Ltd. be reproduced, copied or used for any purpose whatsoever, excited manufacture of articles for Xerox Corporation or Rank Xerox. Ltd.					
210381	ARITHMETI C LOGIC UNIT			10. 10 7YF92	Date	Date 3/5/76 Sheet 5		
77	Item No.	Drawing Title		Drawing No.	No. Req.		Remarks	0,
	34	Resistor, 1K, ±5%, 1/4W		116447-102	10	R1,2	, 4, 6, Thru	12
	35	Resistor, 330n, ±5%, 1/4W		116447-331	2		, R5	
3	36	Resistor Network (SIP)			3	CTS	#750 - 81 - R3	30 ഹ
	37							
	38	Socket, Microcircuit			30	Auga	t #514-AG1	1D
	39	Socket, Microcircuit			33		t #516-AG1	
	40	Socket, Microcircuit			4		t #524-AG1	
	41					1-6-	- "22" 1101	
	42				<u> </u>			
	43	Rivet, Blind - Pull Thru		156111-005	2			
ſ	44	Extractor, Module	*	216250	2			
	45	Stiffener		216242	1			
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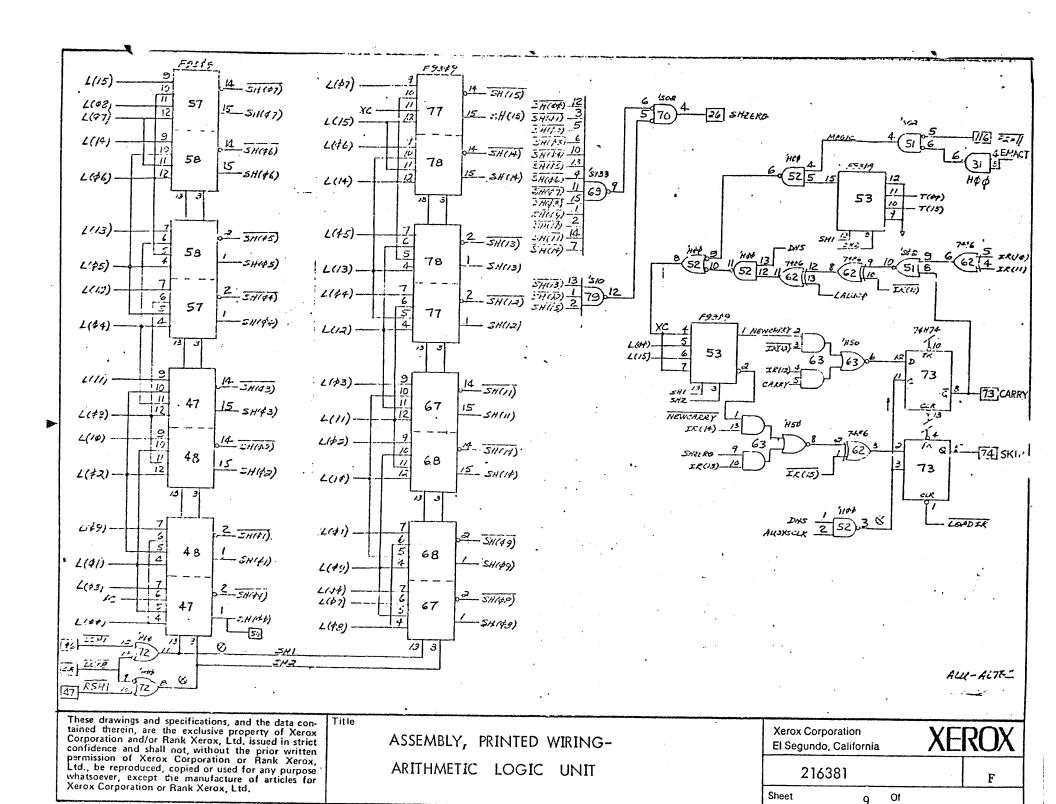
ASSEMBLY, PRINTED WIRING-ARITHMETIC LOGIC UNIT Xerox Corporation
El Segundo, California

216381

Sheet

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Of



DISPLAY CONTROLLER

CHARACTERISTICS

The display controller handles transfers between the main memory and the CRT. The CRT is a standard 875 line raster-scanned TV monitor, refreshed at 60 fields per second from a bit map in main memory. The CRT contains 606 points horizontally, and 808 points vertically, or 489.648 points total.

The basic way in which information is presented on the display is by fetching a series of words from Alto main memory, and serially extracting bits to become the video signal. Therefore, 38 16-bit words are required to represent each scan line; 30704 words are required to fill the screen.

HARDWARE

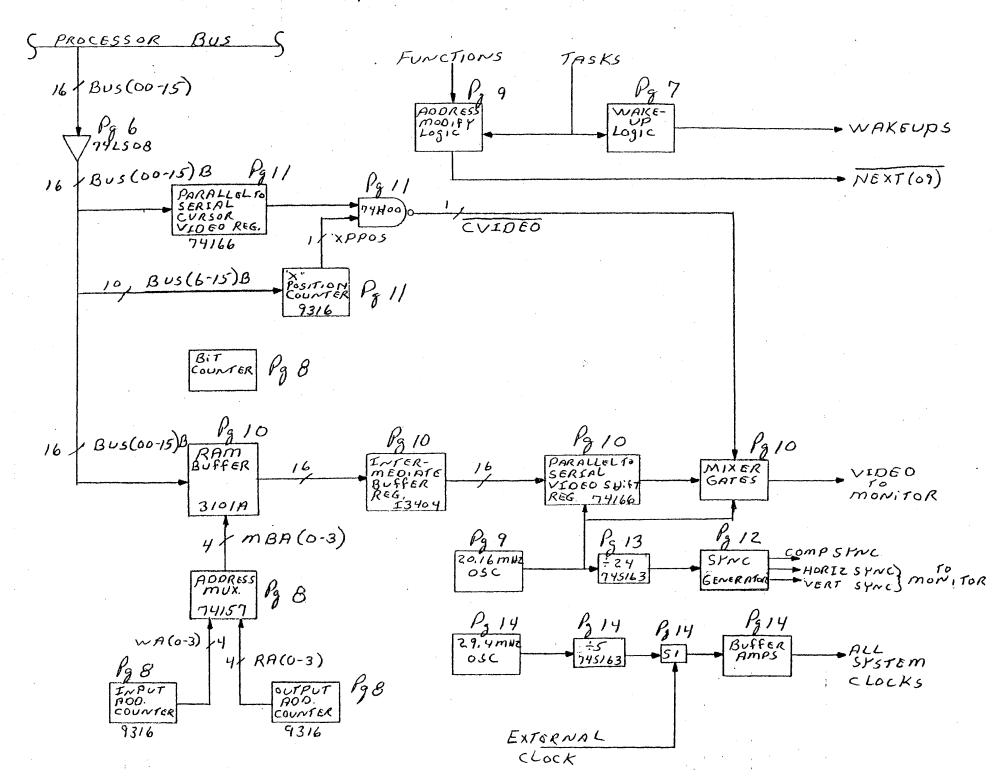
The display controller consists of a sync generator, a data buffer and serializing shift register, and three microcode tasks which control data handling and communicate with the Alto program. The Ram buffer is loaded from the Alto bus for the display word task DWT. The purpose of the intermediate buffer is to synchronize data transfers between the main buffer, which is synchronous with the 170 nsec. master clock, and the shift register, which is clocked with an asynchronous bit clock. The sync generator provides this clock and the vertical and horizontal synchronization signals required by the monitor.

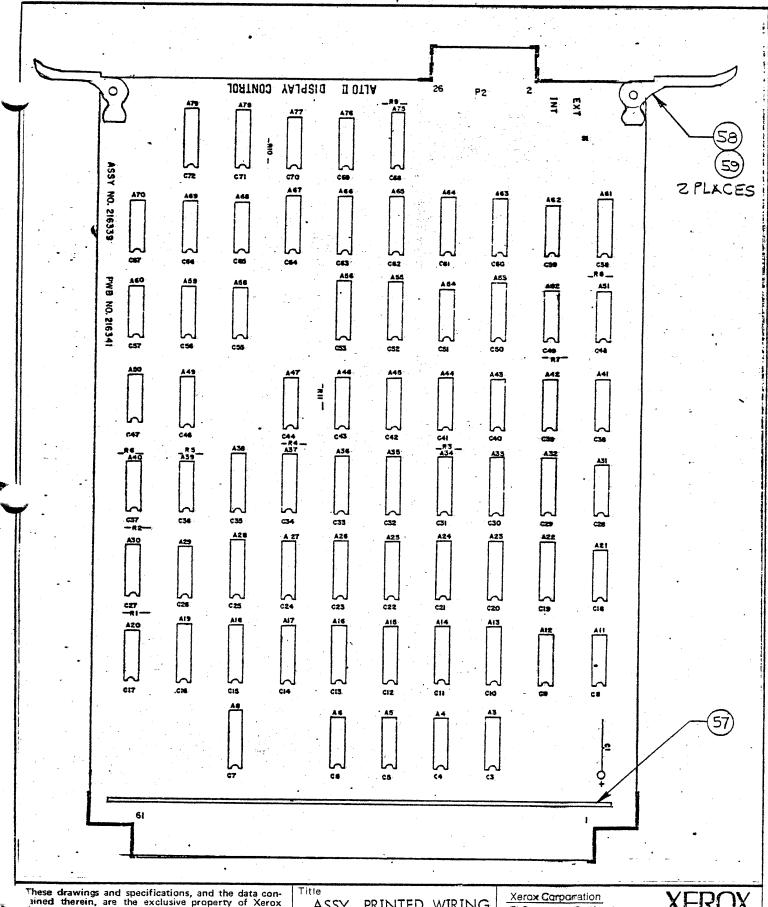
CURSOR

Because of the difficulty of inserting a cursor at the appropriate place in the display bit map at reasonable speed, a hardware cursor is included in the Alto. The cursor consists of an arbitrary 16 by 16 bit patch, which is merged with the video at the appropriate time. The coordinate origin for the cursor is the upper left hand corner of the screen. The cursor presentation is unaffected by changes in display resolution.

The cursor hardware consists of a 16-bit shift register which holds the information to be displayed on the current scan line, and a counter which is incremented by the bit clock, and determines the x coordinate and bit map segment from the R memory into the hardware.

DISPLAY CONTROL MODULE





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ASSY, PRINTED WIRING

El Segundo, California

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Of

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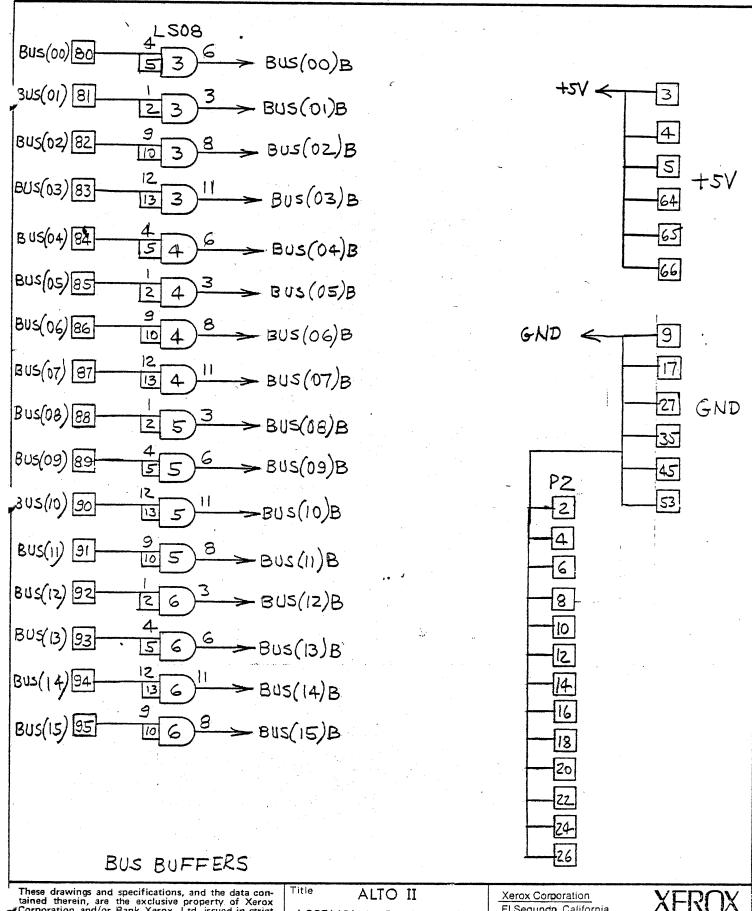
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	DISPLAY CONTROL	Model No		Date 3/	4/76	Sheet 4	Of	
Item No.	Drawing Title		Drawing, No.	No. Req.		Remarks		
1	Board, P.W., Display Control		216341	1	·		-4-1-10	
2	Procedure, Test		216340	REF				
3	Spec, Module Assembly		216207	REF	•.			
4	Microcircuit, 74H00			4	A29,30,4	3,58		
5	74H01			15	A40			
6	74н04			1_	A42			
7	74н08			1.	A31		•	
8	74H10			I	A21			
9	74H11			2"	A11,12			
10	74H21			1	A46			
11	74н30	à.		1	A76			
12	74H50			1	A52			
13	74H74			3	A44, 45,	47		
14	74157		,	1	A35			
15	74166			4	A17,22,2	8,32		
16	74174			2	A18,64			
17	74279		•	1	A19			
18	74298			1	A56			
19	74S02			2	A20,50			
20	74504			4	A41,49,6	2.70		
21	74S10			1	A59			
22	74\$163	·		3	A61,78,7	9		
23	74LS08		er er er er er er er er er er er er er e	4	A3,4,5,6			
24	74508	_		1	A39			
25	74\$74			1	A68			
26	Microcircuit, 74S00		, — , , , , , , , , , , , , , , , , , ,	1	A69			
27	MICROCIRCUIT, 74109			1	A8			
28						*		
29	1		·····					
30	Microcircuit, 13101A Intel			4	A14,15,24	4.25		
31	Microcircuit, 13404 Intel			3	A13,23,3			
32				1 -		<u> </u>		
33								



Trawing Title	Mat	erial	∀			ML Drawing No. Rev. 216339		
34 Microcircuit, MC3026 Motorola 2 A54,77	139	1	ALTO II SSEMBLY, PRINTED WIRING-	These drawings and specifications, and the data contained ther in, are the exclusive property of Xerox Corporation and/or Raicerox, Ltd. issued in strict confidence and shall not, without to be reproduced, copied or used for any purpose whatsoever, exceed the manufacture of articles for Xerox Corporation or Rank Xerox, Ltd.				
34 Microcircuit, MC3026 Motorola 2 A54,77	No.	Item No						
35 K1091A Motorola 1 OSC 20.164HZ 36	awing 2		Drawing Title	Drawing No.	No. Re			
36	ă				2	A54,77		
37 S82823 Signet 1 2.56 PROM 2 38 P3601-1 M.I.L. 2 A38.66 39 F9316 Fairchild 9 A16.26.27.36.37, 40 Microcircuit, 898-1-330 Beckman 2 A33,53 41 42 43 C55 Thru 72 44 C3 Thru 44,46 Thru 45 Capacitor, .05 uF, 10V 46 Capacitor, Tant. 22 uF, 15V 47 48 49 50 Resistor, 330.2.±5%, 1/4W 51 Resistor, 1K, ±5%, 1/4W 52 53 54 Switch 55 Socket, Microcircuit 56 Socket, Microcircuit 57 Stiffener 216242 1 58 Extractor, Module 216250 2					1	OSC 20.16MHZ A60		
38					1	OSC 29.40 MHZ A51		
39 F9316 Fairchild 9 A16,26 27,36,37, 40 Microcircuit, 898-1-330 Beckman 2 A33,53 41 C55 Thru 72 44 C3 Capacitor, .05 uF, 10V 68 Centralab #UK-10-5 46 Capacitor, Tant. 22 uF, 15V 187720-005 1 C1 47 A8 A9 T16447-331 7 R1,2,3,5,6,7,11 51 Resistor, 1K, ±5%, 1/4W 116447-102 5 R4,8,9,10,12 52 S3 Socket, Microcircuit 34 Augat #514-AG11D 56 Socket, Microcircuit 34 Augat #516-AG11D 57 Stiffener 216242 1 58 Extractor, Module 216250 2					1	2.56 PROM A63		
40 Microcircuit, 898-1-330 Beckman 2 A33,53 41					2	A38,66		
41 42 43 44 45 Capacitor, .05 uF, 10V 46 Capacitor, Tant. 22 uF, 15V 47 48 49 50 Resistor, 330-,±5%, 1/4W 51 Resistor, 1K, ±5%, 1/4W 52 Socket, Microcircuit 55 Socket, Microcircuit 56 Socket, Microcircuit 57 Stiffener 58 Extractor, Module 59 River	Ì				9	55.65.67.75		
42 43 44 45 46 47 46 47 48 48 49 50 Resistor, 330a,±5%, 1/4W 51 Resistor, 1K, ±5%, 1/4W 52 53 54 Switch 55 Socket, Microcircuit 56 Socket, Microcircuit 57 Stiffener 58 Extractor, Module 59 River 59 Resistor, 100 C23 Thru 44,46 Thru 68 Centralab #UK-10-9 C1 T1 T1 T1 T1 T1 T1 T1 T1 T1 T1 T2 T2 T3 T1 T1 T2 T3 T1 T1 T2 T3 T1 T1 T2 T3 T3 T3 T3 T4 T1,2,3,5,6,7,11 T1 T1 T2 T4 T4 T5 T5 T5 T5 T5 T5 T5 T5 T5 T5 T5 T5 T5	ŀ		Hieroeffeure, 898-1-330 Beckman		2	A33,53		
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45 Capacitor, .05 uF, 10V 46 Capacitor, Tant. 22 uF, 15V 187720-005 1 C1 47 48 49 50 Resistor, 330 x, ±5%, 1/4W 116447-331 7 R1, 2, 3, 5, 6, 7, 11 51 Resistor, 1K, ±5%, 1/4W 116447-102 5 R4, 8, 9, 10, 12 52 53 54 Switch 5 Socket, Microcircuit 56 Socket, Microcircuit 57 Stiffener 216242 58 Extractor, Module 216250 2 Centralab #UK-10-5 68 Centralab #UK-10-5 1 C1 C1 C1 C1 C1 C1 C1 C1 C1	·					C55 Thru 72		
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48 49 50 Resistor, 330x,±5%, 1/4W 51 Resistor, 1K, ±5%, 1/4W 52 53 54 Switch 55 Socket, Microcircuit 56 Socket, Microcircuit 57 Stiffener 58 Extractor, Module 59 Rivet			Capacitor, Tant. 22 uF, 15V	187720-005	1	C1		
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56 Socket, Microcircuit 34 Augat #514-AG11D 57 Stiffener 216242 1 58 Extractor, Module 216250 2	H				1	Contrl Sw # T8201		
57 Stiffener 216242 1 58 Extractor, Module 216250 2					34	Augat #514-AG11D		
58 Extractor, Module 216250 2					34	Augat #516-AG11D		
59 Rivet				216242	1			
59 Rivet 156111-005 2				216250	2			
	-	39	Rivet	156111-005	2			
	<u> </u>							
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ASSEMBLY, P.W.DISPLAY CONTROL

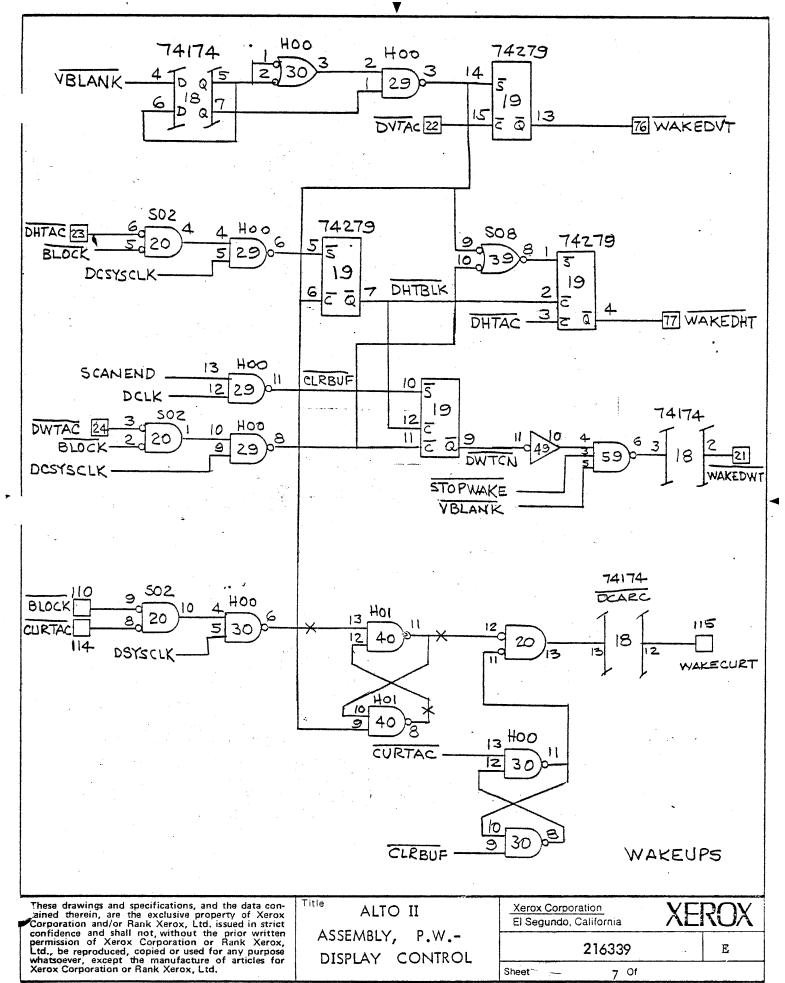
Xerox Corporation
El Segundo, California

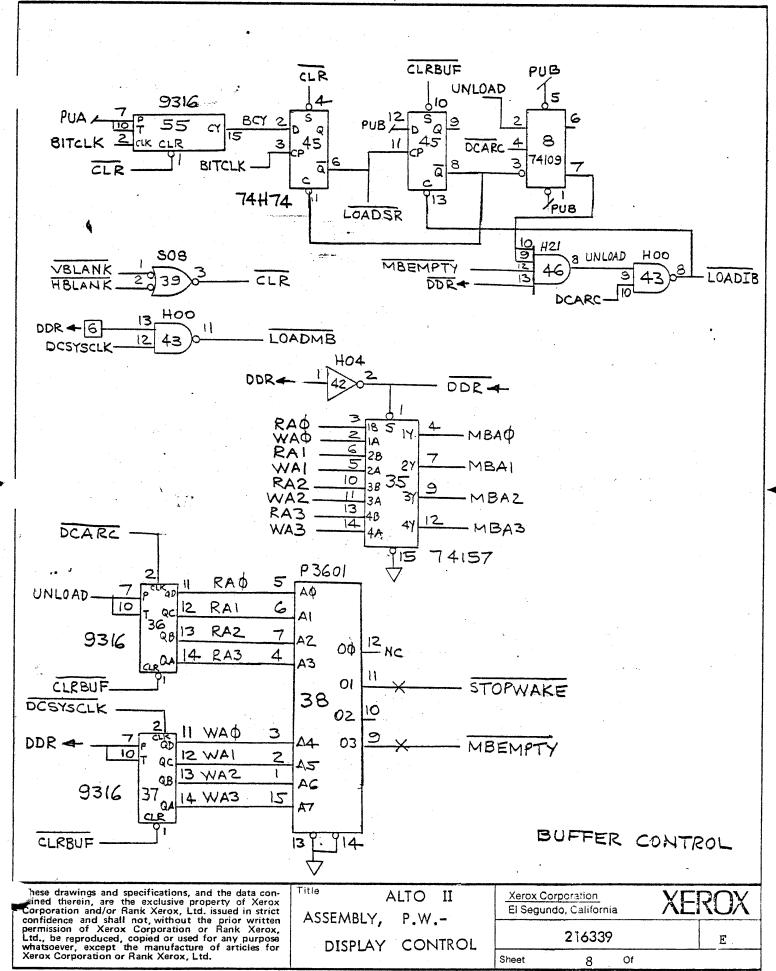
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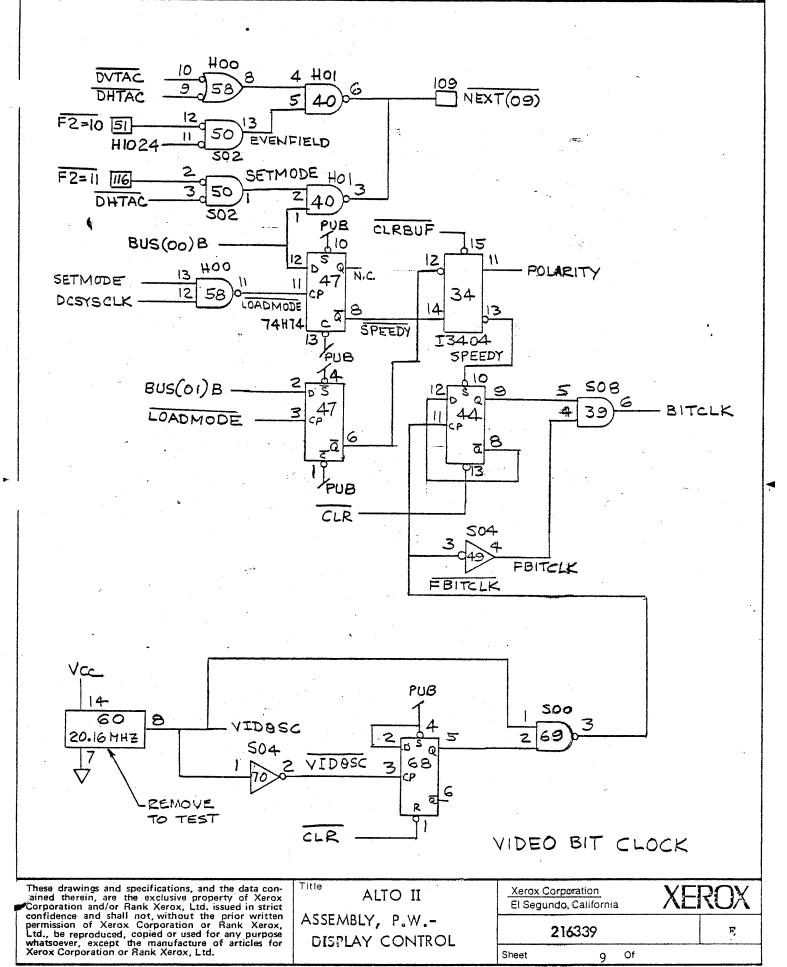
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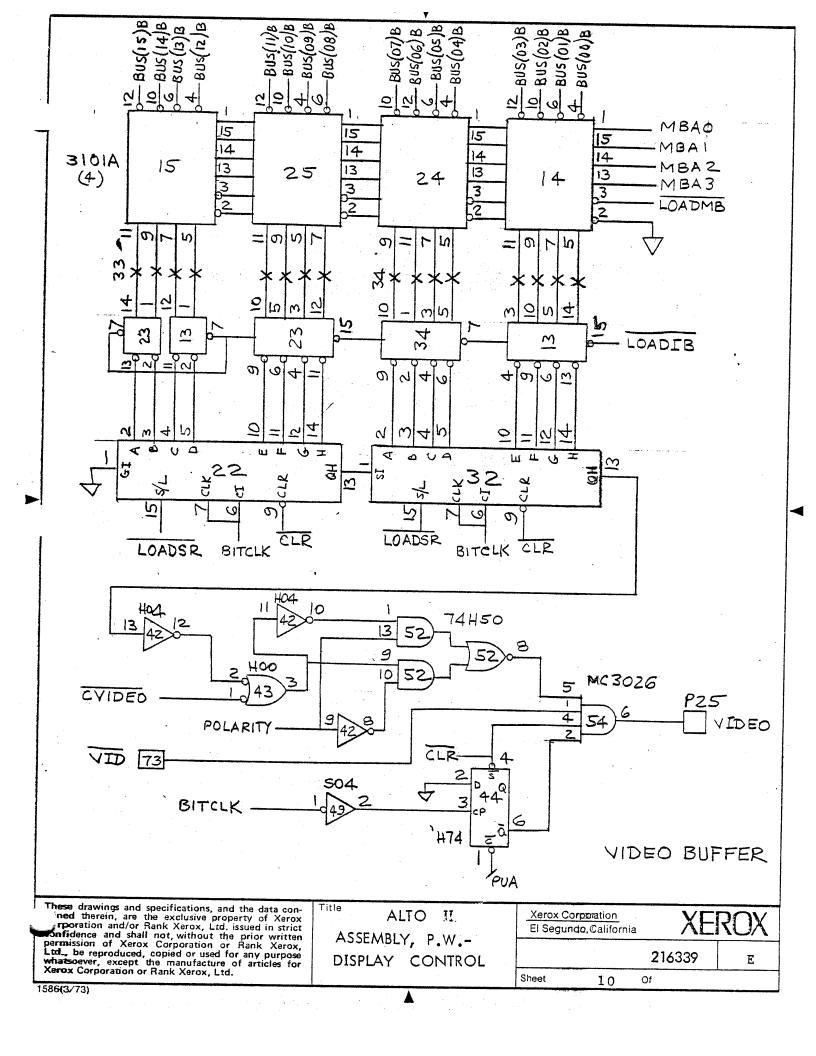
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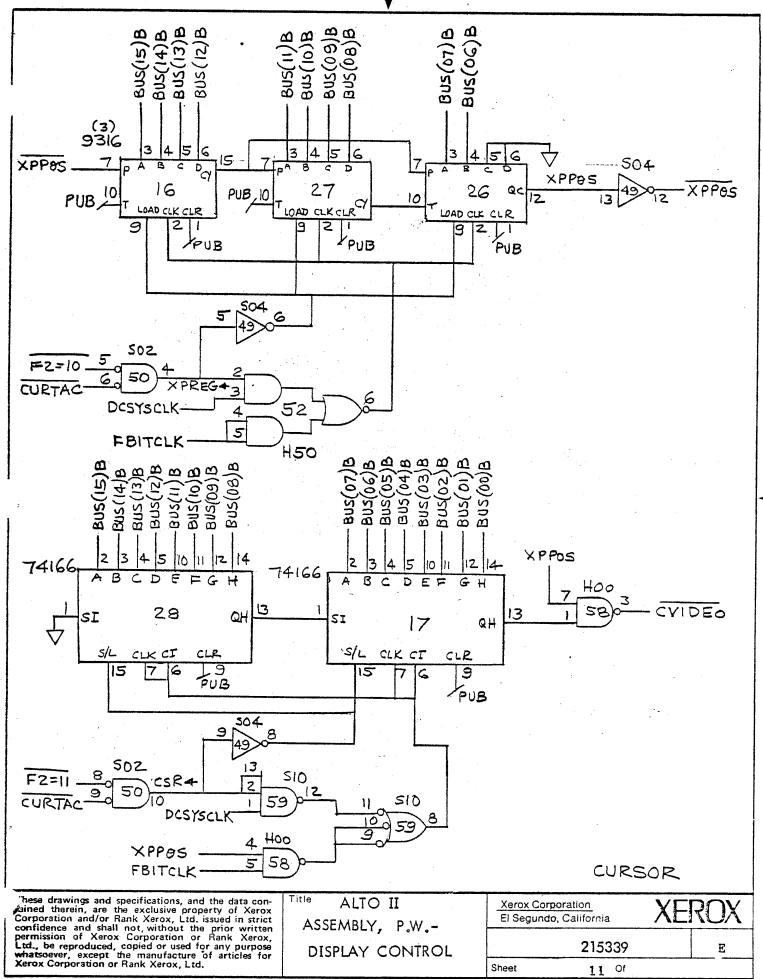


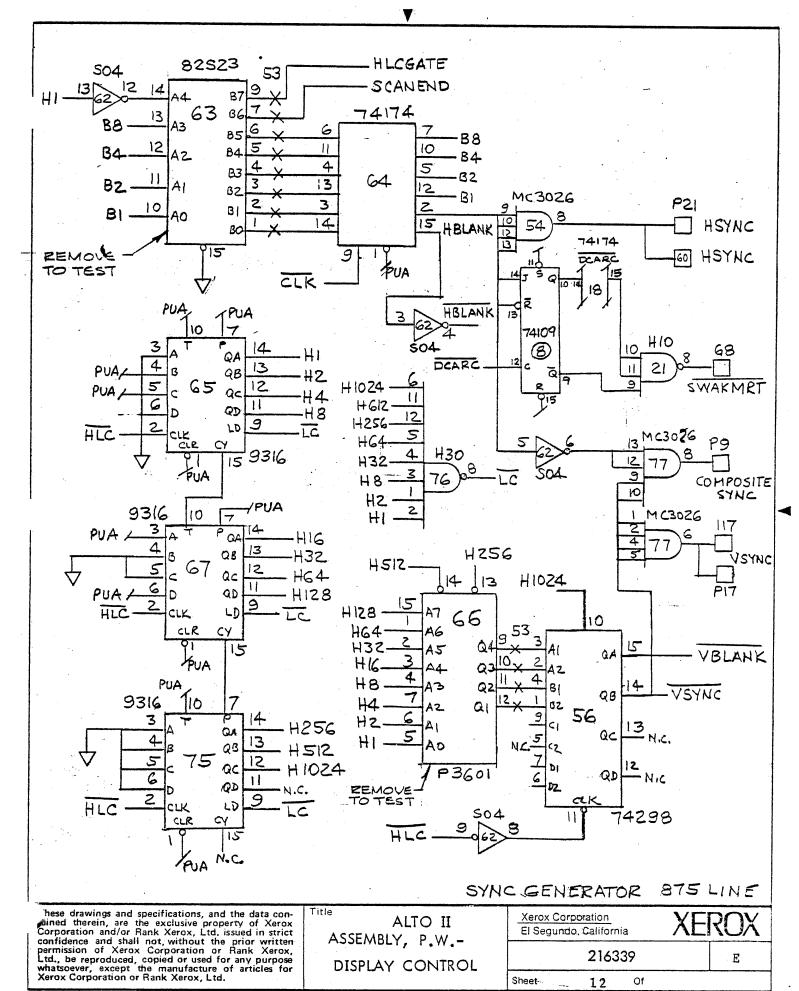


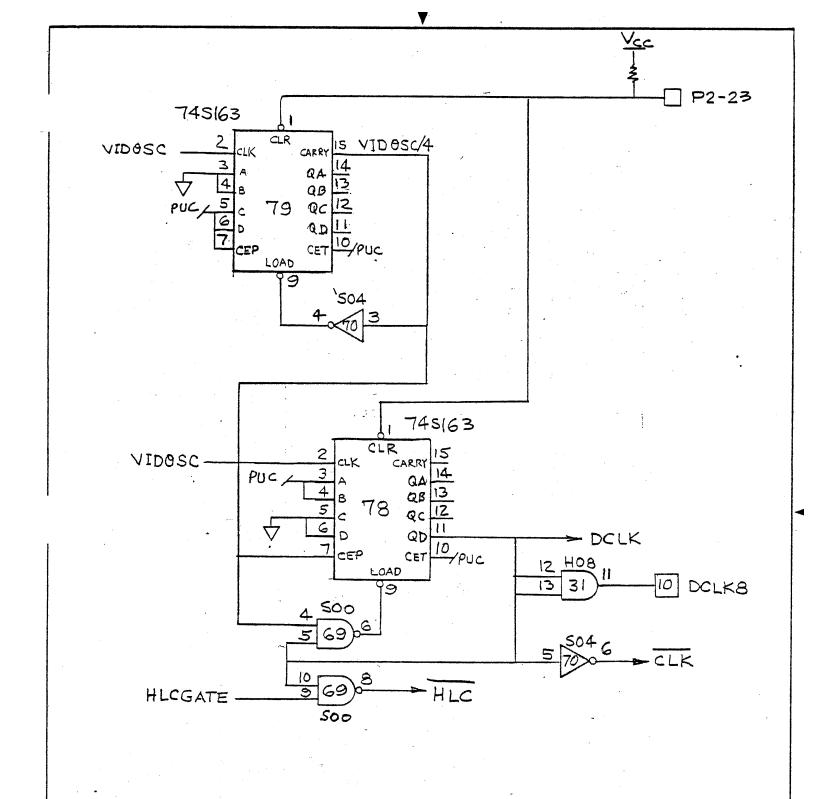


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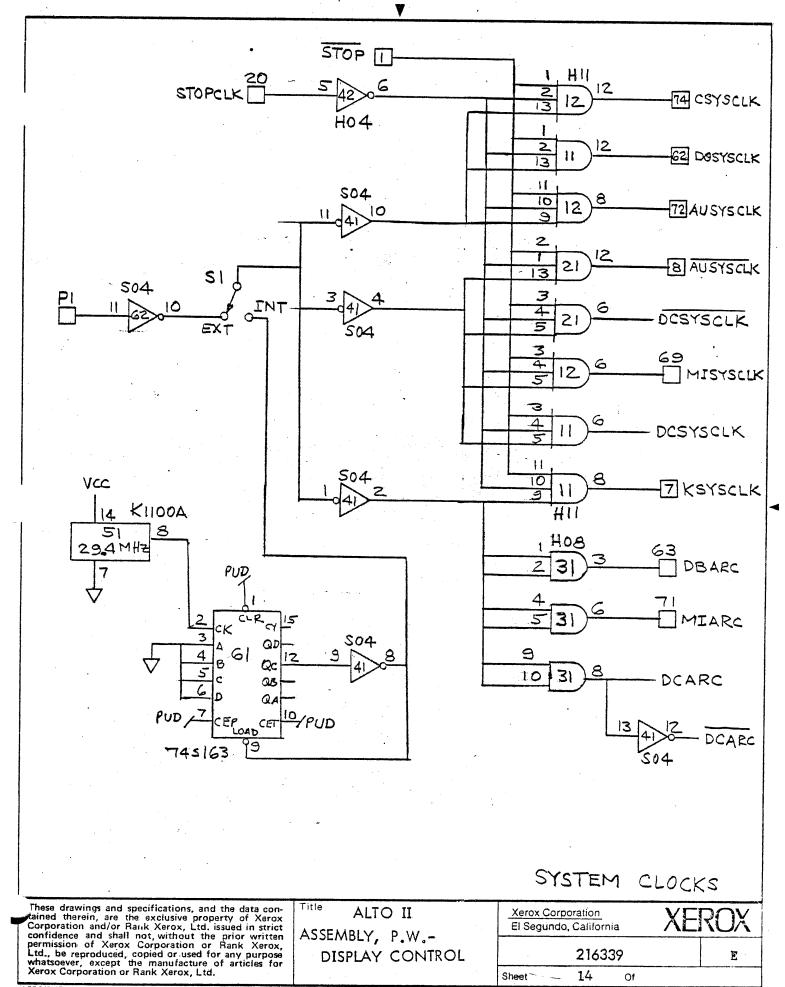
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ASSEMBLY, P.W. DISPLAY CONTROL

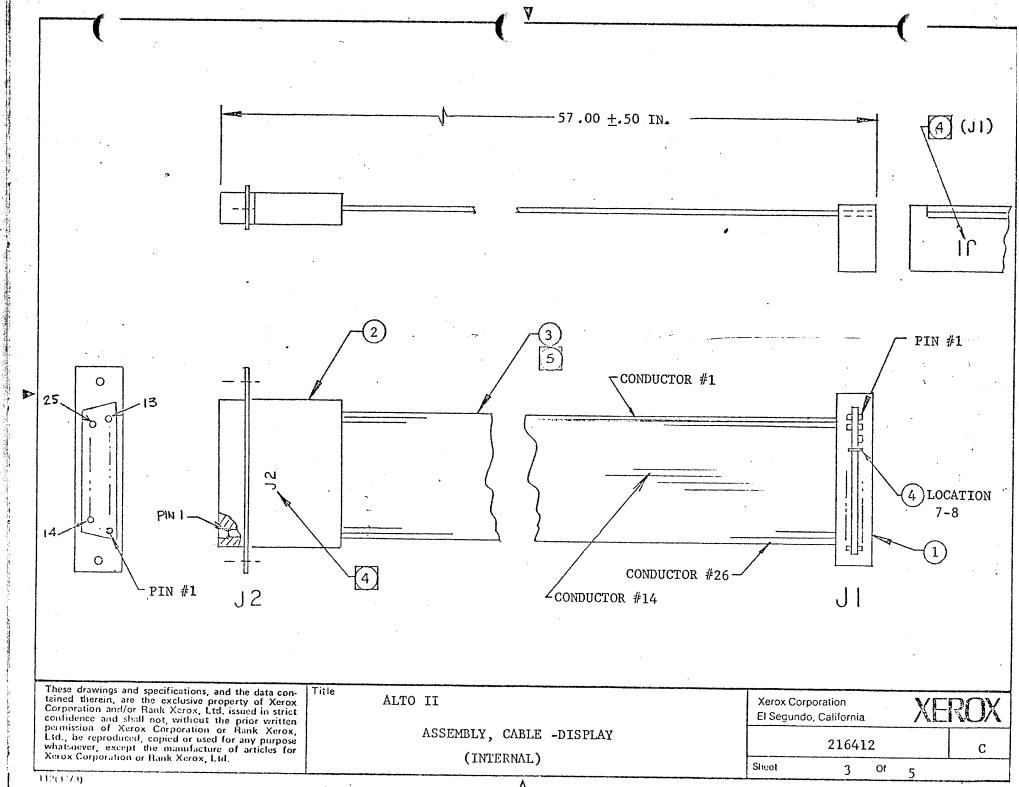
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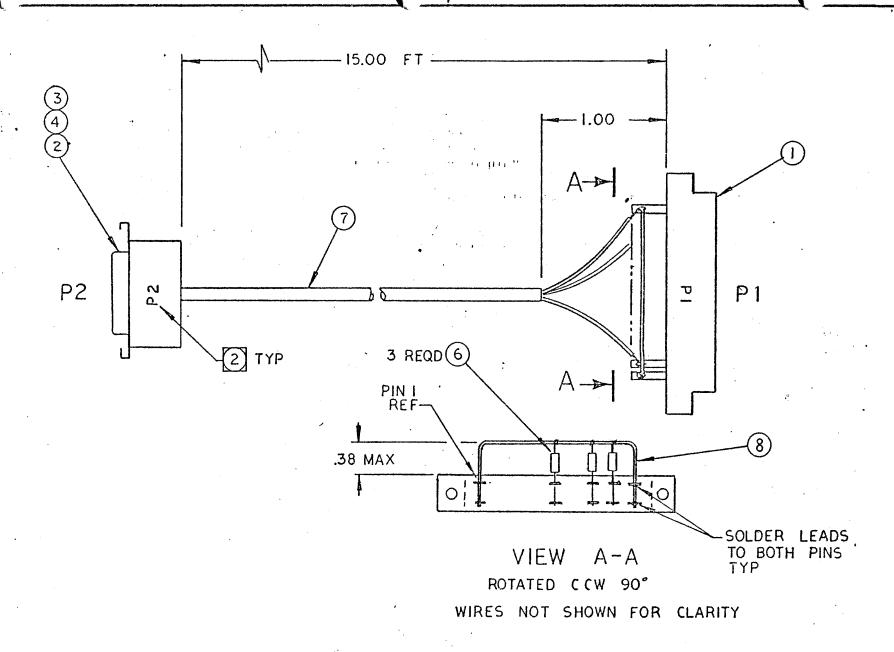


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		7- 				17		(٥,
	Wire No.	Term	From	То	Term	Wire Type	Notes ·	Signal	Chg. Let.
	1		J2 - 13	J1 - 1		3			
.	2		25	2					
	3		12	3					
	4		24	4					
	5		11	5					
	6		23	6					
Ì	7		10	7					
	8		22	8					
	9 .		9	9			·	,	
	10		· 21	10					
	11		8	11				·	
	12		20	12					
	13		. 7	13					
-	14			14			(5)		
	15		19	1.5					
	16		6	16					
	17		18	17					
	18		5.	18				·	
	19		17	19					
	20 .	-	Ćį.	20					
ſ	21		16	21			-	· · · · · · · · · · · · · · · · · · ·	
	22		3	22					
	23		15	23					
ľ	24		2	24					•
	25		14	25					
	26		J2 - 1	J1 - 26		3			
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	Xerox Corporation	or Hank Xer	ox, Ltd.		~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~~	<u> </u>	,	Sheet 5 0f 5	

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ALTO II

ASSEMBLY, CABLE - DISPLAY (EXTERNAL)

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Sheet	3	Of	5	

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					A			<u> </u>		
Wire No.	Term	From	То	Term	Wire Type		Notes	Signal		Chg. Let
1		P1 - A	P1 - 1		8					
2		1	10		8				***************************************	
3		10	L		8					
4 .		6	BUS		6					·
5	э	8	BUS		6	_				
6		9	P1 - BUS	·	6					
7		F	P2 - 16		7	WHT	TWISTED	HORIZONTAL SYNC		
8		BUS	3	·		BLK J	PAIR			
9		J	14			GRN }	TWISTED	VIDEO		
10		BUS	1			BLK J	PAIR			
11		К	18			RED \	TWISTED	VERTICAL SYNC		1
12		P1 - BUS	P2 - 5		7	BLK J	PAIR			
	·									
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Ltd., be reprodu	ced, copied or of the manu	used for any purpose facture of articles for	Ref Designations Are a Prefix Each Designation	Abbreviated. on With:	ASSE	MBLY, CAB (EXTERN	ELE - DISPLAY	216410		С
Xerox Corporation	on or Rank Xe	rox, Ltd.				\		Sheet 5 Of	5	

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NOTES: UNLESS OTHERWISE SPECIFIED

- 1. ASSEMBLE PER MODULE ASSEMBLY SPEC, DWG NO. 216207.
- MAY BE PURCHASED FROM STANDARD WIRE & CABLE COMPANY, EL SEGUNDO, CALIFORNIA. VENDOR PART NO.
- MAY BE PURCHASED FROM CTS CORPORATION, ELKHART, INDIANA. VENDOR PART NUMBER US104L.
- MAY BE PURCHASED FROM ROGAN CORPORATION, NORTHBROOK, ILLINOIS. VENDOR PART NUMBER SC-10, BLACK, .140" x .093".
- AFTER SOLDERING, TRIM "R1" RESISTOR LEADS ON ETCH SIDE AS CLOSE TO THE PRINTED WIRING BOARD AS POSSIBLE AND ADHERE APPROX 1.0" LENGTH OF TEFLON TAPE (ITEM 11) OVER TRIMMED LEADS.
- TAG LOOSE ENDS OF CABLE AS TO THEIR DESTINATIONS PER WIRE LIST.
- [7] INSTALL ITEM 4 TO ITEM 3 USING SUITABLE ADHESIVE.

		WIRE LIST		
WIRE NO.	FROM	то 6	WIRE TYPE	NOTES
1	PWB - 1	J1 - PIN C	9	YELLOW
2	PWB - 2	J1 - PIN D	8	BLUE
3	PWB - 3	J1 - PIN B	7	ORANGE

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Title

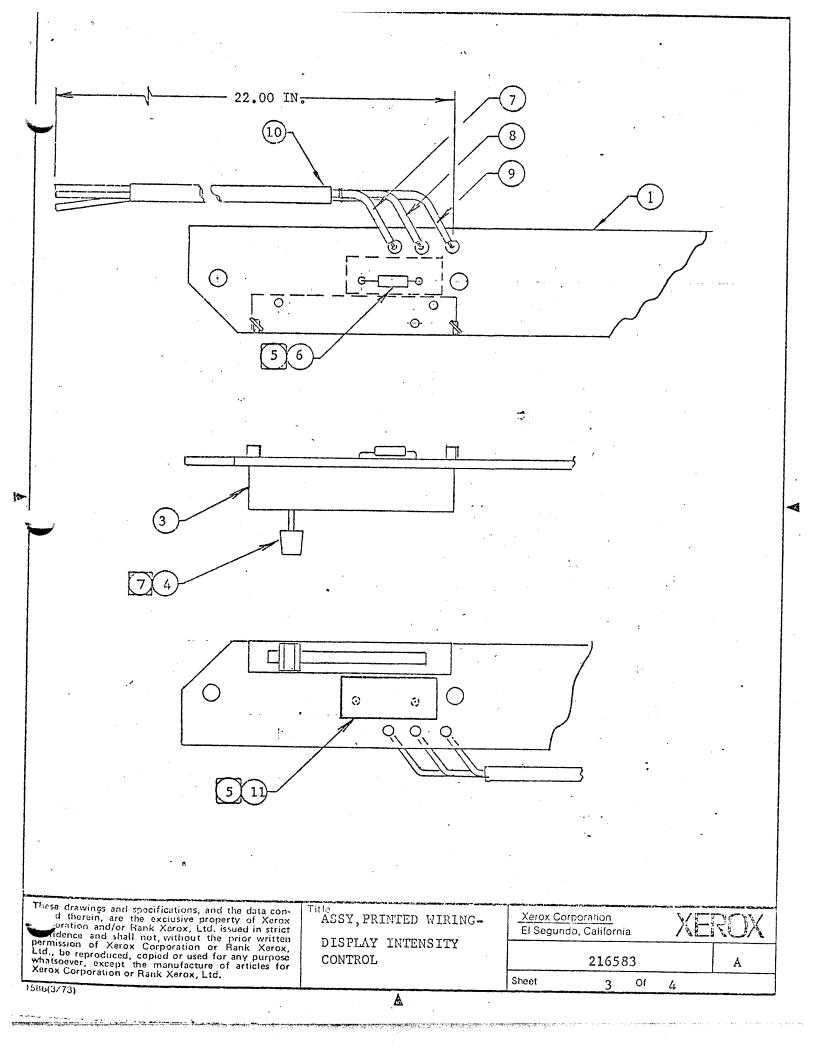
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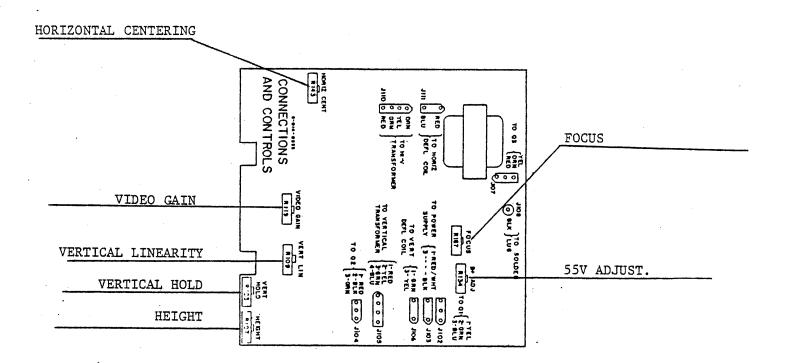
DISPLAY INTENSITY CONT.

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Sheet 2 of 4





DISPLAY ASSEMBLY PCB
AS VIEWED FROM FRONT
OF DISPLAY

SERVICE MANUAL

CRT Data Display TTL Series

5-017-1017

Oct. 27, 1976

Revision B

BALL BROTHERS RESEARCH CORPORATION

ELECTRONIC DISPLAY DIVISION

P.O. BOX 3376 • ST. PAUL, MINNESOTA 55165 • TELEPHONE: (612) 786-8900 • TWX: 910-563-3552



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Section 1 GENERAL INFORMATION

1.1 MONITOR DESCRIPTION

The TTL series Data Display monitor is a solid-state unit for use in industrial or commercial installations where reliable, high quality video reproduction of white alpha-numeric on a black raster.

The monitor features printed circuit board construction for reliability and uniformity. All circuits of the TTL monitor are transistorized. The synchronization circuits have been custom designed to accept video as well as vertical and horizontal drive signals to enable interfacing of this monitor with industrial or digital TTL sources. This feature simplifies the user's sync processing and mixing and allows the unit to operate without composite sync. The electronic packaging has been miniaturized for compatibility with small volume requirements.

1.2 ELECTRICAL SPECIFICATIONS

Input Data Specifications

	·				
	Video	Vertical Drive Signal	Horizontal Drive Signal		
Input Connector	(Necessary Accessory-Available) Printed circuit board card edge connector- Viking #2VK10S/1-2 or Amphenol #225-21031-101				
Pulse Rate or Width	Pulse Width: 45 nsec or greater		Pulse Rate: 15,000 to 16,500 pulses/sec		
Amplitude	Low = Zero $^{+0.4}_{-0.0}$ volts; High = +4 ± 1.5 volts				
Signal Rise & Fall Times (10% to 90% amplitude)	Less than 20 nsec	Less than 100 nsec	Less than 50 nsec		
Input Signal Format		See Figure 1			

Data Display Specifications

Input Impedance	Minimum Shunt Resistance	Maximum Shunt Resistance
(a) Video Input:(b) Vertical Drive Input:(c) Horizontal Drive Input:	3.3K ohms 3.3K ohms 3.3k ohms	40pF 40pF 40pF



Video Amplifier

(a) Bandwidth:

15 MHz (-3 dB)

(b) Rise and Fall Times

Less than 35 nsec. (linear mode)

(10% to 90% amplitude):
Storage Time:

15 nsec, maximum (linear mode)

Retrace and Delay Times

(a) Vertical:

900 µsec retrace, maximum

(b) Horizontal:

9 µsec retrace, maximum

Display Specifications

Cathode Ray Tube: (without bonded panel)

Nominal Diagonal Measurement		*Resoluti	on (TV Lines)
(inches)	Phosphor	Center	Corner
15	P4	1000 at 40 fL	800 at 40 fL
	P39	1000 at 20 fL	800 at 20 fL

^{*}Resolution is measured in accordance with EIA RS-375 except Burst Modulation (or Depth of Modulation) is adjusted for 100 percent.

Geometric Distortion

Geometric Distortion as measured using an "EIA Linearity Chart" in accordance with EIA RS-375 shall be equal to or less than 1.5 percent of the active raster height.

Power Requirements

Power Specifications:

Input Connector	Receptacle, Molex #03-06-2041 Supplied with Unit Mating Plug, Molex #03-06-1041-Necessary Accessory (Available)
Input Voltage	105V to 130V rms (120V nominal); 50/60hz
Input Power	40W (nominal) for 525/60 models.
Output Voltages	+55 VDC (short circuit protected) +17 kVDC; 6.3V rms



1.3 ENVIRONMENTAL SPECIFICATIONS

Temperature (Chassis or Custom Unit)

Operating Range:

5°C to 55°C Ambient

Storage Range:

-40°C to 65°C

Humidity

5 to 80 percent (Noncondensing)

Altitude

Operating Range:

Up to 10,000 feet

1.4 HUMAN FACTORS SPECIFICATIONS

X-Ray Radiation

These units comply with DHEW title 21, Subchapter J.

1.5 CONTROLS

Customer Access - Necessary Accessories (Available)

- (1) Contrast, 500 ohm potentiometer carbon composition ≥1/8 Watt
- (2) Brightness, 100 kilohm potentiometer ≥1/8 Watt
 Optional: The Brightness Control can be mounted
 on the printed circuit board as an internal
 set up control.

Internal Set Up Controls

- (1) Height
- (2) Vertical Linearity
- (3) Vertical Hold
- (4) Focus
- (5) Width
- (6) Low Voltage Adjust
- (7) Horizontal Centering
- (8) Video Gain Adjust



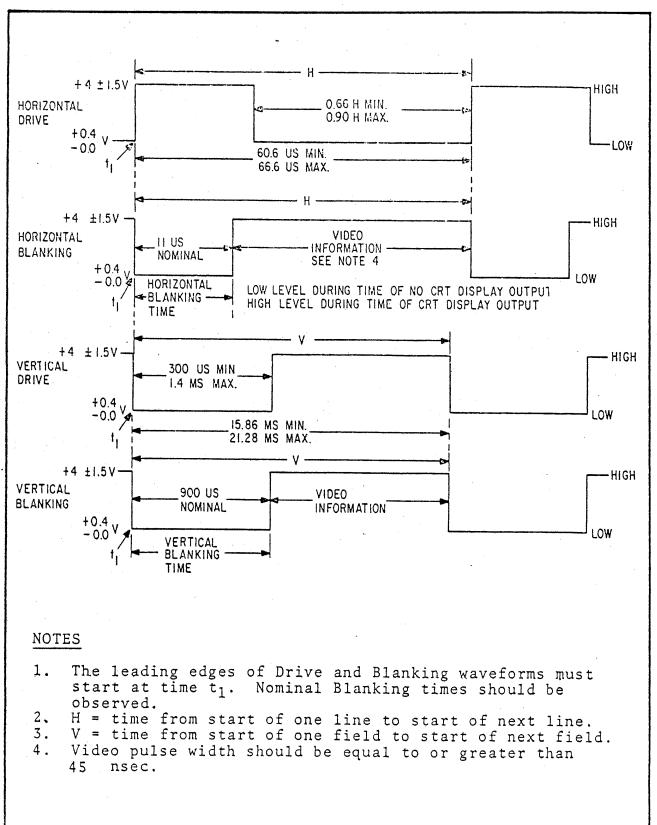


Fig. 1 Synchronization and Blanking Generator Waveforms for the TTL Series Data Display Series.



Section 2 OPERATING PROCEDURES

2.1 INSTALLATION

Power for the TTL monitor is supplied by a self-contained power supply. 120V AC is applied to the unit via a 4 pin molex connector.

The video and synchronization signals are fed to their appropriate connections as indicated on the schematic.

Mount the monitor so that the air flow around the unit is not blocked and the ambient temperature surrounding it does not exceed 55°C.

2.2 GROUNDING TECHNIQUES

The method of interconnecting and grounding the equipment is a function of the signal frequency; any optimum grounding depends largely on the system in which the equipment is used.

The following grounding technique is recommended when installing a TTL Data Display Monitor.

The vertical/video, horizontal drive, vertical drive, and CRT arc bypass are all returned to the chassis plate ground. Normally, it is assumed that the frame and chassis plate of the monitor will be installed in a system where they will be an integral part of system ground. If this is true, then further grounding should not be necessary. However, the mating of the monitor's frame with the system or the generator's signal source ground must be electrically good. Good electrical metal-to-metal contact must be assured.

Where strong radiated noise and signal fields inhibit the monitor's operation or where a signal's waveform is deteriorated by long or poorly selected cabling, careful attention must be given to proper grounding of the outer conductor. Improper grounding can cause annoying ground loops and in some cases cause transistor failures.

The TTL monitor has provisions at the printed circuit board card edge connector to pick up a ground return for the vertical/video, horizontal drive, and vertical drive circuits if a separate return wire is required.

2.3 VIDEO LEAD ROUTING

The video lead probably will carry frequency signals and should be given the following considerations:

- A. To minimize distributed capacity and capacitive pickup of nearby radiated fields, route the video leads separately and away from all other wiring.
- B. Make the lead length as short as possible, consistent with the packaging requirements.



- C. Ideally, the video line should meet the requirements of a terminated coaxial system; i.e., the video line should exhibit a constant impedance from source to load. An effective method of testing the video line is:
 - a. Establish a configuration and keep the foregoing requirements in mind.
 - b. Drive the source end of the video line with the output of TTL logic or an equivalent pulse generator capable of providing pulses with rise and fall times of typically 10 nanoseconds and pulse widths of approximately 100 nanoseconds. Any convenient duty cycle and repetition rate may be used. The generator should be capable of supplying +2.5 volt pulse into a shunt impedance of 3.3k ohms (resistive) and 40 pF (capacitive).
 - c. Observe the pulse at the receiving end of the video line with a low capacitance (less than 5 pF) oscilloscope probe. Adjust the routing and termination of the video line to maintain rise and fall times of 20 nanoseconds or less and overshoots within 10 percent of the pulse amplitude.

2.4 INITIAL TURN-ON PROCEDURE

Connect the video and synchronization signals to the monitor. Apply AC power to the monitor. Adjust the brightness and contrast controls for desired effect and stabilize the picture with the vertical and horizontal hold controls.



Section 3 THEORY OF OPERATION

3.1 VIDEO AMPLIFIER

The incoming video signal of 4V P-P (typical) is applied to the monitor via pin 8. The video signal is applied through R115 to the base of Q103. Transistors Q103 and Q104 form the video amplifier stage for the monitor. Refer to figure 3-1 and schematic at rear of manual.

Transistor Q103 and its components comprise the video inverter amplifier with an adjustable gain of 12 to 25. Q103 operates as a class B amplifier. It remains cutoff until a positive going signal arrives at the base and turns Q103 on. R118 and R119 provide series feedback which makes the voltage gain relatively independent of transistor variations and stabilizes it against voltage and current changes caused by ambient temperature variations.

The negative going signal at the collector of Q103 is direct coupled to the base of Q104, an emitter follower output driver that provides a low source impedance for driving the cathode of the CRT. The class B biasing of Q104 allows more than adequate video output signal to modulate the CRT's cathode and results in a maximum available contrast ratio. Typically about 25V P-P video is required for optimum contrast.

The over-all brightness of the CRT is determined by the negative voltage at the grid and is varied by the brightness control. Normal adjustment range of CRT grid voltage is from +10 to -100VDC.

3.2 VERTICAL DEFLECTION AMPLIFIER

Transistor Q101 is a programmable unijunction transistor and with its external circuitry, forms a relaxation oscillator operating at the vertical rate. The sawtooth forming network consists of R106, R107, R108, C103 and C104. These capacitors charge exponentially until the voltage at the anode of Q101 exceeds its gate voltage at which time Q101 becomes essentially a closed switch allowing a rapid discharge through L101. The oscillator is synchronized by a negative pulse applied to its gate from pin 9.

A divider network consisting of R102, R103 and R104 sets the free running frequency by establishing an adjustable reference voltage at the gate. This feature programs the firing of Q101 and amounts to resistive selection of the intrinsic standoff ratio of the unijunction. The frequency is thus controlled by external elements only; it does not depend on this parameter of the unijunction. CR101 and CR102 provide temperature compensation. L101 forms a tuned circuit with C103 and C104 during conduction of Q101 that provides a stable control on the drop out time of Q101.



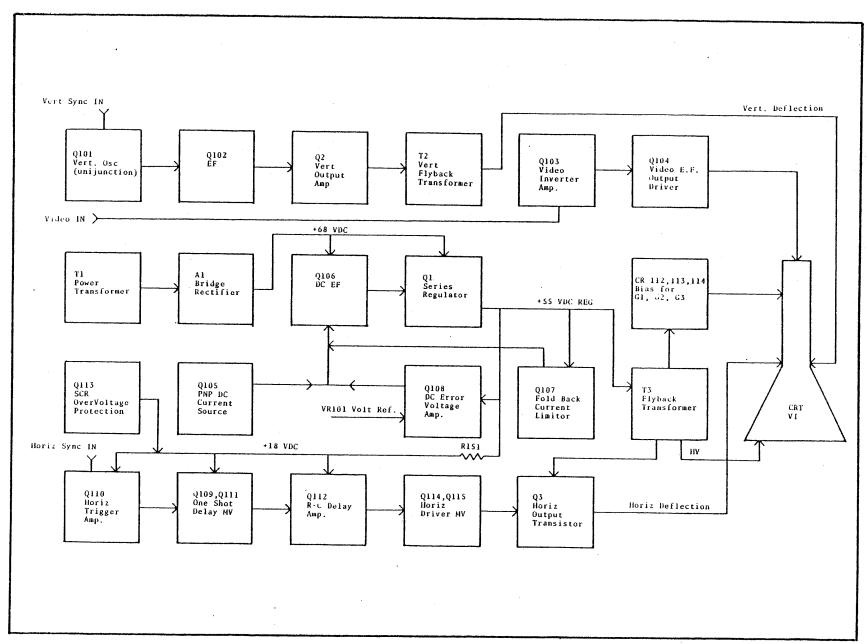


Fig. 3-1 TTL Block Diagram



The sawtooth at the anode of Q101 is directly coupled to the base of Q102. This stage functions as a darlington pair emitter follower driver for the output stage Q2. It presents an extremely high impedance in shunt with R108 and prevents the beta dependent input impedance of Q2 from affecting the frequency of the sawtooth forming network.

Linearity control of the sawtooth is accomplished by coupling the output at Q102 emitter resistively back into the junction of C103 and C104. R110, R109 and C104 integrates the sawtooth and inserts a parabolic component. The slope change rate of the sawtooth at Q102 output is controlled by the setting of R109.

Height control R107 varies the amplitude of the sawtooth voltage developed by controlling the effective B+ applied to R108 and therefore controls the vertical raster size on the CRT.

The vertical output amplifier Q1 uses a power type transistor operating as a class B amplifier. The output is transformer coupled to provide a proper impedance match with the yoke. CR103, R113 and C107 form a clamp circuit which limits the collector voltage at Q2 to safe levels during retrace. R121 prevents oscillations by providing damping across the vertical deflection coils.

3.3 HORIZONTAL DEFLECTION CIRCUITS

The horizontal sync pulse must be delayed almost a full line to provide the proper timing to drive the horizontal output amplifier. Two circuits are used to create this delay: 1) a one-shot delay multivibrator Q109/Q111 and 2) the R-C delay amplifier Q112. The delayed pulse from Q112 is used to trigger the driver multivibrator (MV) Q114/Q115 at the line rate. This MV does not create any significant delay but does establish the proper time duration of approximately one-half line and the output polarity to drive the horizontal output amplifier.

The horizontal sync input signal is applied to pin 6 of the circuit card. This signal is differentiated and the positive edge of the signal is used to trigger Q110. The negative pulse at the collector of Q110 will trigger the one-shot Q109/Q111. After one half line the MV recovers and returns to its original state. The Q109 output signal is applied to Q112 through C117. This causes Q112 to generate a 15 Volt pulse at its collector. After one-third line duration, capacitor C117 discharges through CR108, R144 and R143 and terminates the output signal at Q112. The trailing edge of Q112 output signal is differentiated by C121 and is used to trigger Q114/Q115.

Q114 and Q115 are used as a one-shot driver MV. The normal state of the MV is with Q114 at saturation and Q115 at cut off. A negative going differentiated pulse from Q112 is applied to Q114. This drives Q114 to cut off, and Q115 into saturation. Q114 is held at cutoff by the feedback circuit consisting of R157, R156, C122 and CR109. After approximately one-half line duration, capacitor C122



discharges through R152 and drives Q114 into saturation. Q115 is driven to cutoff and results in a 100 Volt pulse at its collector with an additional 100V transient at the leading edge. This signal is clipped and limited to approximately 55V by R157 and CR110. It is further attenuated to 25V amplitude by resistor divider network R156 and R155. Q115 output signal is also coupled through C122 to initiate regeneration and hold Q114 in conduction until the next trigger pulse arrives.

During conduction of the driver transistor Q115 energy is stored in the coupling transformer. The voltage at the secondary is also negative so that Q3 is held at cut off. When the primary current of T101 is interrupted due to collector cutoff of Q115 the secondary voltage reverses polarity. Q3 goes into conduction due to the positive signal at its base. The collector current of Q3 will slowly increase in a sawtooth pattern during the remaining period of the TV line scan. Typically the peak sawtooth current through Q3 will be two to three amps depending upon line rate and length of TV line scan.

The horizontal output stage has three main functions: to supply the yoke with the correct horizontal scanning currents; develop 17kV for the CRT anode and to develop +800V and -100V for the CRT supply voltages.

Horizontal output transistor Q3 acts as a switch which is turned on and off by the rectangular waveform on the base. When Q3 is turned on, the supply voltage plus the charge on the Cl35 causes yoke current to increase in a linear manner and moves the beam from near the center of the screen to the right side. At this instant, the transistor is turned off by a negative voltage on its base which causes the output circuit to complete one half cycle of sine wave oscillation. A positive flyback voltage pulse of several microseconds duration and several hundred volts amplitude in the form of a half cycle sine wave pulse is developed by the combined inductance of the yoke, T3 and Cl27. The peak magnetic energy which was stored in the yoke during scan time is then transferred to Cl27 and the yoke's distributed capacity. During this cycle, the beam is returned to the center of the screen.

C127 and the distributed capacity now discharge into the yoke and induce a current in a direction opposite to the current of the previous part of the cycle. The magnetic field thus created around the yoke moves the beam to the left of the screen.

After one half cycle, the voltage across C127 swings below ground potential and biases the damper diode CR116 into conduction and prevents the flyback pulse from oscillating. The magnetic energy that was stored in the yoke from the discharge of the distributed capacity and C127 is released to provide sweep for the first half of the scan and to charge C135 through the rectifying action of the damper diode. The beam is then at the center of the screen. The cycle will recur when the base voltage of Q3 is driven positive again.



C135 also serves to block DC currents through the yoke and provide "S" shaping of the current waveform. "S" shaping compensates for stretching at the left and right sides of the CRT. The width sleeve inserted between the yoke and CRT also provides partial linearity correction at the beginning of the horizontal scan.

L104 is an adjustable width control placed in series with the horizontal deflection coils. This variable inductor allows a greater or lesser amount of the deflection current to flow through the horizontal yoke and varies the width of the horizontal scan.

The positive flyback pulse developed during the horizontal retrace time is rectified by CR114 and filtered by C125. This produces approximately 600 volts and is coupled through the focus control R167 to G3 of the CRT. CR112, CR113, C123 and C124 comprise a voltage doubler which delivers approximately 1000 volts to a divider network of R163 and R170. This divider provides approximately 800 volts for G2 of the CRT. This same pulse is transformer-coupled to the secondary of transformer T2 where it is rectified by CR1 and CR115 to produce rectified voltages of approximately 17kV and -100 volts. The 17kV is the anode voltage for the CRT. The -100 volts serves as the source voltage for the brightness control R165 or an external brightness control.

The collector voltage for Q109, Q110, Q111 and Q112 is obtained by dropping the +55 volt supply down to approximately +20 volts by the use of the series dropping resistor R151. The use of a separate lower supply bus provides a means of automatic shut down in the event of an over voltage condition which might generate X-rays and protection of random drive pulses to the horizontal output transistor during "turn on" or "turn off" of the monitor.

Protection against X-rays due to over voltage operation of the line or DC regulator circuits is provided by Q113, VR102 and associated components. In the event the +55 VDC regulator circuit should fail and the output voltage exceed approximately 60 volts, the voltage developed by resistor divider network R147, R148 and R149 will increase also. This increased voltage will cause current conduction through VR102 and R150. The voltage developed across R150 will cause Q113 to fire so that the heavy current will flow from anode to cathode to discharge capacitor C118 and drop the entire supply voltage across R151. This will disable the low level MV's and consequently disable the horizontal output stage and the associated high voltage supply.

This separate supply bus also provides protection against random drive pulses to the horizontal output transistor during "turn on" or "turn off". Normally several AC cycles are required after "turn on" to bring the +55 VDC bus up to normal. By virtue of the component values selected for Q109, Q111 and series dropping resistor R151, Q109 and Q111 would not trigger until the regulator voltage exceeds approximately +30 volts. This DC supply is adequate to provide stable operation of the horizontal circuit and base drive to the horizontal output amplifier so that random drive pulses and poor



collector saturation of Q3 are avoided.

During "turn off" this separate supply bus also offers some degree of protection against CRT spot burn. After AC power is turned off power supply filter capacitor C3 is rapidly discharged by the load current so that the +55 VDC regulator output decays rapidly to 30 volts. Below this level Q109 and Q111 will fail to trigger. As a result the horizontal output transistor and associated HV circuitry are disabled. This will result in a reduction of discharge current from the power supply filter C3 to approximately one third its former rate.

The energy retained by C3 will also be used mainly by the vertical deflection circuit for a significently longer period of operation. The energy of the CRT beam will then be distributed along the vertical axis of the CRT to prevent spot burn while the HV stored in the CRT aquadag is discharged.

3.4 LOW VOLTAGE REGULATED SUPPLY

The AC line voltage is applied through a molex connector to the primary windings of transformer T1 which is located on the power supply module.

The secondary windings illustrated at the bottom of Tl is used to supply 6.3 VAC filament voltage. The other winding is used to apply an AC voltage to Al so that approximately +68 volts is developed across C3.

The +68 volts is dropped to +55 volts by the series regulator Q1. DC regulation of +55 volts is maintained by tapping down the voltage through divider network R133, R134, and R135. Approximately +7 volts at the center tap of R134 is applied to the base of Q108. Also a DC reference voltage from VR101 is applied to the emitter of Q108. This transistor then developes a DC error current which flows through R130 to the base of emitter follower Q106. A DC bias current is supplied to the base of Q106 and the collector of Q108 by Q105 which is used as a DC current generator. The bias current from Q105 will tend to shift the base of Q106 in a positive direction whereas the current from Q108 will tend to shift the base in the negative direction. This results in an error current from the collector of Q108 that controls emitter follower Q106 and also the series pass transistor Q1. The result is that the DC output voltage is maintained at +55 VDC with various load currents and variations of the input AC voltage.

Fold-back current limiting of the +55 VDC supply is provided by means of transistor Q107, resistor R127, R128 and R129. The DC bias current flowing down through R127 and R129 to ground provides a DC drop of approximately 2.4 volts across R127. The DC load current flowing through R128 will provide a voltage drop across this resistor so that the drop is proportional to the load current. If this load current exceeds 2.4A, the emitter of Q107 will be biased approximately 3 volts below the emitter of Q1, assuming that voltage drop of 2.4V across R128 and .6 volts across CR106. In as much



the base of Q107 is biased at 2.4 volts below the emitter of Q1, Q107 will conduct so that the voltage drop across Q105 is increased and the DC output voltage of the supply will decrease below +55 volts. This will limit peak current to approximately 2.4 amperes. In the event of a direct short on the +55 VDC bus, the output voltage will drop to approximately zero and the short-circuit current will be limited to approximately 100MA. Clearing or removing the external short-circuit will allow the regulator to resume normal circuit operation. The average current through Q1 is approximately one half ampere, however the combined peak currents of the horizontal and vertical deflection circuits may be much greater than this even though electrolytic capacitors are used across the +55 volt bus.



Section 4 PRELIMINARY ADJUSTMENTS

4.1 SYNCHRONIZATION AND DRIVE SIGNALS

Apply horizontal and vertical drive signals to the horizontal and vertical drive terminals as indicated on the schematic. Adjust the levels to a nominal 4 volt peak-to-peak.

The horizontal drive signal is required to initiate horizontal scan and high voltage, and should be connected before applying power to the monitor.

4.2 LOW VOLTAGE SUPPLY

Connect a voltmeter between ground and junction of R131 and CR106 cathode. Adjust the B+ voltage control R134 for a reading of 55V.

4.3 BRIGHTNESS

Normally, the monitor will be used to display alphanumeric or other black and white information. Normally the video polarity is usually white characters on a black background.

The brightness control should be adjusted to a point where the white raster is just extinguished. The CRT will then be at its cutoff point, and a maximum contrast ratio can be obtained when a video signal is applied.

4.4 VIDEO CONTRAST

Q103 is designed to operate linearly when a +2.5V signal is applied to its base. An external contrast control (500Ω) is used to maintain this level. This control should be adjusted for a typical signal level of +2.5V peak-to-peak when measured at the video input terminal of the board edge connector. The video gain control R119 should be adjusted for optimum contrast or detail while observing the CRT.

4.5 VERTICAL ADJUSTMENTS

There is a slight interaction among the vertical frequency, height, and linearity controls. A change in the height of the picture may affect linearity. Consequently the adjustment should be carried out in the following sequence:

- (1) Apply video and synchronization signals to the monitor.
- (2) Set the vertical hold control, R103, near the mechanical center of its rotation.
- (3) Adjust the vertical height control R107 for desired height.
- (4) Adjust the vertical linearity control R109 for best vertical linearity.
- (5) Remove the vertical drive signal from the unit or use a short jumper lead and short the vertical drive input terminal of the board edge connector to ground.



- (6) Readjust the vertical hold control R103 until the picture rolls down slowly.
- (7) Restore vertical drive to the monitor and check height and linearity.

4.6 HORIZONTAL ADJUSTMENTS

Raster width is affected by a combination of the low voltage supply, width coil L104, and the width sleeve located on the neck of the CRT beneath the yoke.

- (1) Apply video and sync signals to the monitor.
- (2) Adjust the horizontal width coil L104 for the desired width.
- (3) Insert the width sleeve farther under the yoke to obtain the best linearity. Although this adjustment will affect the raster width, it should not be used solely for that purpose. It should be inserted only as far as required for adequate linearity correction, otherwise excessive current will be drawn by the horizontal output amplifier.
- (4) Readjust L104 for proper width.
- (5) Observe final horizontal linearity and width, and touch up either adjustment if needed.

No horizontal hold control is used in this monitor. The raster should be properly locked and can be centered with the video centering control R143.

4.7 FOCUS ADJUSTMENT

The focus control, R165, provides an ajustment for maintaining best over-all display focus.

4.8 CENTERING

If the raster is not properly centered, it may be repositioned by rotating the ring magnets behind the deflection yoke. The magnets should not be used to offset the raster from its nominal center position because it would degrade the resolution of the display. If the picture is tilted, rotate the entire yoke.



Section 5 TROUBLE SHOOTING AND MAINTENANCE

5.1 ISOLATION OF CIRCUIT FAULTS

In the event of failure or malfunction of the monitor there is a sequence of simple steps which can be carried out to isolate the fault to a particular circuit area. The first thing to check is the +55 VDC bus. In the event of a short circuit the voltage regulator will "foldback" to limit the current. In the event of a direct short the DC voltage will decay to zero. In other situations excessive load current will cause the regulator to "fold back" and then "start up" again. This cycle may reoccur at a relatively high rate such as a thousand times per second which is probably due to attempting to energize a faulty horizontal output stage. A low audible buzz may often be heard. This "fold back" and "restart" of the regulator may also occur at a much lower rate such that it appears to be synchronized with the vertical rate. This probably would be due to excessive pulse current drawn by the vertical output deflection amplifier. Isolation of faulty circuit blocks may be done as follows:

- (1) Disconnect Molex connector to J104 to isolate the vertical output stage.
- (2) Disconnect Molex connector to J110 to isolate the flyback transformer and horizontal output transistor stage.

NOTE: Removal of connector at J110 will open circuit the "ground" wire conductor to chassis. Use short "alligator clip lead" between chassis and case of aluminum filter capacitor C3 in power supply module to reestablish ground connection.

Removal of the above two circuit blocks should reduce load current on the +55 VDC regulator to a fraction of the former value. Failure of the regulator to perform normally should probably be attributed to a shorted electrolytic capacitor on the board or defective components in the regulator circuit.

Actual isolation of a fault to a single transistor stage is best accomplished by use of a scope and reference to typical waveforms contained in this section. The most critical tests or waveforms of the horizontal output stages are:

(1) Driver transformer (T101) primary waveform

(2) Horizontal flyback pulse at Q3-C

- (3) Radiated pulse from flyback transformer T3 (Hold a 10:1 scope probe approximately 2" away from the HV flyback transformer).
- (4) Check parabolic waveform voltage across "s" shaping capacitor C135.
- (5) Measure DC current to horizontal output amplifier by measuring voltage drop across R168. Typical current of .25 A DC should generate a voltage drop of .3 volt DC. Current on high line rate models should run somewhat higher.



(6) "Tearing" of raster may be due to "over Voltage" adjustment of the +55 VDC regulator. This may cause erratic "firing" of the SCR transistor Q113. Absence of drive signals to horizontal output stage may be due to complete shut down of SCR Q113.

Tests on the power transistor circuits located on the PC board can be carried out by the use of scope and reference to typical voltage waveforms.

Typical waveforms are illustrated by section 5.2. Waveforms of high line rate models are similar with the time duration of the waveforms will be somewhat less, i.e. they should be scaled in time such that they are proportional to the time of a horizontal line. The amplitude of the horizontal flyback pulse should be somewhat less due to lower values of yoke inductances. Waveforms which occur at field rate should be similar. Figure 5-1 illustrates the component location and the location of the molex connectors and wire color codes.

Waveforms which occur at field rate were taken with the scope externally synchronized to the leading edge of vertical drive. In the case of waveforms at horizontal line rate the scope was synchronized to the leading edge of horizontal drive, consequently the time relationship of each waveform actually indicates the relative time delay of each multivibrator. In most cases, the scope was DC coupled when the waveforms were taken so that the relative position of ground potential on the waveforms could be indicated.



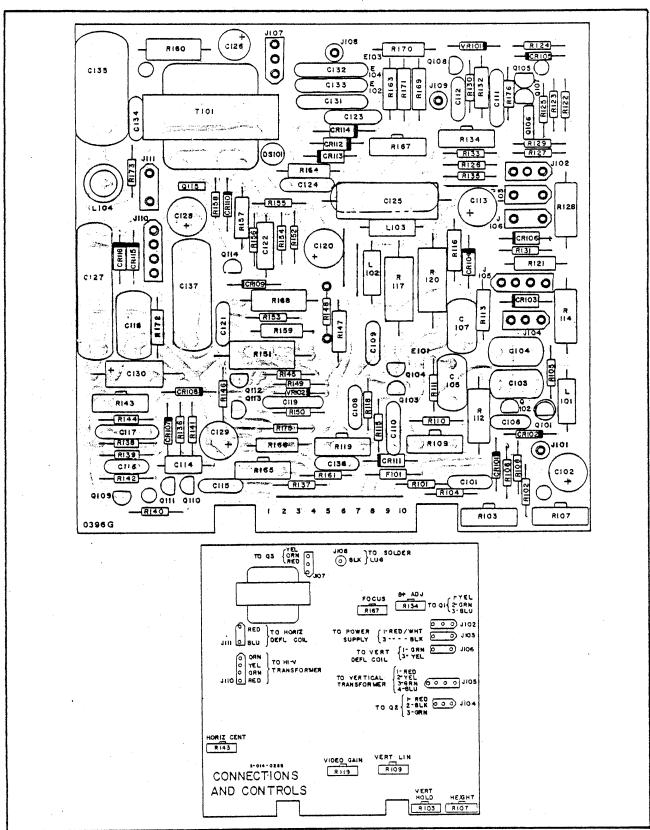


Fig. 5-1 Circuit Board Component Location and Intercabling Diagram.



5.2 TYPICAL WAVEFORMS AND VOLTAGES

Q101-anode Field Rate		
1V/Div		# _
Ground Ref.		
Q2-Base	•	
Field Rate		
1V/Div		‡ 2
Ground Ref		
Q2-Emitter		
Field Rate		3
1V/Div		7
Ground Ref		
Q2-Collector		
Field Rate		4
50V/Div		7

Ground Ref.

5

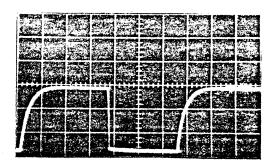
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#7

#8

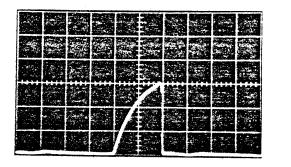


Q109-Collector Line Rate 5V/Div



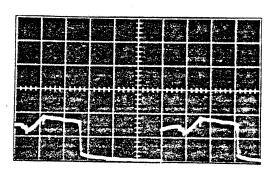
Ground Ref.

Q112-Collector Line Rate 5V/Div



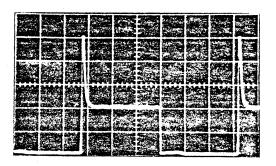
Ground Ref.

Q114-Collector Line Rate .5 Volt/Div



Ground Ref.

Q115-Collector Line Rate 50V/Div

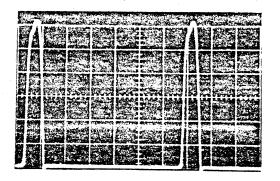


Ground Ref.



Q3-Collector
(Horiz. Flyback Pulse)
Line Rate
100V/Div

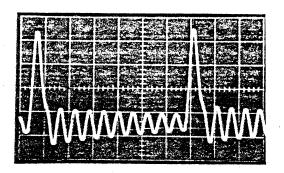
Ground Ref.



#9

Radiated Pulse from flyback Transformer. 10:1 Probe held 2" away Line Rate 50V/Div

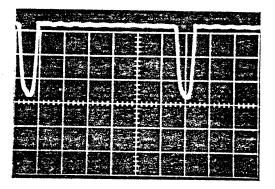
Scope AC Coupled



#10

Ground Ref.

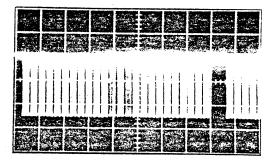
CR115 Cathode Line Rate 50V/Div



#11

CRT Cathode Line Rate 10V/Div

Ground Ref.



#12



Section 6 TTL Parts List

SYMBOL	DESCRIPTION	BBRC PART NUMBER
A1	Bridge Rectifer, VS148	1-021-0413
	CAPACITOR, fixed; uF unless otherwise stated	
C1	.001 ± 10%; 1000V, ceramic disc	1-012-0540
C2	.01; 1000V, arc gap ceramic	1-021-0112
C3	1400; 90V, electrolytic	1-012-2186
C101	.001 ± 10%; 1000V, ceramic disc	1-012-0540
C102	50; 50V, electrolytic	1-012-2157
C103	.22 ± 10%; 200V, mylar	1-012-0930
C104	.22 ± 10%; 200V, mylar	1-012-0930
C105	.22 ± 10%; 200V, mylar (TTL15/AM, TTL15/C, TTL15/875, TTL15/1029)	1-012-0950
C106	.001 ± 10%; 1000V, ceramic disc (TTL15/AM, TTL15/C, TTL15/875, TTL15/1029).	1-012-0540
C107	.1 ± 10%; 400V, mylar	1-012-2239
C108	250pF ± 5%; 500V, dipped mica	10-57-5251
or	18ŐpF ± 5%; 500V, dipped mica (TTL15/C, TTL15/875, TTL15/1029)	10-57-5181
C109	100pF ± 5%; 500V, dipped mica	1-012-0300
C110	.1 ± 20%; 100V, ceramic disc	10-12-7104
C111	.1 ± 20%; 100V, ceramic disc	10-12-7104
C112	27pF ± 5%; dipped mica	1-012-2161
C113	5; 150V, electrolytic	1-012-2195
C114	.0039 ± 10%; 200V, mylar	10-47-7392
or	.0033 ± 10%; 200V, mylar (TTL15/AV)	10-47-7332
or	.0022 ± 10%; 200V, mylar (TTL15/C, TTL15/875)	10-47-7222
or	.0018 ± 10%; 200V, mylar (TTL15/AM)	10-47-7182
or	.0015 ± 10%; 200V, mylar (TTL15/1029)	10-47-7152
C115 ·	.001 ± 10%; 1000V, ceramic disc	1-012-0540
C116	22pF ± 5%; 500V, dipped mica	10-57-5220
C117	750pF ± 5%; 100V, dipped mica	10-57-5751
C118	.47 ± 10%; 200V, mylar	1-012-1927
C119	.1 ± 20%; 100V, ceramic disc	10-12-7104
C120	5; 150V, electrolytic	1-012-2195
C121	.001 ± 10%; 1000V, ceramic disc	1-012-0540
or	.002 ± 10%; 500V, ceramic disc (TTL15/AM)	10-16-7208
C122	.0082 ± 10%; 200V, mylar	10-47-7822
or	.0022 ± 10%; 200V, mylar (TTL15/AM, TTL15/C, TTL15/1029, TTL15/875)	10-47-7222
C123	.01 ± 20%; 1000V, ceramic disc	1-012-2214
C124	.02 ± 20%; 1000V, ceramic disc	1-012-2217
C125	.015 ± 10%; 1000V, film/paper	1-012-2201
or	.1 ± 10%; 600V, mylar (TTL15/1029)	1-012-2202
C126	25; 25V, electrolytic	1-012-2212
or	10; 25V, electrolytic (TTL15/AM, TTL15/C, TTL15/1029, TTL15/875)	1-012-3211



			BBRC
SYMBOL		DESCRIPTION	PART NUMBER
C127		.0056 ± 10%; 2000V, mylar	10-35-7562
	or	.005 ± 10%; 1600V, mylar (TTL15/0)	1-012-2232
	or	.0068 ± 10%; 1600V, mylar (TTL12)	1-012-2210
C128		5; 150V, electrolytic	1-012-2195
C129		5; 150V, electrolytic	1-012-2195
C130		1; 150V, electrolytic	1-012-2168
C131		.01; 1000V, arc cap, ceramic	1-012-0112
C133		.01; 1000V, arc cap, ceramic	1-012-0112
C133		.01; 1000V, arc cap, ceramic	1-012-0112
C134		001 ± 10%; 1000V, ceramic disc	1-012-0540
C135		1.5 ± 10%; 100V, polycarbonate	1-012-2216
C136		2pF; 250V, arc cap	1-012-0111
C137		1 ± 10%; 100V, mylar	1-012-1025
C138		250pF ± 5%; 500V, dipped mica	10-57-5251
	કેંગ્ર	DIODE	
CR101		1N 360 5	1-021-0410
CR102		1N3605	1-021-0410
CR103		1N3280	1-021-0403
CR104		1N628	1-021-0160
CR105		1N3605	1-021-0410
CR106		1N4001	78-62-4001
CR107		1N3605	1-021-0410
CR108		1N360S	1-021-0410
CR109		1N3605	1-021-0410
CR110		1N628	1-021-0160
CR111		1N3280	1-021-0403
CR112		1N3280	1-021-0403
CR113		1N3280	1-021-0403
CR114		VG-1X	1-021-0447
CR115		1N3280	1-021-0447
CR116		1N5398	1-021-0403
CR1		RHC-25-20	1-021-0438
DS101		NO. 1764	1-026-0308
		FUSE	1.020-0308
F1		3/4A-125	
	or	1A-125	1-028-0242
F2	01	2A-125V	28-13-0100
		•	1-028-0249
7.1		COIL	
L1		Deflection coil assembly	6-004-0323
	or	Deflection coil assembly (TTL15/0)	6-004-0676
	or	Deflection coil assembly (TTL12)	6-004-0350
	or	Deflection coil assembly (TTL15C)	6-004-0347
		Deflection coil assembly (TTL15/1029)	6-004-0328
		Deflection coil assembly (TTL15/875)	6-004-0336
	or	Deflection coil assembly (TTL15/AM)	6-004-0354



SYMBOL		DESCRIPTION	BBRC PART NUMBER
L2	,	10 µH	15-13-1100
L101		560µH	1-016-0302
L102		4.7µH	15-13-1479
L103		22µH	15-13-1220
L104		Width Coil	1-016-0304
	or	Width coil (TTL15/0)	1-016-0309
	or	Width coil (TTL15/AM, TTL15/C, TTL15/1029, TTL15.875)	1-016-0299
		TRANSISTOR	
Q1		DTS-410	78-85-0410
Q2		2SD-199	1-015-1176
Q3		DTS-402 (525)	78-85-0402
	or	DTS-802 (TTL15/875)	1-015-1189
	or	A705 (TTL15/0)	78-85-0709
Q4		A 7 0.5	78-85-0709
Q101		2N6027	1-015-1157
Q102		MPS-A65	1-015-1186
Q103		MPS-6565	1-015-1185
Q104		MPS-6565	1-015-1185
Q105		MPS-L51	1-015-1175
Q106		MJE-340	78-86-0340
Q107		2N 5 8 3 0	1-015-1172
Q108		2N 5 8 3 0	1-015-1172
Q109		2N4124	1-015-1139
Q110		2N4124	1-015-1139
Q111		2N4124	1-015-1139
Q112		2N4124	1-015-1139
Q113		2N5060	1-015-1168
Q114		MPS-A16	1-015-1193
Q115		MJE-340	78-86-0340
		RESISTOR fixed film: 4W ± 5% unless otherwise stated	
R1		500M; 6W, deposited carbon (AA version)	1-011-1800
	or	500M; 6W, deposited carbon (BB version)	1-011-2456
R2		$1\Omega \pm 10\%$; 3W, wirewound	1-011-1742.
R3		$1\Omega \pm 10\%$; 3W, wirewound	1-011-1742
R101		470Ω	70-16-0471
R102		470Ω	70-16-0471
	or	10K (TTL15/AM, TTL15/C, TTL15/1029, TTL15/875)	70-16-0103
R103		Var; 10K ± 20%; 1/8W, composition, vertical hold	1-011-5312
	or	Var; 5K ± 20%; 1/8W, composition, vertical hold	
		(TTĹ15/1029, TŤL15/AM, ŤŤL15/C, TŤL15/875)	1-011-5637
R104		6.8K	70-16-0682
R105		100K	70-16-0104
R106		2.7 K	70-16-0273
R107		Var; 50K ± 20%; 1/8W, composition, vertical height	1-011-5373
R108		220K	70-16-0224
	or	470K (TTL15/AM, TTL15/C, TTL15/875, TTL15/1029)	70-16-0474



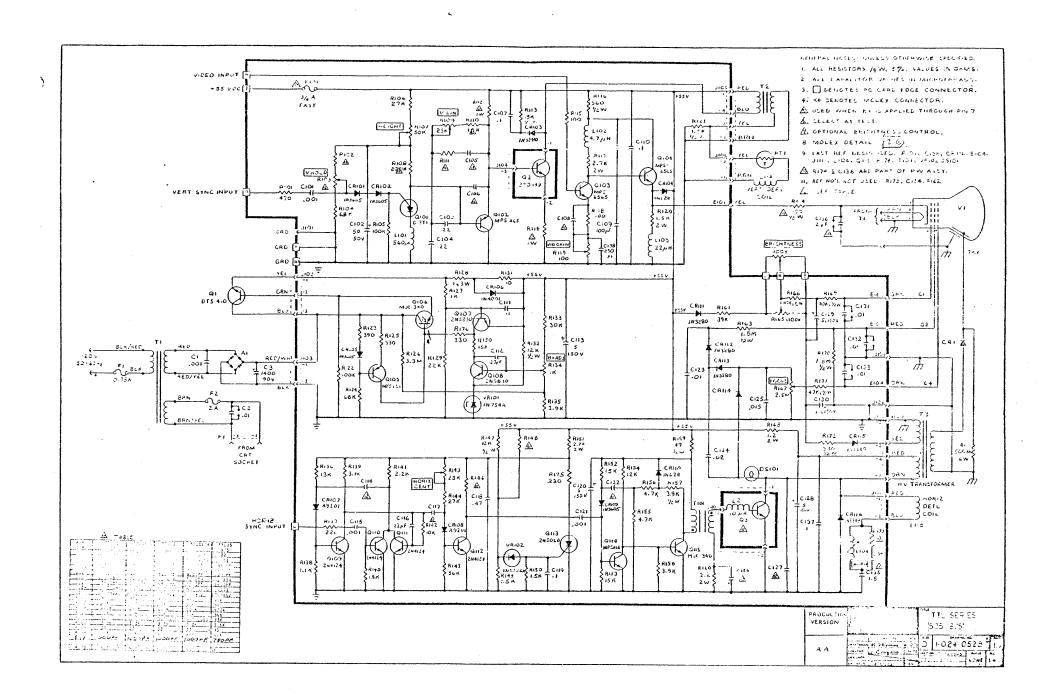
SYMBOL	DESCRIPTION	BBRC PART NUMBER
R109	Var; 25K \pm 20%; 1/8W, composition, vertical lin.	1-011-5325
R110	10K	70-16-0103
or	36K (TTL15-AM, TTL15/C, TTL15/875, TTL15/1029)	70-16-0363
R111	33K (TTL15/AM, TTL15/C, TTL15/875, TTL15/1029)	70-16-0333
R112	3.3K; lW, composition	1-011-2425
or	5.6K; lW, composition (TTL15/AM, TTL15/C, TTL15/875, TTL15/1029)	1-011-2444
R113	15K	70-16-0153
R114	350; 1W, composition	1-011-2426
or	22Ω; 1W, composition (TTL15/AM, TTL15/C)	1-011-2421
or	15Ω; 1W, composition (TTL15/875, TTL15/1029)	78-15-0150
R115	100Ω	70-16-0101
R116	560Ω; ¼W	1-011-2264
R117	2.7K; 2W, composition	1-011-2420
R118	100Ω	70-16-0101
R119	Var; $100\Omega \pm 20\%$; 1.8W, composition, video gain	1-011-5095
R120	1.5K; 2W, composition	1-011-1500
R121	1.5K; ½W	1-011-2274
R122	- 100K	70-16-0104
 R123	390Ω	70-16-0391
R124	68K	70-16-0683
R125	330Ω	70-16-0331
R126	3.3M	70-16-0335
R127	1K	70-16-0102
R128	$1\Omega \pm 10\%$; 3W, wirewound	1-011-1742
R129	22K	70-16-0223
R130	15K	70-16-0153
R131	10Ω	70-16-0100
R132	12K; ½W	1-011-2296
R133	30K	70-16-2296
R134	Var; 1K ± 20%; 1/8Wc compsition, B+ adjust	1-011-5182
R135	3.9K	70-16-0392
R136	13K	70-16-0133
R137	2.2K	70-16-0222
R138	1.1K	70-16-0112
R139	3.9K	70-16-0392
R140	1.5K	70-16-0152
R141	2.2K	70-16-0222
R142	10K	70-16-0103
R143	Var; 25K ± 20%; 1/8W, composition, norizontal centering	1-011-5325
R144	27K	70-16-0273
or	22K (TTL15/1029, TTL15/875)	70-16-0223
R145	56K	70-16-0563
R146	22K	70-16-0223
R147	12K; ¼W	1-011-2296
R148	Selected	

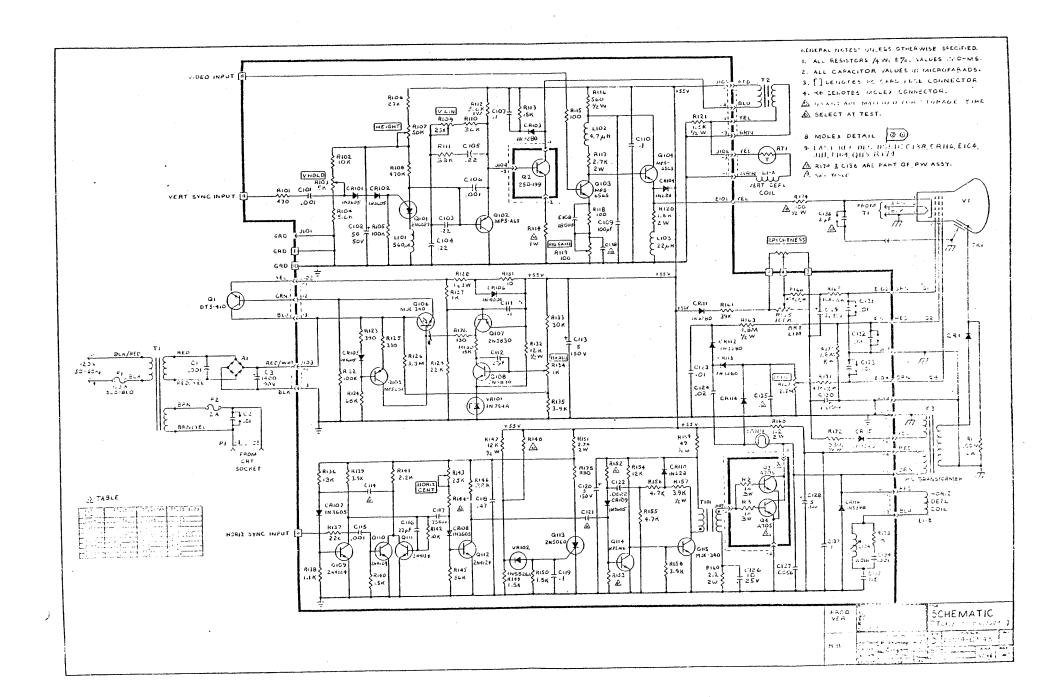


SYMBOL		DESCRIPTION	BBRC PART NUMBER
R150		1.5K	70-16-0152
R151		2.7K; 2W, composition	1-011-2420
R152		15K	70-16-0153
	or	22K (TTL15C, TTL15/AM, TTL15/875)	70-16-0223
	or	20K (TTL15.1029)	70-16-0203
R153		15K	70-16-0153
	or	8.2K (TTL15/1029)	70-16-0822
R154		12K	70-16-0123
R155		4.7K	70-16-0472
R156		4.7K	70-16-0472
R157		3.9K; ½W	1-011-2284
R158		3.9K	70-16-0392
R159		47Ω; ½W	1-011-2238
R160		2.2Ω; 2W, wirewound	1-011-0120
R161		39K	70-16-0393
R162		Not used	
R163		1.8M; ½W	1-011-2348
R164		100 Ω ; ½W (used model without DS201)	1-011-2246
R165		Var; 100K ± 20%; 1/8W, composition brightness (optional)	1-011-5435
R166		47K; ½W	1-011-2310
R167		Var; 2.5M ± 20%; 1/8W, composition focus	1-011-3566
R168		1.2Ω; 2W, wirewound	1-011-1395
R169		10K; ½W	1-011-2294
R170		1.8M; ½W	1-011-2348
R171		47K; ½W	1-011-2310
R172		330, ½W	1-011-2258
R173		1K (875)	70-16-0102
R174		100Ω; ½W	1-011-2246
R175		350Ω	70-16-0331
R176		350Ω	70-16-0331
RT1		Thermistor, 2.50 3 25°C (part of L1)	1-011-7008
		TRANS FORMER	
T1		Power	1-017-5400
	or	Power (TTL15/AM, TTL15/C, TTL15/1029, TTL15/875)	1-017-5391
T 2		Vertical output	6-003-0341
T 3		High voltage (TTL15)	6-003-0407
	or	High voltage (TTL15/875)	6-003-0464
	or	High voltage (TTL15/AV)	6-003-0404
	or	High voltage (TTL15/AM)	6-003-0496
	or	High voltage (TTL12)	6-003-0436
	or	High voltage (TTL15/C)	6-003-0446
	or	High voltage (TTL15/1029)	6-003-0448
T101		Horizontal driver	1-017-5380
	or	Horizontal driver (TTL15/0, TTL15/AM, TTL15/C, TTL15/1029)	
		JENER DIODE	. v. 0090
VR101		1N754A	78-15-0754
VR102		1N5526	1-021-0449
			T-04T-0443



SYMBOL	DESCRIPTION	BBRC PART NUMBER
	MISCELLANEOUS	
V1	15 inch CRT	
	Assembly, main chassis board (TTL15)	6-002-0525
	Assembly, main chassis board (TTL15/0)	6-002-0560
	Assembly, main chassis board (TTL15/C)	6-002-0582
	Assembly, power supply module (TTL15/1029, TTL15/875)	6-003-0412
	Assembly, main chassis board (TTL12)	6-002-0569
	Assembly, main chassis board (TTL12 W/Brt control)	6-002-0574
	Assembly, main chassis board (TTL15 W/Brt control)	6-002-0551
	Assembly, switchable power supply module	6-003-0424
	Assembly, neatsink (TTL15/0)	6-003-0434
	Assembly, neatsink (TTL15/AM, TTL15/1029, TTL15/C)	6-003-0446
	Assembly, heatsink	6-003-0396
	Assembly, heatsink (TTL15/875)	6-003-0411
	Assembly, neatsink (TTL15/875)	6-003-0404
	Assembly, main chassis board (TTL15/AV)	6-002-0620
	Assembly, main chassis board (TTL15/1029)	6-002-0589
	Assembly, main chassis board (TTL15/875)	6-002-0537
	Assembly, main chassis board (TTL15/AM)	6-002-0638
	Assembly, power supply module (TTL15/C, TTL15/AM)	6-003-0445
	Assembly, power regulator module (TTL15/I)	6-003-0409







INSTRUCTION MANUAL ADDENDUM

MODEL EFFECTED: TV-15 and TTL Series

SUBJECT: 100-240V Low Voltage Power Supply

This is an optional supply module for use on the TV15 \S TTL series data display and is capable of operating from input line voltages of 100V, 120V, 220V or 240V, 50/60Hz.

The power supply input voltage is determined by the setting of the two slide switches located at the rear of the supply. These switches are stamped to indicate the appropriate line voltage setting.

To set the supply for a particular line voltage, the numbers on the two switches are added together. This enables the supply to be set for four different input line voltages. The position of the switches and the resultant input voltages are:

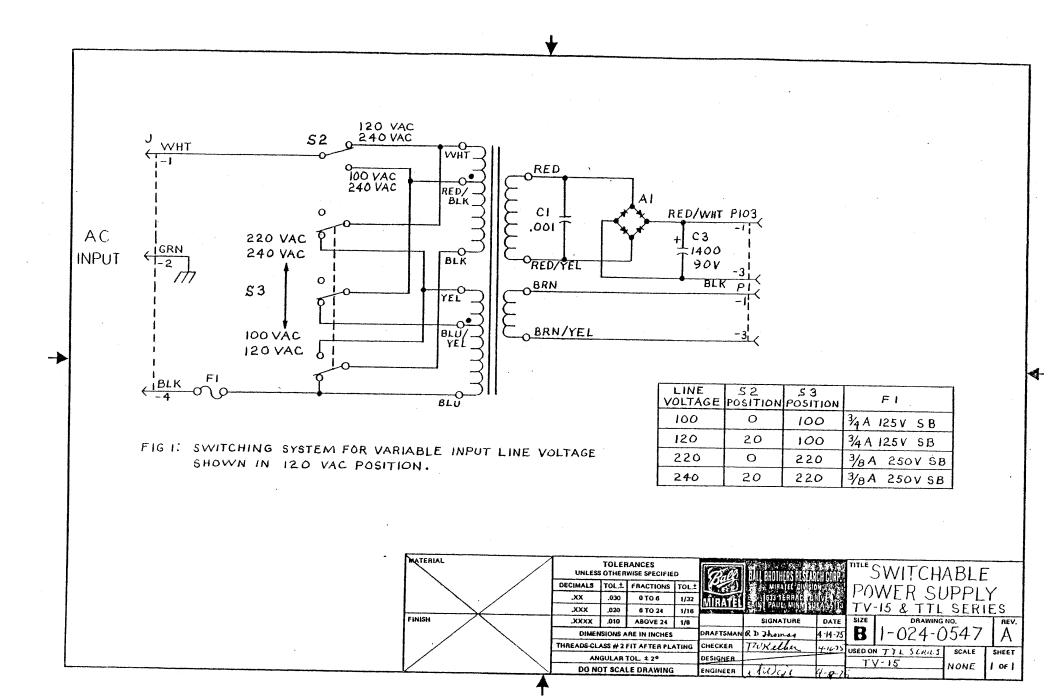
S 2	Position	S 3	Position	AC	Line	Voltage
	0		100	•	100)
	20		100	-	120	o '
	0		220		22	0
	20		220		240	j

When changing input voltage from 100/120 to 220/240 volt, the fuse (F1) must also be changed.

FUSE SIZE TABLE

PART LIST ADDENDUM

T1	Power Transformer	1-017-5400
S 2	Switch, Slide, SPDT	1-018-0255
S3	Switch Slide, 3PDT	1-018-0256
	Power Supply Module	6-003-0424



ETHERNET

An Ethernet is the principal means of communications between an Alto and the outside world. The object was to design a communication system which could grow smoothly to accommodate several buildings full of personal computers and the facilities needed for their support. The Ethernet is a broadcast, multi-drop, packet-switching, bit-serial, digital communications network: it connects up to 256 nodes, separated by as much as 1 kilometer, with a 2.94 megbits/sec, channel. Control of the Ethernet is distributed among the communication computers to eliminate the reliability problems of an active central controller, to avoid a bottleneck in a system, rich in parallelism, and to reduce the fixed costs which make small systems unecononical.

The Ethernet is intended to be an efficient, low-level packet transport mechanism which gives its best efforts to delivering packets, but it is not error free. Even when transmitted without source-detected interference, a packet may not reach its destination without error; thus, packets are delivered only with high probability. Stations requiring a residual error rate lower than that provided by this bare packet transport mechanism must follow mutually agreed upon packet protocols.

Alto Ethernets come in three pieces: the transceiver, the interface, and the microcode. The transceiver is a small device which taps into the passing Ether, inserting and extracting bits under the control of the interface while disturbing the Ether as little as possible. The same device is used to connect all types of Ethernet interfaces to the Ether, so the transceiver design is not specific to the Alto, and will not be described here.

When a program wishes to send a packet, it must first turn off the receiver if it is on. If the receiver is actively copying a packet into memory, the transmitter should wait for the reveiver to finish (a maximum of about 1.5 msec. assuming 250-300 word packets). The program can tell whether the receiver is actively transferring or idle by zeroing the first word of the input buffer before starting the receiver. When the program wants to start the transmitter, it checks the first word of the input buffer: if it is still zero, input has not yet begun and the interface may be reset and the transmitter started with a high probability of not missing an incoming packet. There is still a small window between testing the word and starting the transmitter when a packet can arrive and be missed, but paragraph two warned that the Ethernet is not error free anyway, so missing a few more packets should be harmless.

The first word of all Ethernet packets must contain the address to which the packet is destined in the left byte, and the address of the sender (or source') in the right byte. Reveivers examine at least the destination byte, and in some cases (not in Altos) the source byte to determine whether to copy the message into memory as it passes by. Address zero has special meaning to the Ethernet. Packets with destination zero are broadcast packets, and all receivers will receive them. If a program wishes to receive all packest on the Ether regardless of address (useful for debugging and diagnostic programs), it should use zero. A host which does this is said to be promiscuous. Address 377 (octal) is reserved for Ethernet booting. Address 376 (octal) is reserved as the destination for diagnostic messages.

ETHERNET HARDWARE

The Ethernet hardware consists of a FIFO buffer, an output shift register and phase encoder, a clock recovery circuit, an input shift register, a CRC register, and one microcode task. Packets on the Ether are phase encoded and transmitter synchronous; it is the responsibility of the receiver to decide where a packet begins (and thus establish the phase of the data clock), separate the clock from the data, and deserialize the incoming bit stream. The purpose of the write register is to synchronize data transfers between the input shift register whose clock is derived from the incoming data, and the FIFO which is synchronous to the processor system clock. The large FIFO is necessary because the Ethernet task has relatively low priority, and the worst case latency from request to task wakeup is on the order of 20 microseconds. The phase encoder uses the system clock (one Ethernet bit time is two clock periods).

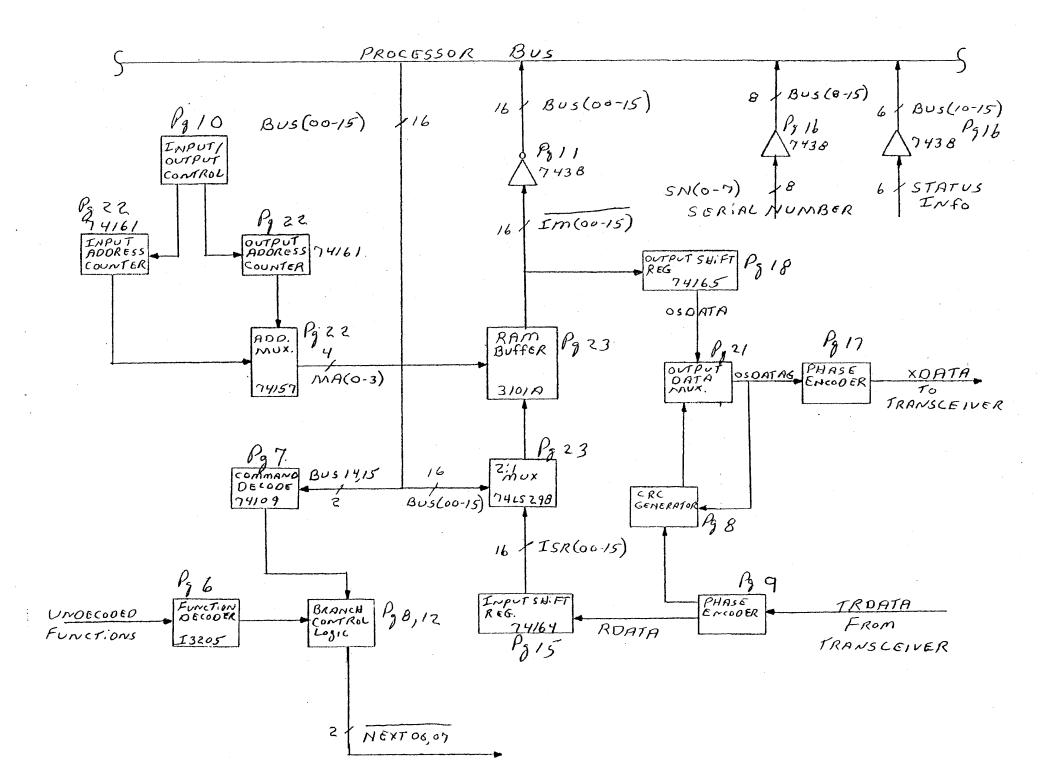
Included in the clock recovery section is a one-shot which is retriggered by each level transition of a passing packet. This detects the envelope of a packet and is called its 'carrier'. Ethernet phase encoders mark the beginning of a packet by prefixing a single 1 bit, called the sync bit, to the front of all transmissions. The leading edge of the sync bit of a packet will trigger the carrier one-shot of a listening receiver and establish the receiver clock phase. The sync bit is clocked into the input shift register and recirculated every 16 bit times thereafter to mark the presence of a complete word in the register. If carrier drops without the sync bit at the end of the register, the transmission was imcomplete, and is flagged in the hardware status bits. When the shift register if full, the word is transferred to the write register where it sits until the FIFO control has synchronized its presence and there is room to accept it. If the shift register fills up again before the word has been transferred from the write register to the FIFO, data has been lost and the input data late flip flop is set.

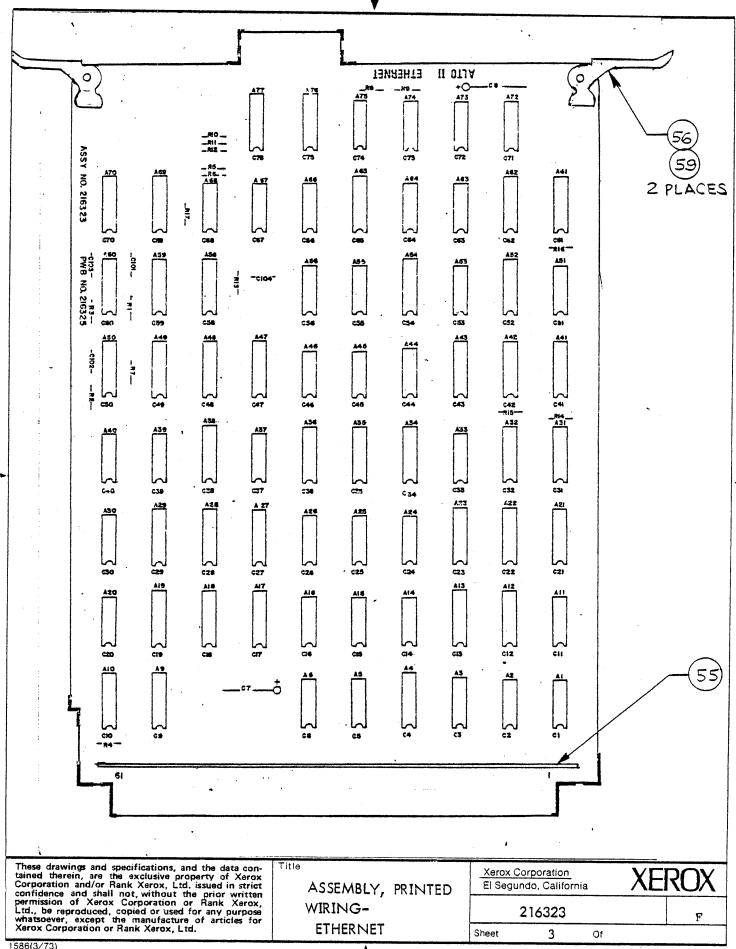
Ethernet transmitters accumulate a 16 bit cyclic redundancy checksum on the data as it is serialized, and append it to an outgoing packet after the last data word. As a receiver deserializes an incoming packet it recomputes the checksum over the data plus the appended CRC word. If the resulting receiver checksum is non-zero, the received packet is assumed to be in error; and the condition is flagged in the hardware status byte.

The phase encoder is started when the microcode has decremented the countdown to zero, there is no carrier present, and either the FIFO is full, or if the message is less than 16 words long, all of it has been transferred to the FIFO. The phase encoder will not start up while there is carrier present. This means that collisions can only happen because of delay in sensing carrier between widely spaced transmitters. Collisions are detected at the transceiver by comparing the data the interface is suppling to the data being received off the Ether. If the two are not identical, a signal is returned to the interface which sets the collision flip flop causing a wakeup request to the microcode which resets the interface.

The interface and the transceiver are connected together by three twisted pairs for signals plus supply voltages and ground supplied from the interface. The signals are (1) transmitted data to the transceiver, (2) received data from the transceiver, and (3) the collision signal from the transceiver indicating interference.

ETHERNET MODULE



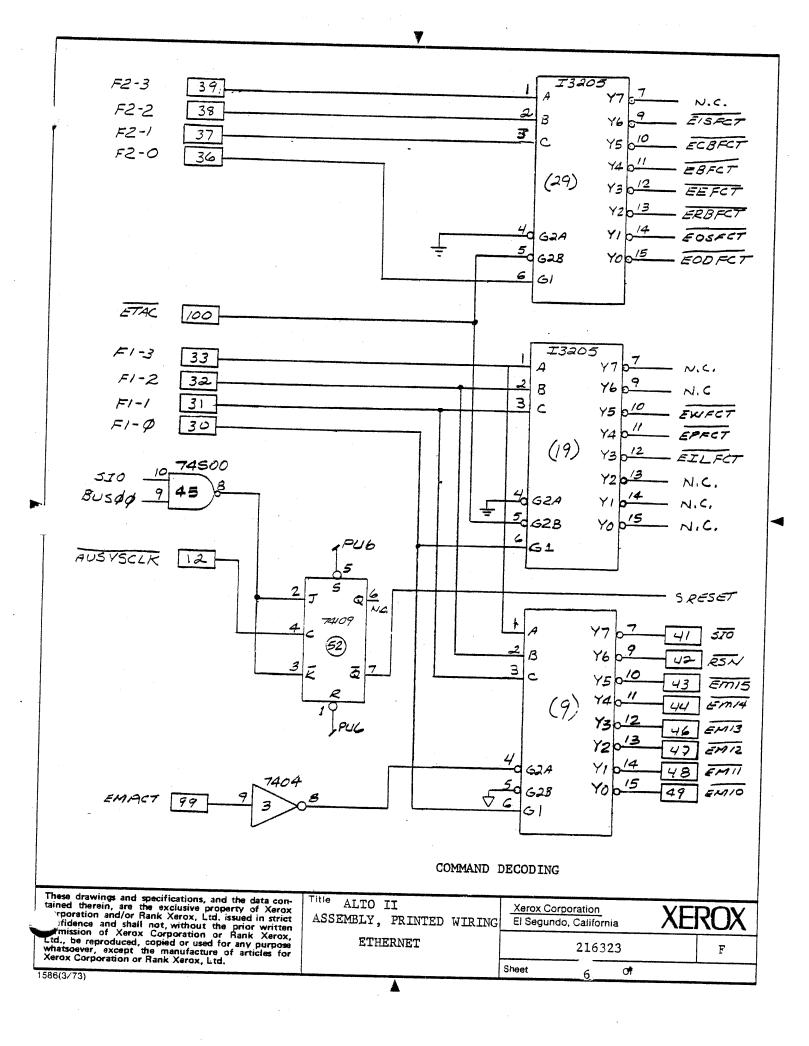


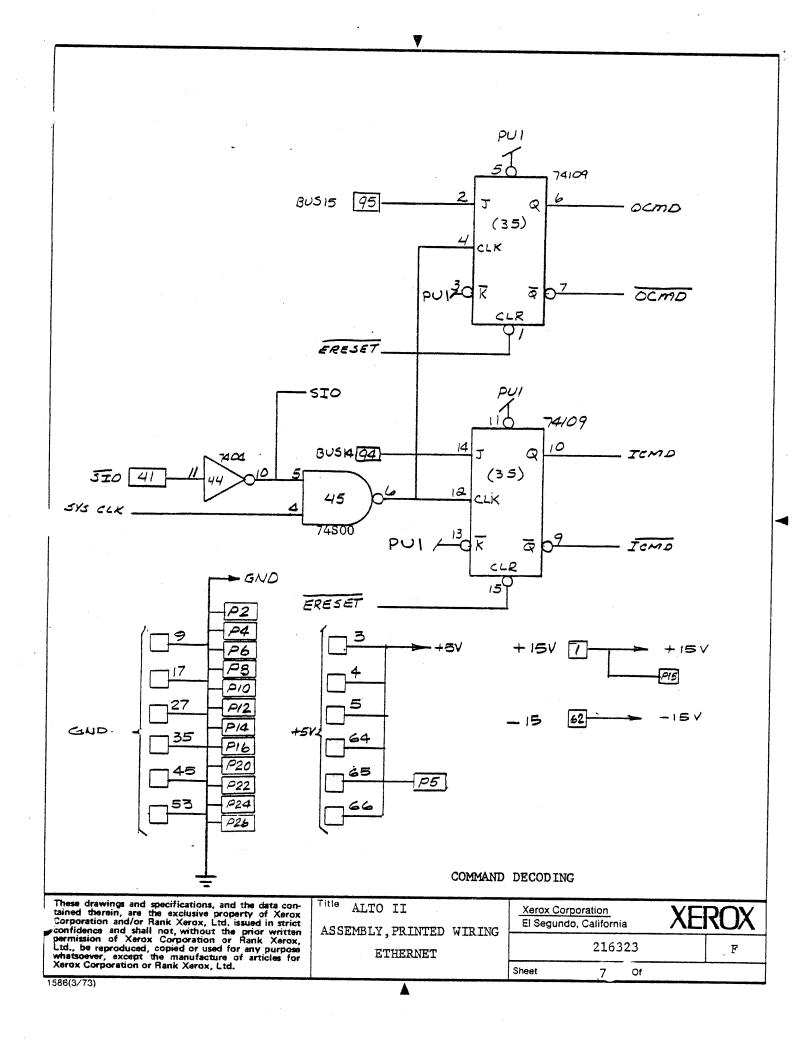


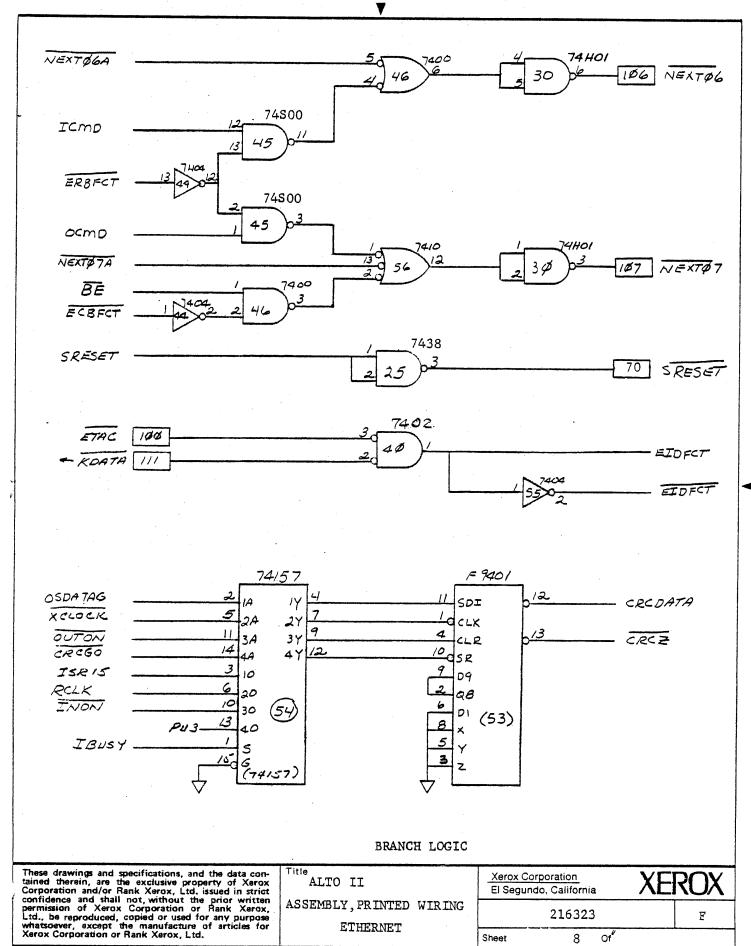
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-			7408			3	A	3,64,68		
-	6		7404			3	1	44, 55		<u> </u>
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	15		74174			1	A43			
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	24		74S00			3	A11			
	25		748157					,75,66		
	26		898-1-R1.0K Beckman			1	A76			
	27		I3205 Intel			3		32,38		
	28		I3101A Intel			3		19,29		
	29		20001 11	72		4		18,22,28		
	30		P3601 M.I.L. OR TNZ 9401 Fairchild	+/3		3		42,49		
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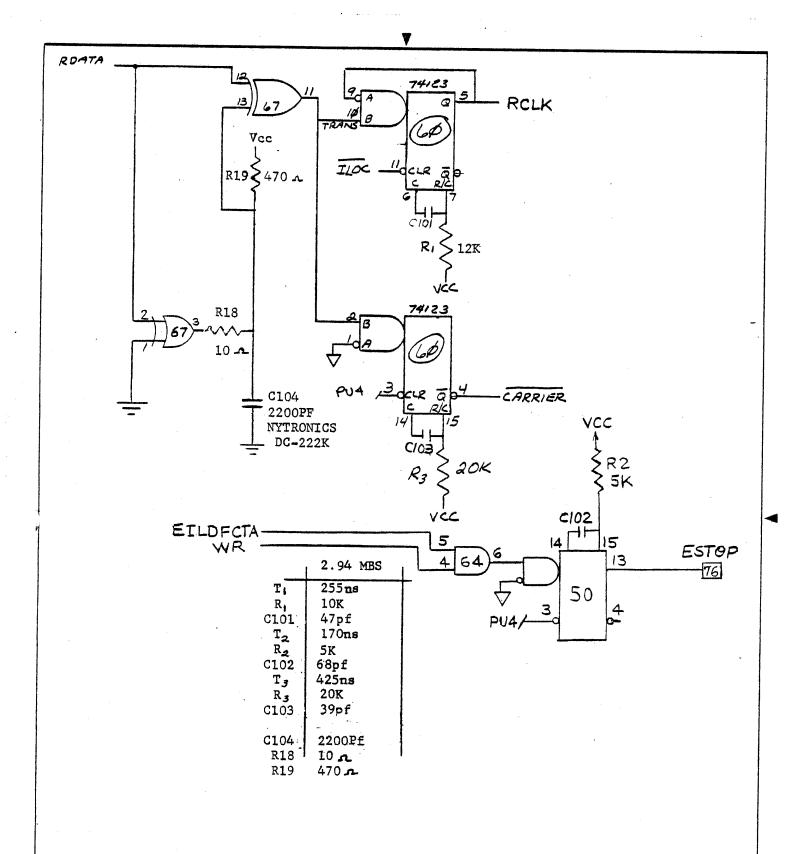
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-	37	Capaci	tor, 2200 pF Nytroni	cs DC-				1	C1			
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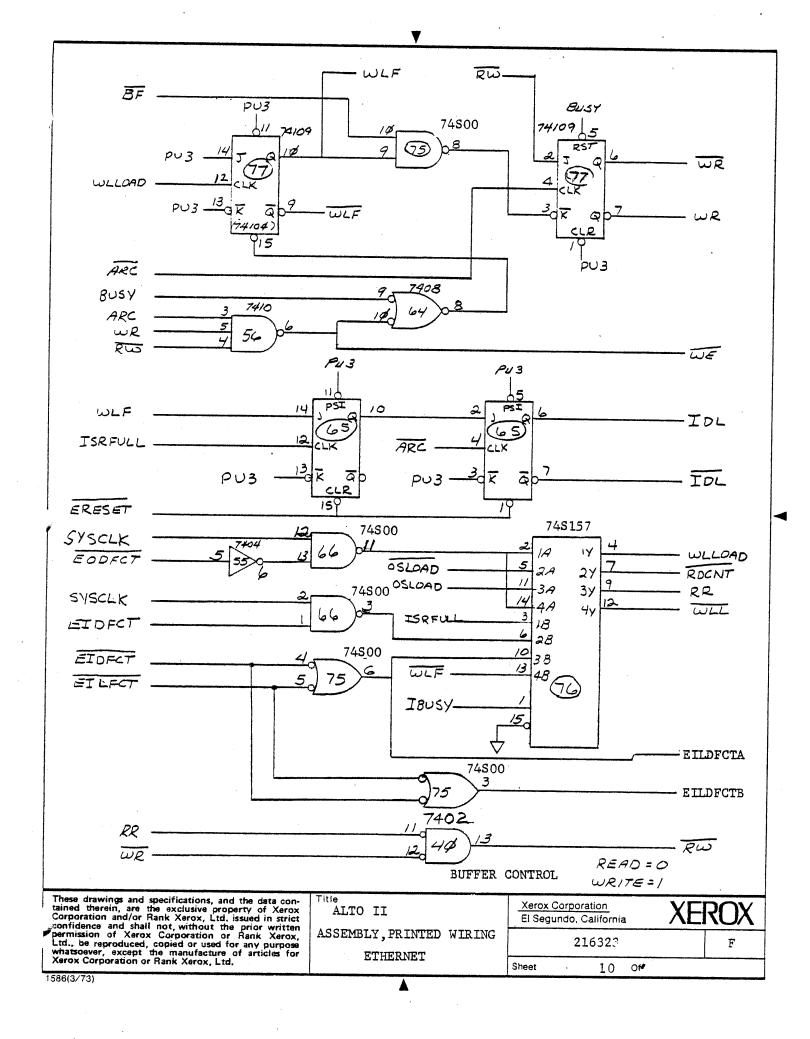
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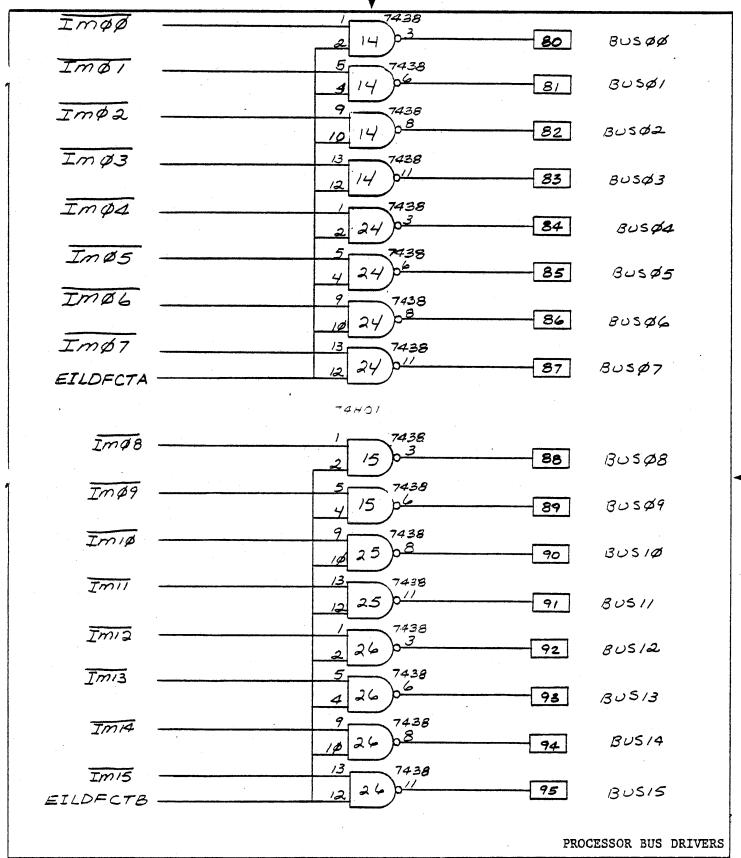
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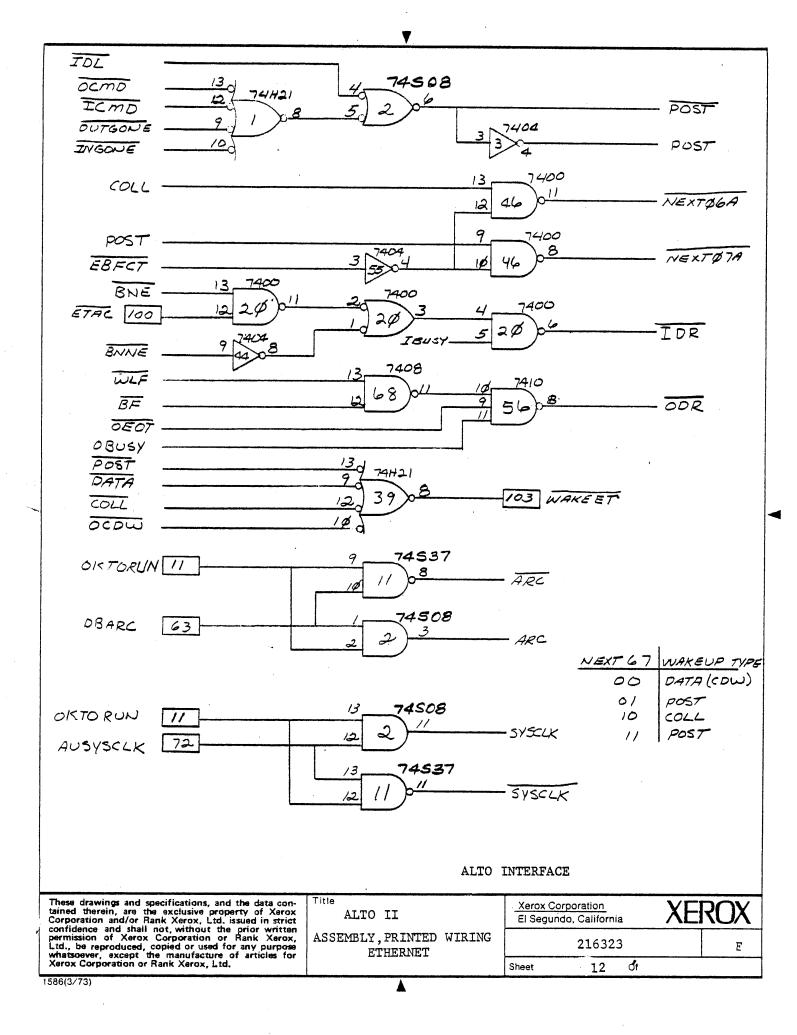


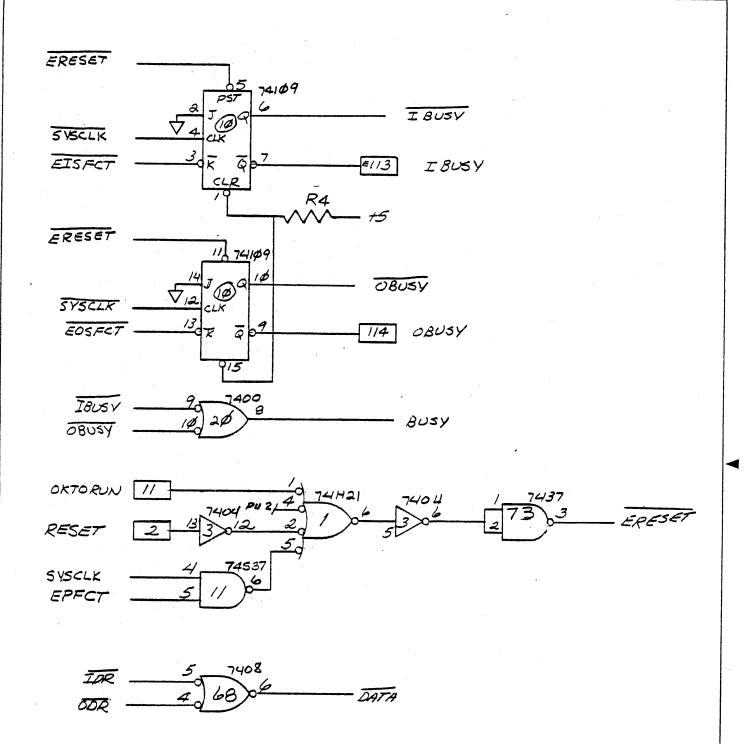
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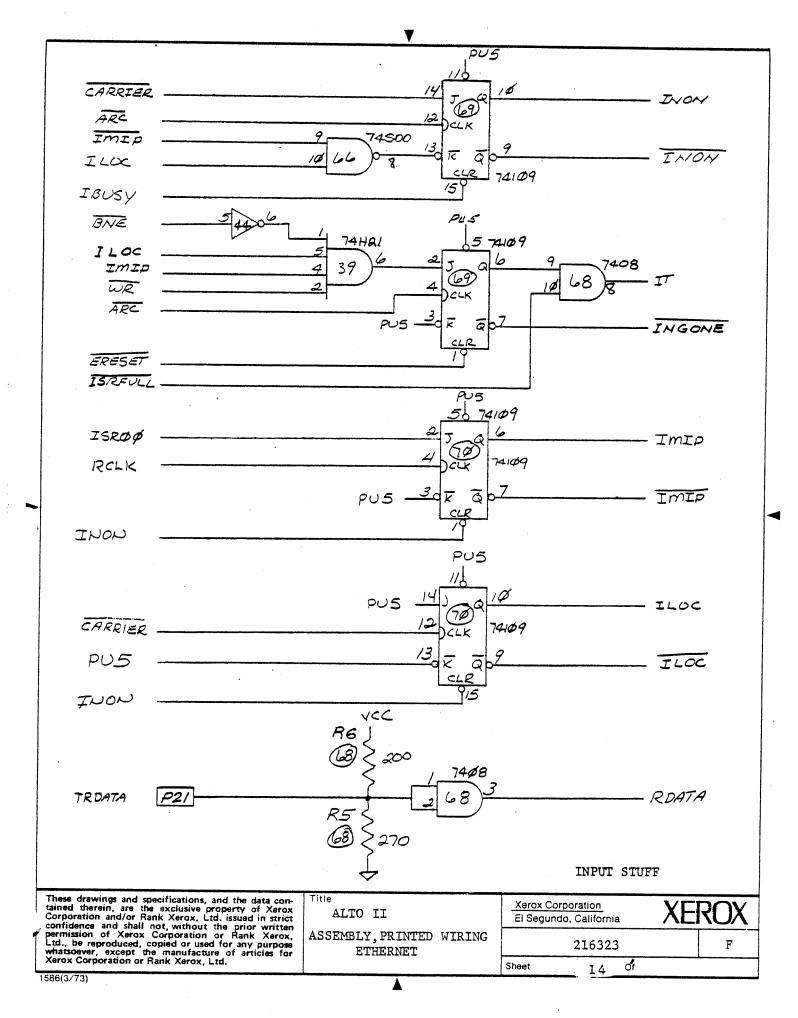
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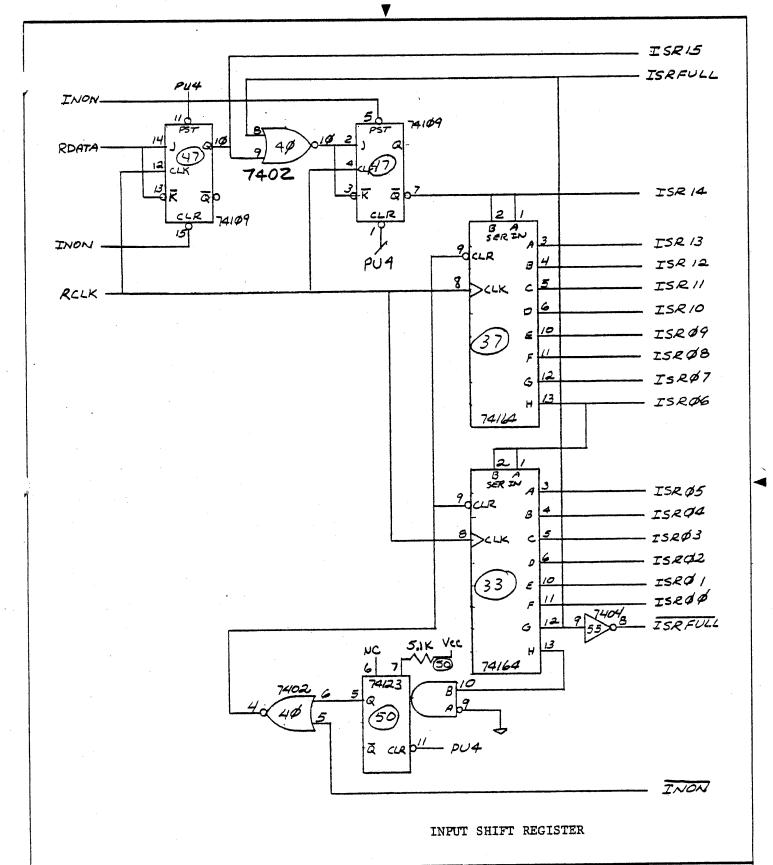
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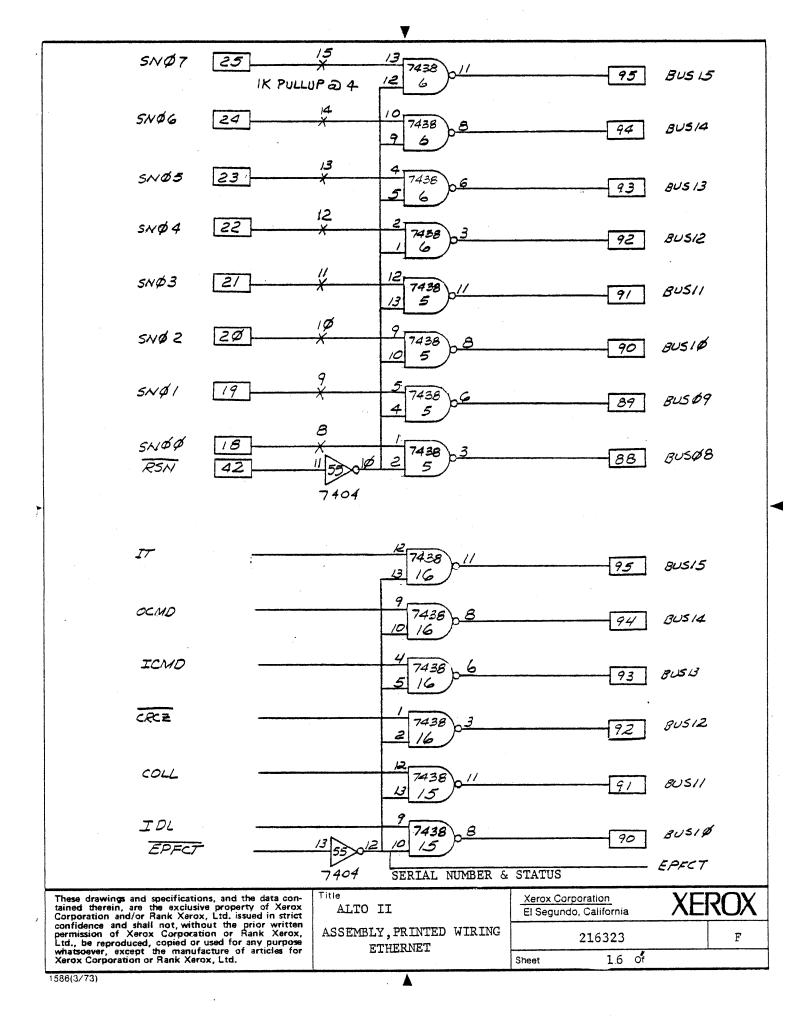
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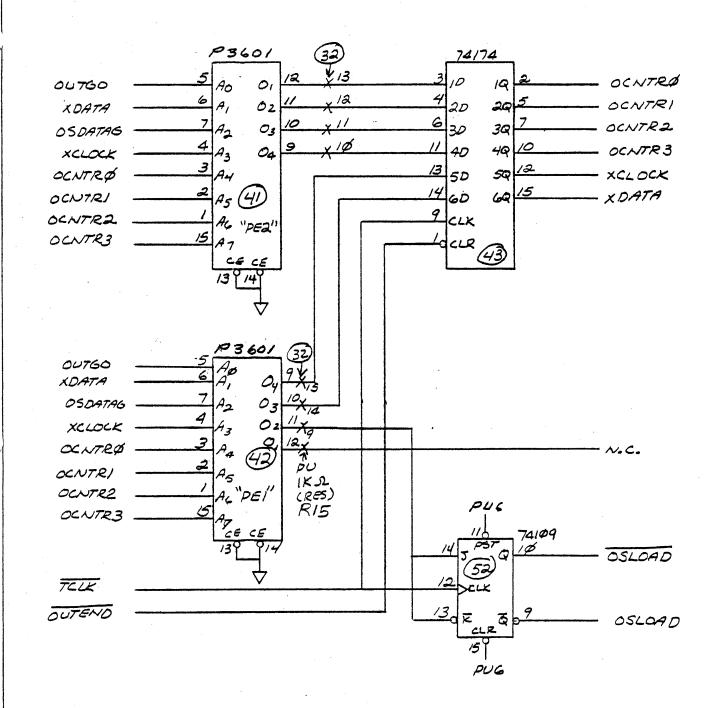




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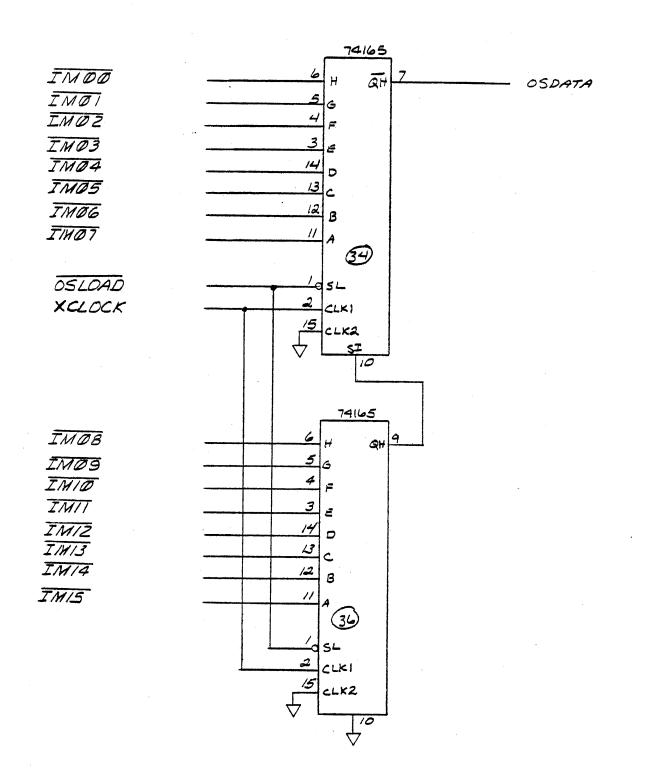




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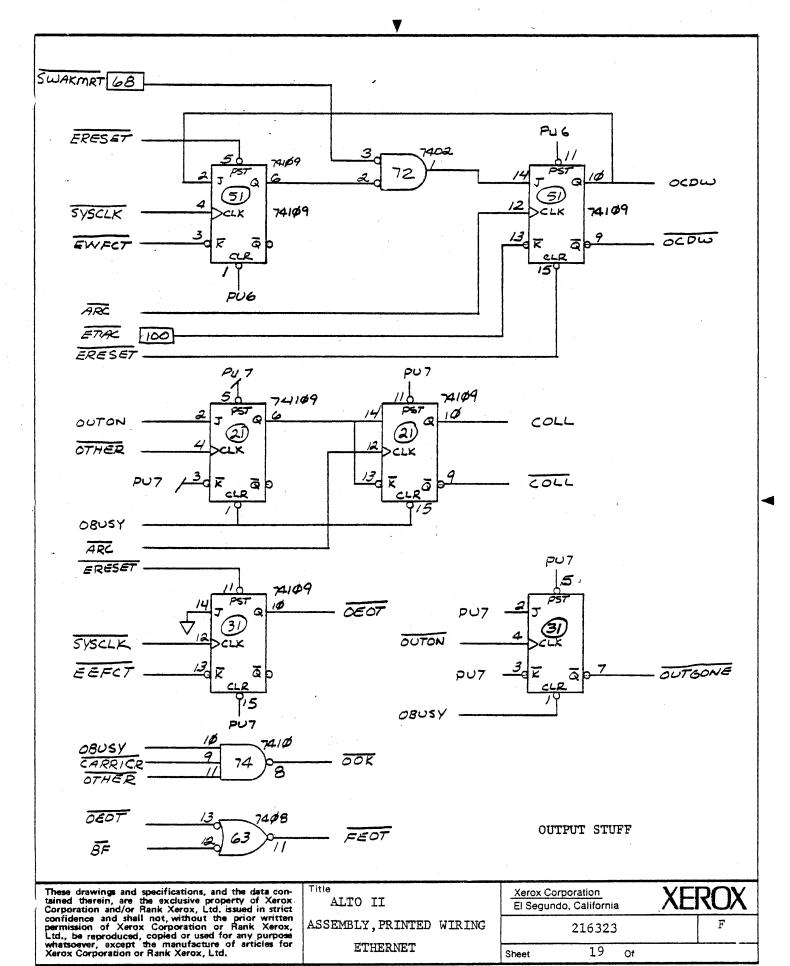
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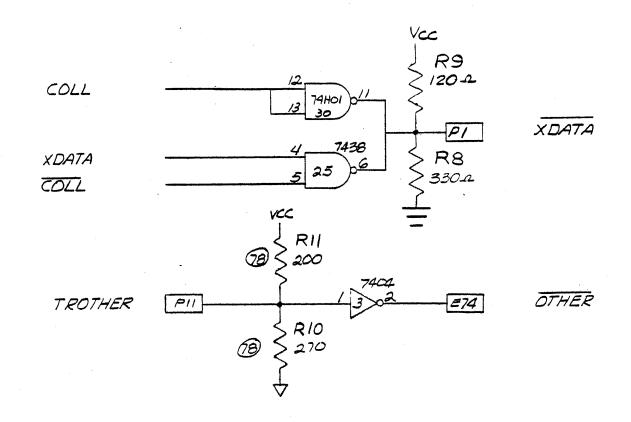
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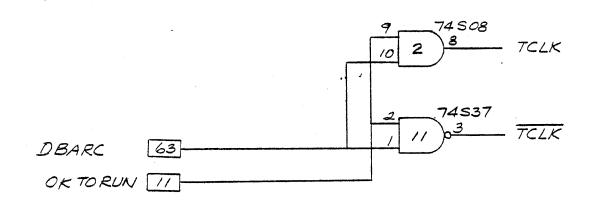
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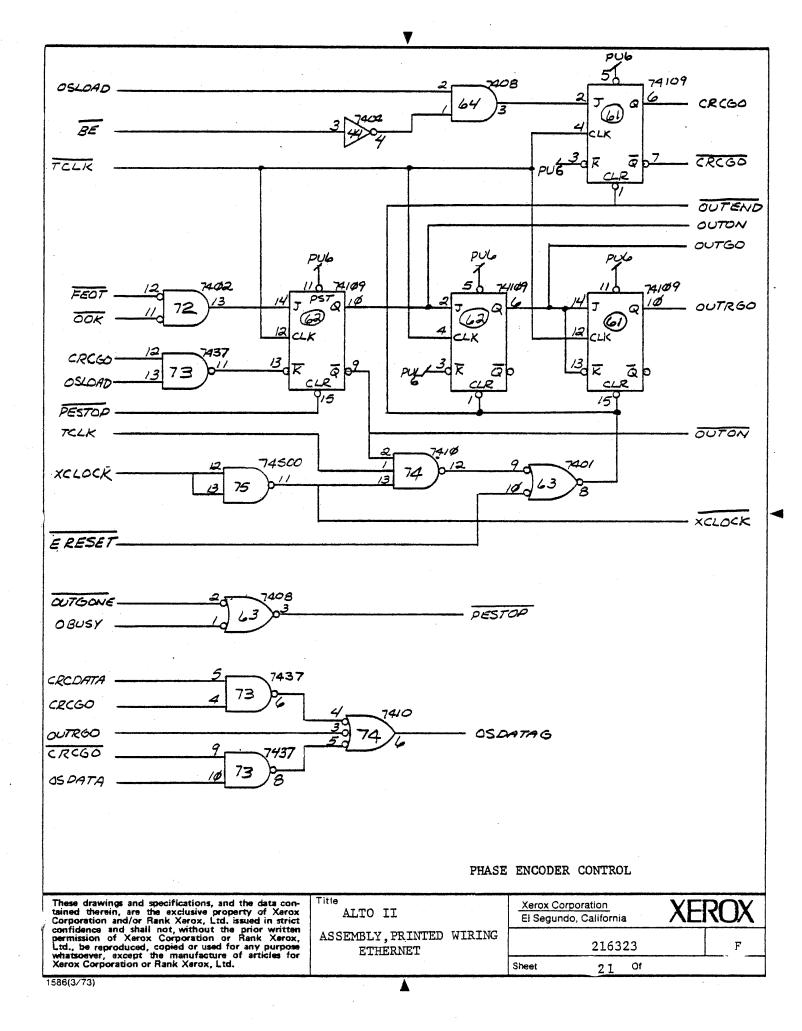
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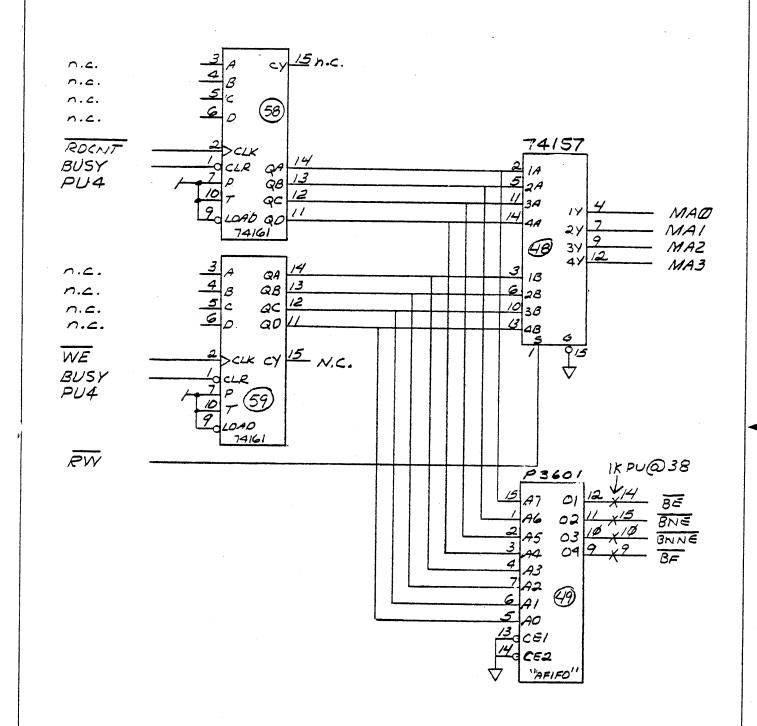
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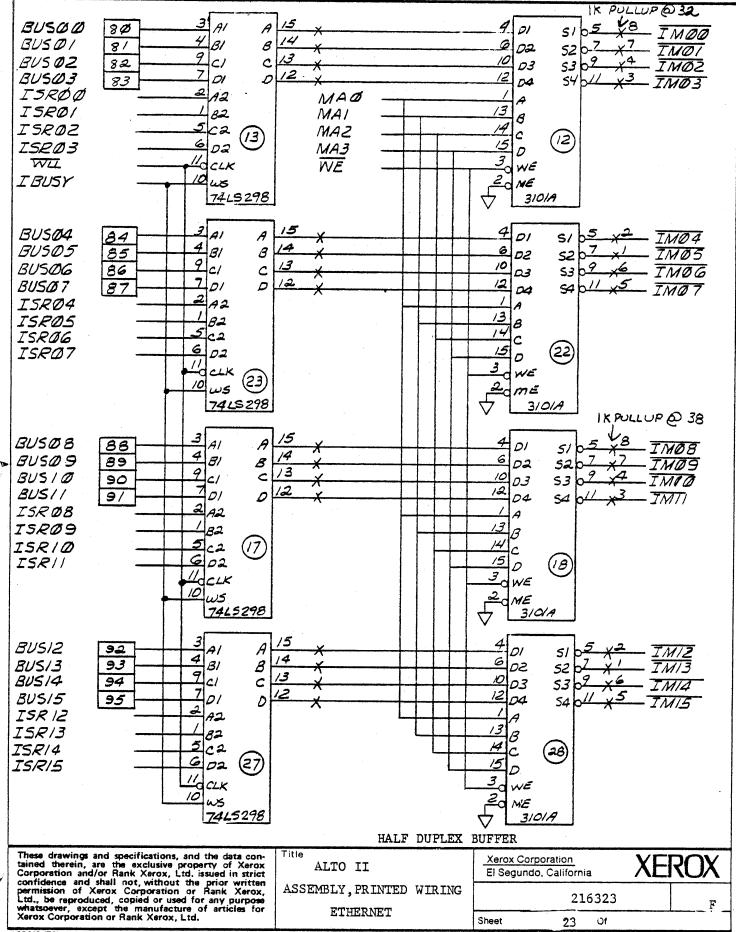
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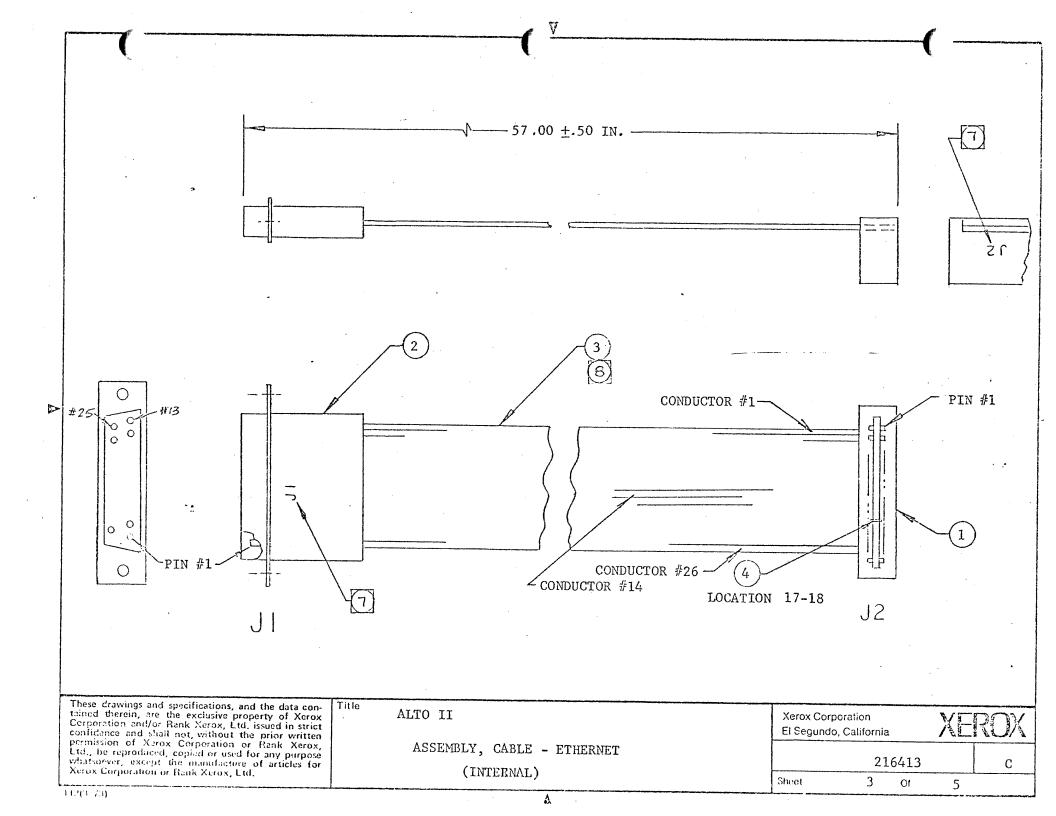
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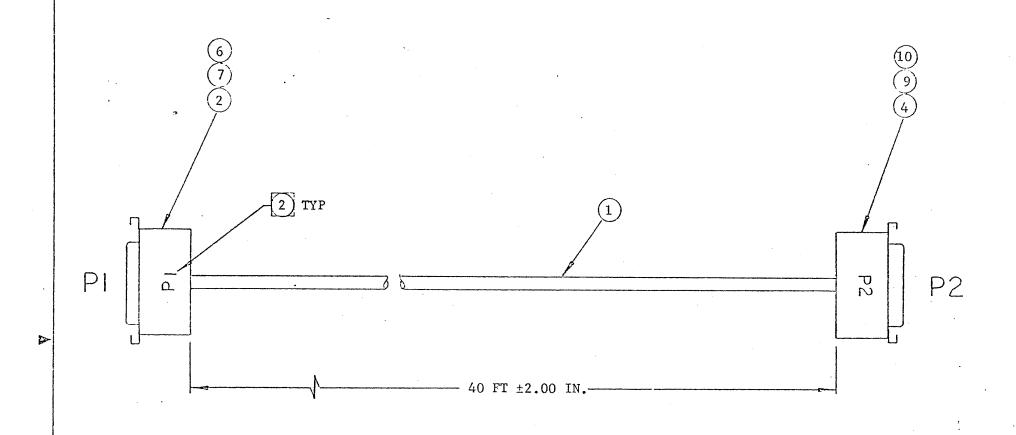




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ļ	Wire N	Term	From	То	Term	Wire Type	Notes	Signal	Chg. Let.
-	11		J1 - 13	J2 - 1		3			
	2		25	2					
	3		12	3					
	4		24	4					
	5 .		11	5					
	6	7	23	6					
	7		10	7					
	8		22	8					
	9		9	9					
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	12		20	12					
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	24		2	24					
	25		14	25					
	26		J1 - 1	J2 - 26		3			
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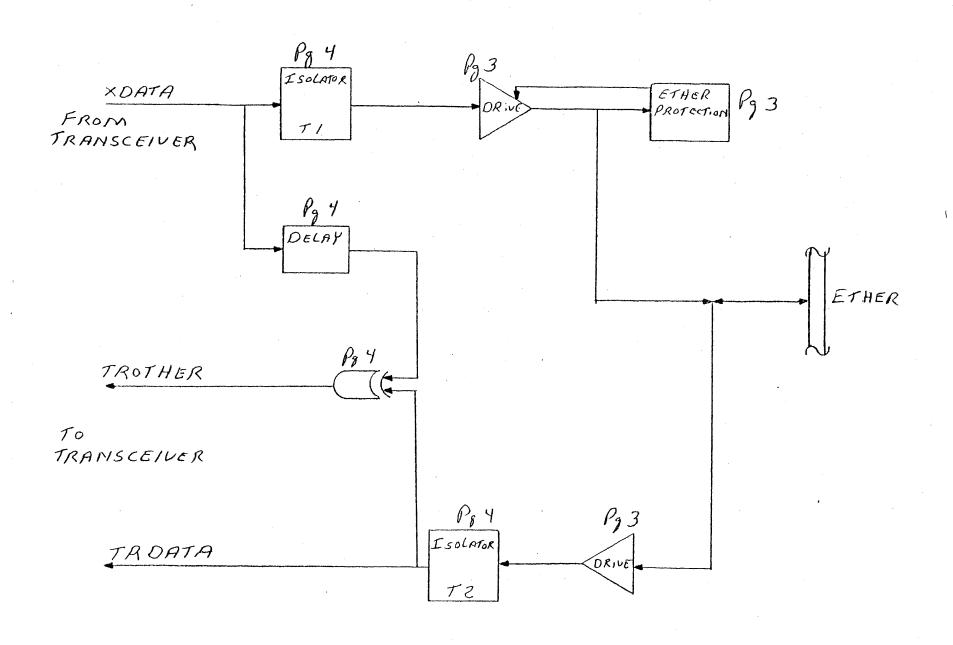
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ASSEMBLY, CABLE - ETHERNET (EXTERNAL)

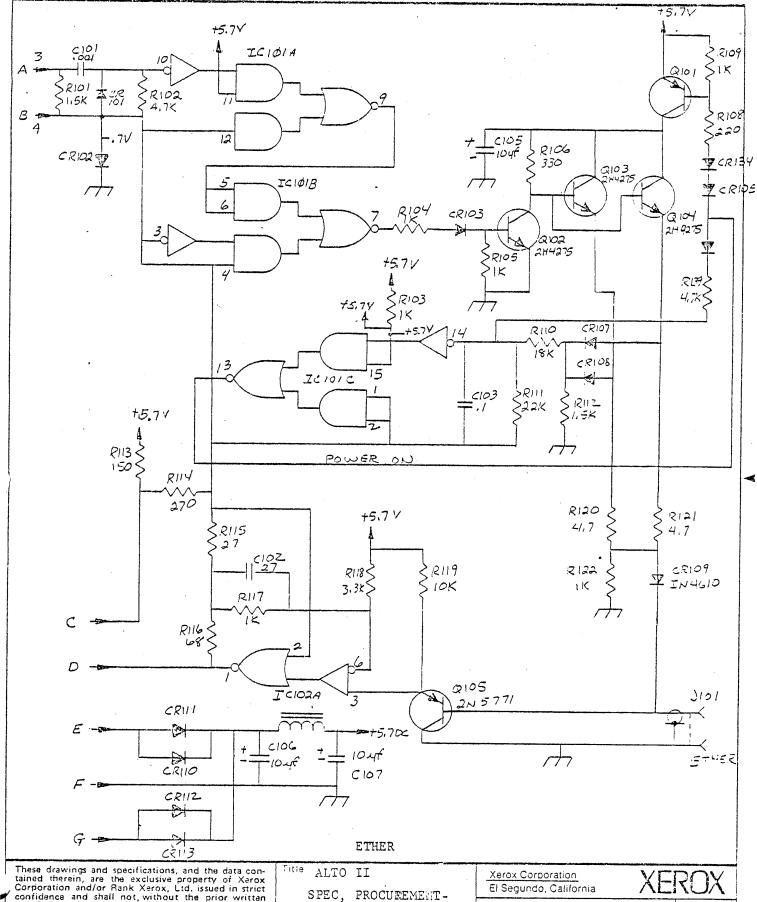
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Wire No. 1 2 3 4	Term	P1 - 12 24 13	P2 - 9	Term	Wire Type	<u>-</u>	Notes	Signal		Chg. Let
3 4		24			1	·		}		l .
3 4			1			BRN)	TWISTED	SPARE		
4		13	- 			BLK S	PAIR	SPARE GND		
			3			GRN)		XDATA		
- 1		25	4			BLK \		GŅD	·	
5	3	16	5			WHT		TRDATA		
6		3	6			BLK J		TRDATA GND		
7		8	7			BLU)		TROTHER		
8		20	15			BLK J		TROTHER GND		
9		11	10			RED		+5V		
10		23	11			BLK S		+5V GND		
11		19	13			YEL)	TWISTED PAIR	+15V		
12		P1 - 6	P2 - 14		1	BLK J	111111	+15V GND		
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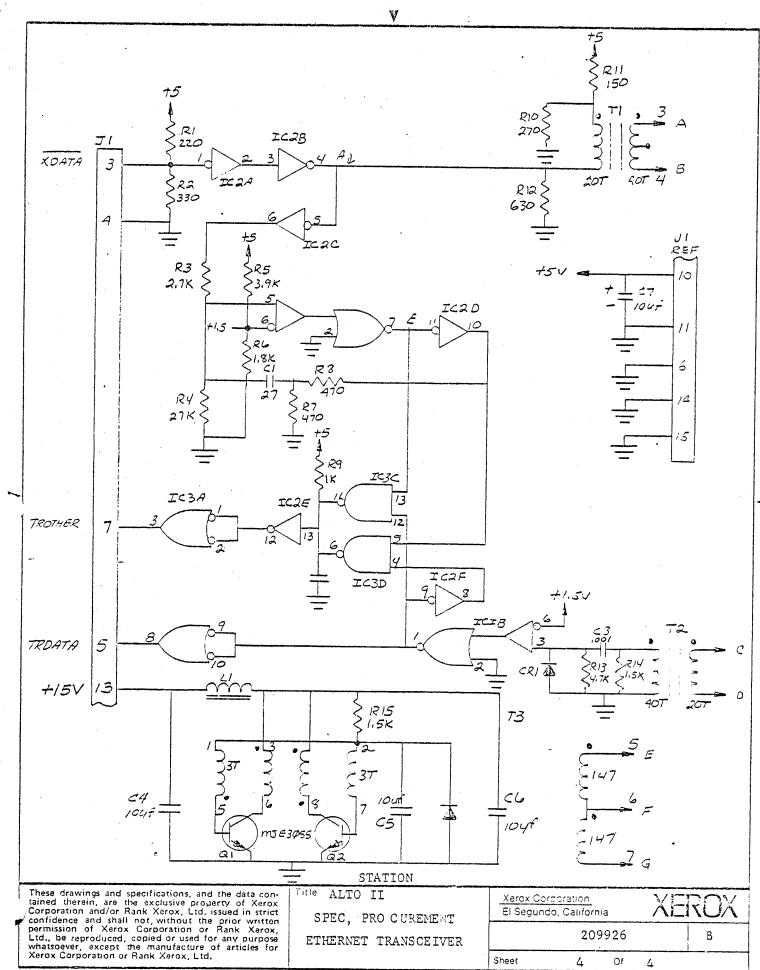


ETHERNET TRANSCEIVER

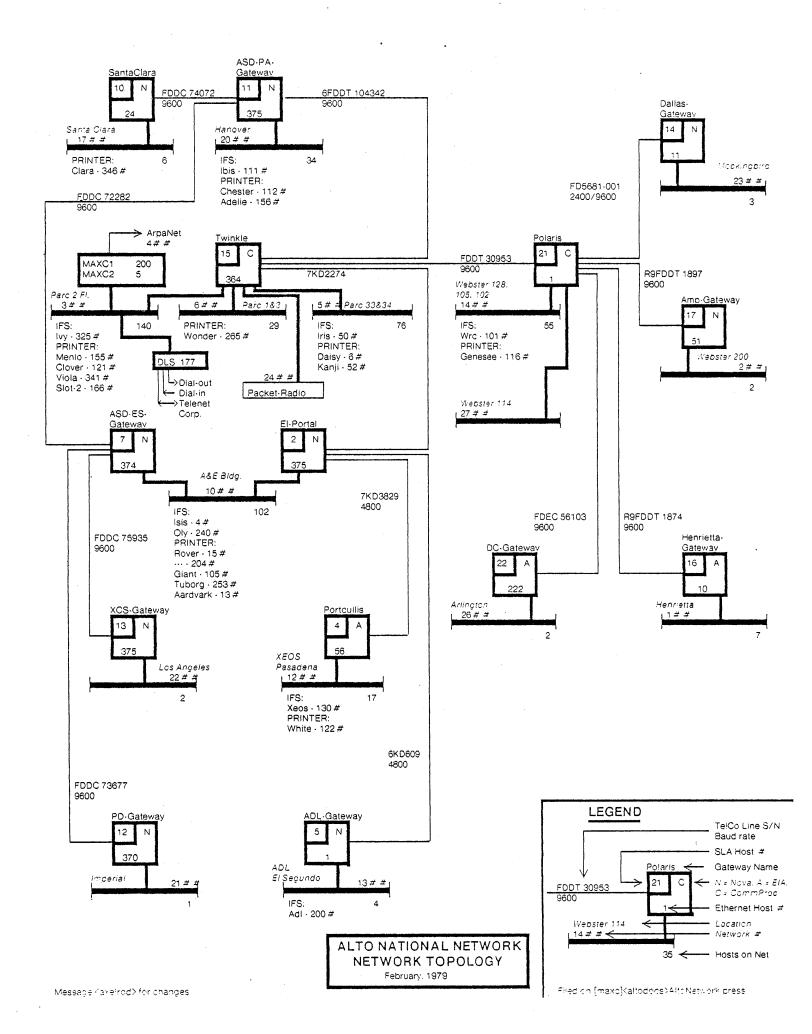
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ETHERNET TRANSCEIVER INSTALLATION Xerox P/N 885230/209926



Parts Needed in Addition to Transceiver

Jerrold PTC Tap Block P/N 1491A

Jerrold Cable Drill P/N CD-11

Replacement Tip P/N CT-2

Round head, square neck, 5/16-18 x 1" carriage bolt

RG-11 type Foars Coax

Installation Instructions

Remove the bolt and messenger cable hanger (held together by a splined pressure fit), and insert the carriage bolt in its place. (This step may already be done.)

Clamp the tap block to the cable with the threaded opening pointing in the direction in which the transceiver will be oriented. Attempt to space taps at least 6 feet apart. Clumps of closely spaced taps will reduce the maximum cable length and number of taps permissible.

Screw the drill into the hole until the threads bottom out. Back the drill out and clean out any remaining braid. Bits of braid left in the hole can short out the Ether.

Screw the Ethernet Transceiver into the hole. Be gentle! The stinger is brass and the tap block is steel. The stinger will lose a cross threading contest.

Do not tighten the transceiver by
twisting on the box. Use a 7/16"
wrench on the hex faces of the stinger.

. Before applying power, check the voltages with a dummy transceiver or a voltmeter:

+15v +|- 2% between pins 15(+) and 14(-) +5v +|- 5% between pins 10(+) and 11(-)

Connect the interface cable to transceiver. If possible strain relief the interface cable by tying it to a nearby object such as a pipe.

When power is applied to the transceiver, you should see a red light through the transparent window in the side of the box. If the light is out the transceiver has detected an internal failure. Removing power momentarily resets the failure detection circuit. If the light does not come on, the transceiver is defective and should be removed.

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DISK AND CONTROLLER

The disk controller is designed to accommodate one of a variety of DIABLO disk drives, including models 31 and 44. Each drive accommodates one or two disks. Each disk has two heads, one per side. Information is recorded on each disk in a 12-sector format on each of up to 406 (depending on the disk model) radial track positions. Thus, each disk contains up to 9744 recording positions (2 heads X 12 sectors X 406 track positions) Figure 1 tabulates various useful information about the performance of the disk drives.

DEVICE	DIABLO 31	DIABLO 44	
Number of drives/Alto Number of packs	1 or 2 1 removable	1 1 removable 1 fixed	
Number of cylinders Tracks/cylinder/pack Sectors per track Words per sector	203 2 12 2 header 8 label 256 data	406 2 12 2 header 8 label 256 data	
Data words/track Sectors/pack	3072 4872	3072 9744	
Rotation time Seek time (approx.) min-avg-max Average access to 1 megabyte	40 15 + 8.6*sqrt(dt) 15-70-135 80	25 8 + 3*sqrt(dt) 8-30-68 32 (using both packs)	ms ms* ms
Transfer rate: peak/avg peak/avg per sector for full display for 64K memory whole drive	1.6/1.22 10.2/13 3.3 .460 1.03 19.3	2.5/1.9 6.7/8 2.1 .266 .6 44 (both packs)	MHz us/word ms sec sec sec

*The notation dt stands for the number of tracks traveled during the seek.

FIGURE 1

The disk controller records three independent data blocks in each sector. The first is two words long, and is intended to include the address of the sector. This block is called the Header block. The second block is eight words long, and is called the Label block. The third block is 256 words long, and is the Data block. Each block may be independently read, written, or checked, except that writing, once begun, must continue until the end of the sector.

When a block is checked, information on the disk is compared word for word with a specified block of main memory. During checking, a main memory word containing 0 has special significance. When this word is encountered, the matching word read from the disk is stored in its place and does not take part in the check. This feature permits a combination of reading and checking to occur in the same block. (It also has the drawback of making it impossible to use the disk controller to check for words containing 0 on the disk).

The Alto program communicates with the disk controller via a four-word block of main memory. The first word is interpreted as a pointer to a chain of disk command blocks. If it contains 0, the disk controller will remain idle. Otherwise, the disk controller will commence execution of the command contained in the first disk command block. a command is completed successfully, the disk controller stores a pointer to the next command in the chain and the cycle repeats. If a command terminates in error, a 0 is immediately stored and the disk controller idles. At the beginning of each sector, status information, including the number of the current sector, is stored. be used by the Alto program to sense the readiness of the disk and to schedule disk transfers, for example. When the disk controller begins executing a command, it stores the disk address of that command. This information is later used by the disk controller to decide whether seek operations or disk switches are necessary. It can be used by the Alto program for scheduling disk arm motion. If the Alto program stores an illegal disk address (like -1) in this word, the disk controller will perform a seek at the beginning of the next disk operation. (This is useful, for example, when a disk driver wants to force a restore operation). The disk controller also communicates with the Alto program by interrupts.

A disk command block is a ten-word block of memory which describes a disk transfer operation to the disk controller, and which is also used by the controller to record the status of that operation. The first word is a pointer to the next disk command block in this chain. A 0 means that this is the last disk command block in the chain. When the command is complete, the disk controller stores its status in the second word. The third word contains the command itself, telling the disk controller what to do. The fourth word contains a pointer to the block of memory from/to which the header block will be transferred. The fifth word contains a similar pointer for the label block. The sixth word contains a similar pointer for the data block.

The seventh and eighth words of the disk command block control the initiation of interrupts when the command block is finished.

The ninth word is unused by the disk controller, and may be used by the Alto program to facilitate chained disk operations. The tenth word contains the disk address at which the current operation is to take place.

DCB: Pointer to next command block.

DCB+1: Status. DCB+2: Command.

DCB+3: Header block pointer.

DCB+4: Label block pointer.

DCB+5: Data pointer.

DCB+6: Command complete no-error interrupt bit mask. DCB+7: Command complete error interrupt bit mask.

DCB+8: Currently unused.

DCB+9: Disk address.

A disk address word A contains the following fields:

FIELD	RANGE	SIGNIFICANCE
A[0-3]	0-13B	Sector number.
A[4-12]	0-625B (Model 44) 0-312B (Model 31)	Cylinder number.
A[13]	0-1	Head number.

FIELD	RANGE	SIGNIFICANCE
A[14]	0-1	Disk number (see also C[15]). O is removable pack on Model 44. 1 is optional second Model 31 drive.
A[15]	0-1	<pre>0 normally. 1 if cylinder 0 is to be addressed via a hardware "restore" operation.</pre>

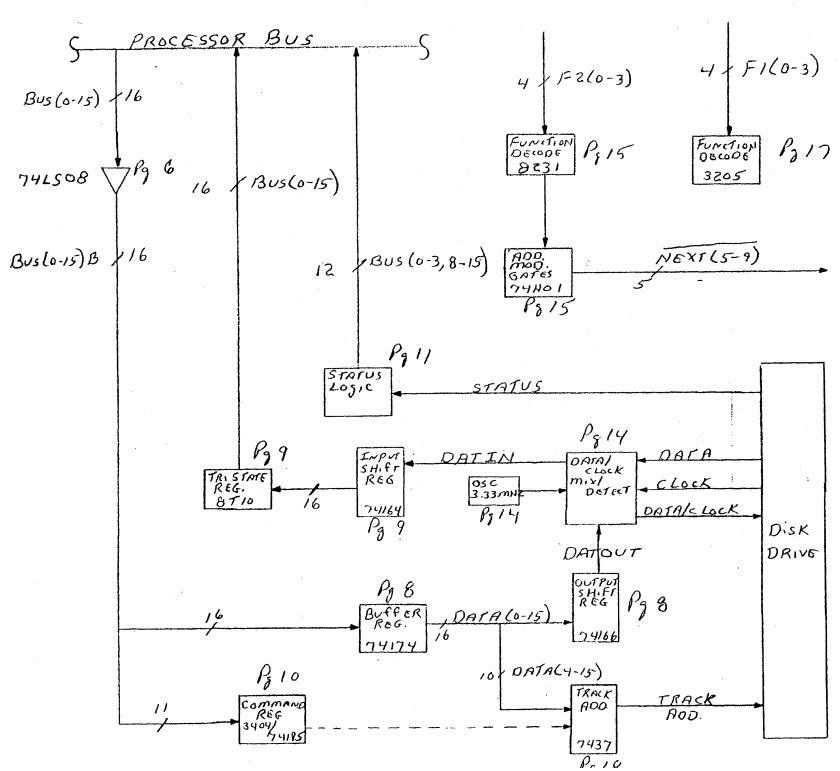
A disk command word ${\tt C}$ contains the following fields:

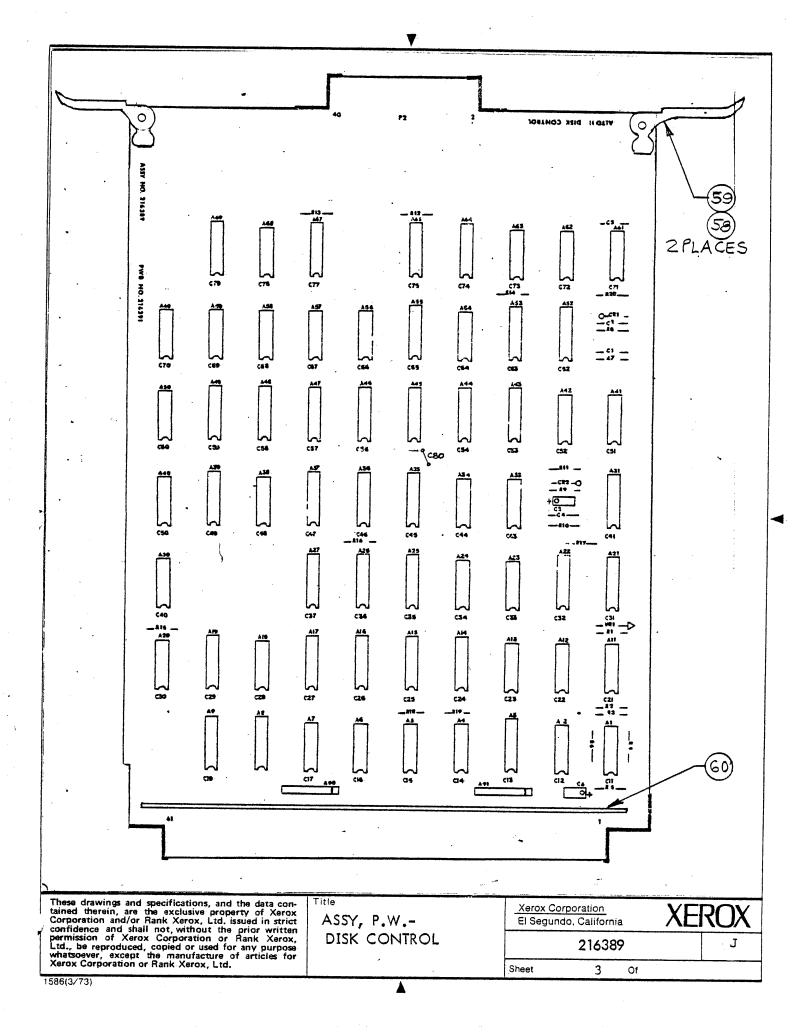
FIELD	RANGE	SIGNIFICANCE
C[0-7]	110B	Checked to verify that this is a valid disk command.
C[8-9]	0-3	0 if Header block to be read. 1 if Header block to be checked. 2 or 3 if Header block to be written.
C[10-11]	0-3	O if Label block to be read. 1 if Label block to be checked. 2 or 3 if Label block to be written.
C[12-13]	0-3	O if Data block to be read. 1 if Data block to be checked. 2 or 3 if Data block to be written.
C[14]	0-1	O normally. 1 if the command is to terminate immediately after the correct cylinder position is reached (before any data is transferred).
C[15]	0-1	XOR'ed with A[14] to yield hardware disk number.

A disk status word S has the following fields:

FIELD	VALUES	SIGNIFICANCE
S[0-3]	0-13B	Current sector number.
S[4-7]	17В	One can tell whether status has been stored by setting this field initially to 0 and then checking for non-zero.
S[8]	0-1	1 means seek failed, possibly due to illegal cylinder address.
S[9]	0-1	1 means seek in progress.
s[10]	0-1	1 means disk unit not ready.

FIELD	VALUES	SIGNIFICANCE
S[11]	0-1	l means data for sector processing was late during the last sector. Data and current sector number unreliable.
S[12]	0-1	l means disk interface was not transferring data last sector.
S[13]	0-1	1 means checksum error. Command allowed to proceed.
S[14-15]	0-3	0 means command completed correctly. 1 means hardware error (see S[8-11]) or sector overflow.
		<pre>2 means check error. Command terminated instantly. 3 means disk command specified illegal sector.</pre>



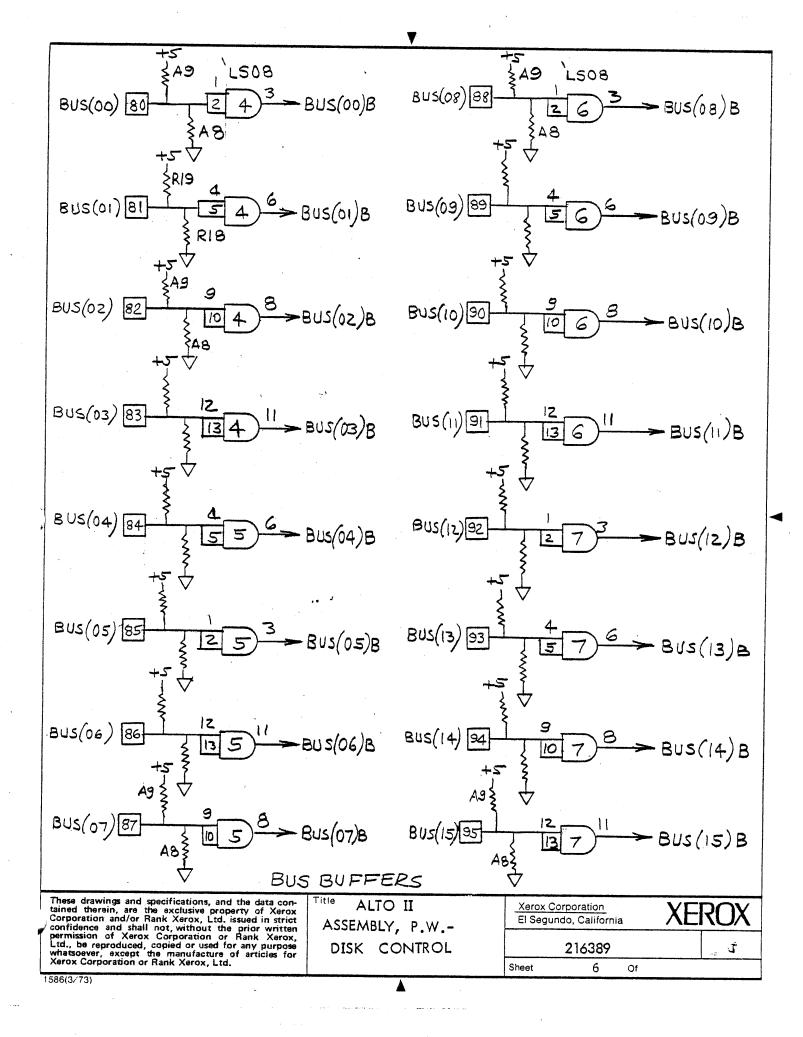


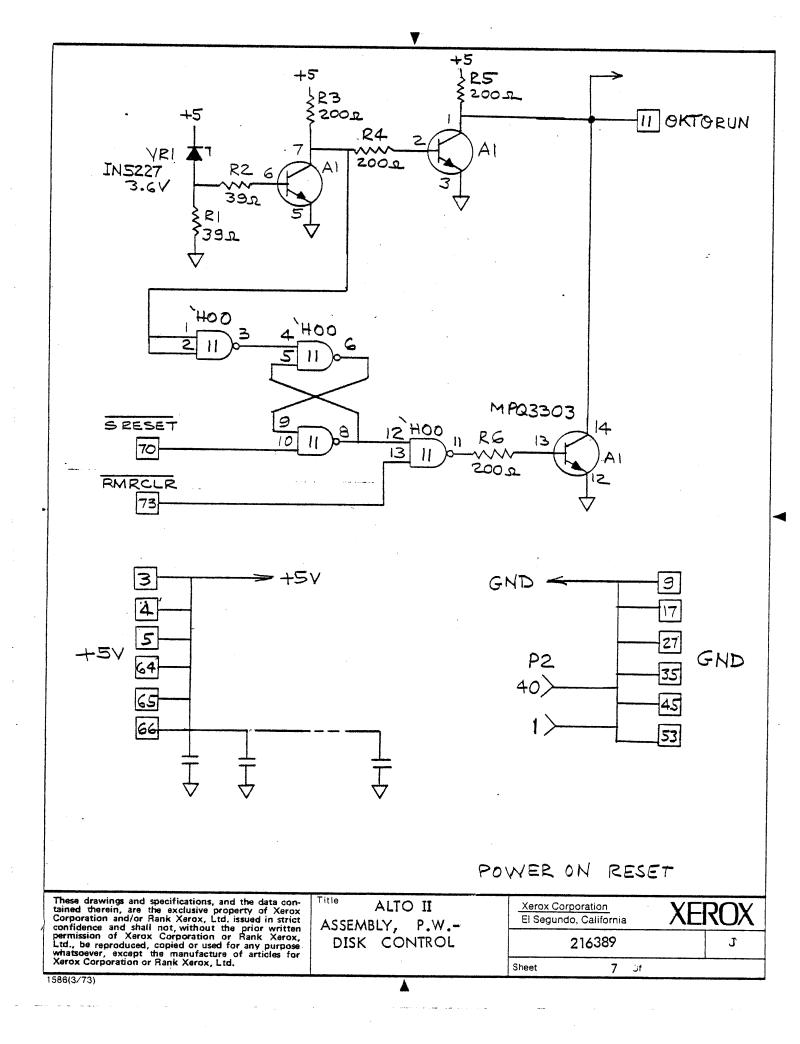


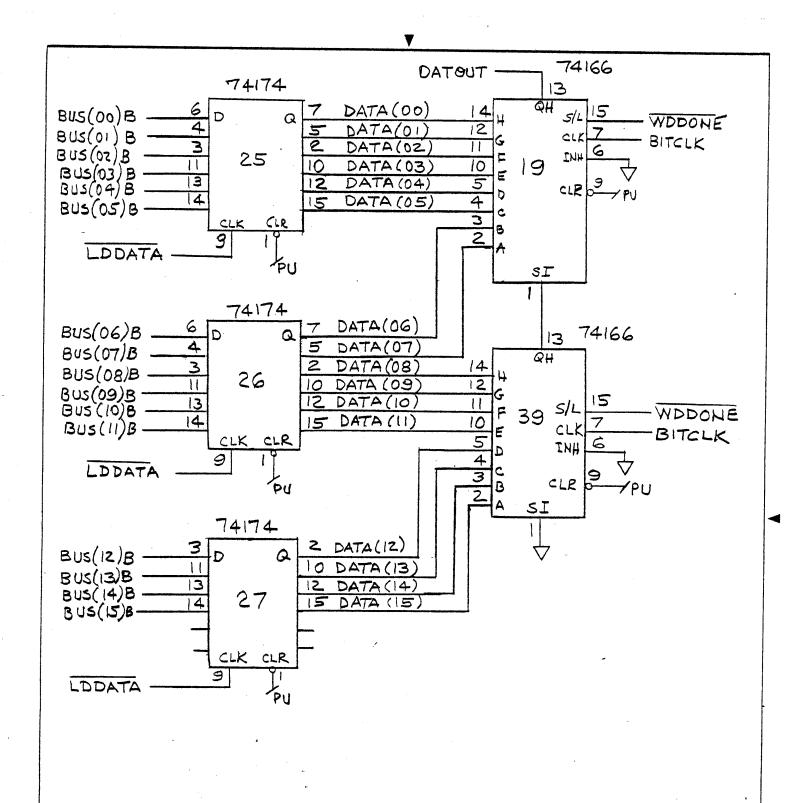
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6		DISK CONTROL	Model No ALTO II	Data	6/14/76 Sheet 4 Of
216389	Item No.	Drawing Title	Drawing No.	No. Red	
2	1	BOARD, PRINTED WIRING	216391	1	
٦ 2	2	SPEC, TEST	216390	REF	
Σ	3	SPEC, MODULE ASSEMBLY	216207	REF	
	4	MICROCIRCUIT, MPQ3303		1	A1
	5	74H04		3	A2, 56, 59
	6	3205		1	A3
	7	74LS08		4	A4, 5, 6, 7
	8	74H00		1	A11
	9	74504		1	A12
. [10	74164		2	A13, 18
	11	. 8T10		6	A14, 15, 16, 17, 46, 47
	12	74166		2	A19, 39
	13	7437		5	A20, 30, 40, 58, 68
	14	74109		8	A21, 22, 43, 44, 45, 53,
			•		55,67
	15	74532		2	A23, 57
	16	7438		2	A24, 34
	17	74174		3	A25, 26, 27
Γ	18	74123		2	A31,52
ſ	19	7400		2	A33, 63
ſ	20	I3404		1	A35
	21	74195		2	A36, 37
	22	74H08		2	A38, 42
	23	74H11		1	A41
ľ	24	8231		2	A48, 49
Ī	25	74H01		2	A50,60
	26	74502		1	A54
	27	7414		1	A61
T	28	74H 106		1	A64
	29	74153		1	A65
ľ	30	MICROCIRCUIT, 74161		1	A69
r	31	OSCILLATOR, K1100A 3.33	МН	1	A62
_	32		****		02



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Rev.		Title ITO II SSEMBLY, PRINTED WIRING- DISK CONTROL	n, are Xerox, prior w be repri the mar	the exclusive pro Ltd. issued in stric ritten permission o oduced, copied or u nufacture of articles	perty of ct confider of Xerox Consequence of Xerox	x, and the data contained there- Xerox Corporation and/or Rank ence and shall not, without the Corporation or Rank Xerox, Ltd., any purpose whatsoever, except x Corporation or Rank Xerox, Ltd.
98 No	Item No.	Drawing Title		Drawing No.	No. Req	<u> </u>
Drawing No. 216389	33	RESISTOR, NETWORK, DIP, 270Ω (AB#316A	4271)		1	A9
1 1	34	RESISTOR, NETWORK, DIP, 560Ω(AB#316.			1	A8
L N	35	RESISTOR, NETWORK, 470Ω, (CTS #750-8))	2	A90,91
	36	RESISTOR, FILM, $39\Omega \pm 5\%$, $1/4W$		116447 - 390	2	R1,R2
	37	200 Ω ±5%, 1/4W		116447-201	5	R3, 4, 5, 6, 12
ľ	38	24K ±5%, 1/4W		116447-243	2	R7,8
ľ	39	15K ±5%, 1/4W		116447-153	1	R9
	40	30K ±5%, 1/4W		116447-303	1	R10
	41	330 Ω ±5%, 1/4W		116447-331	2	R11, R20
	42	560 Ω ±5%, 1/4W		116447-561	1	R18
Ī	43	270 Ω ±5%, ₹·/4W		116447-271	1	R19
Ī	44	RESISTOR, FILM, 1K ±5%, 1/4W		116447-102	5	R13, 14, 15, 16, 17
	45				 	
7	46	DIODE, IN4148			2	CR1, CR2
Ī	47	DIODE, ZENER, IN5227			1	VR1
l	48					
Ì	49	CAPACITOR, 150PF NYTRONICS DC151			1	C1
	50	CAPACITOR, .01µF			2	C2, C4
	51	CAPACITOR, .47µF ±20%, 50V		114491-474	1	C3
	52	CAPACITOR, 270PF NYTRONICS DC 27	1		1	C5
	53	CAPACITOR, TANT., 22µF ±20%, 15V		114491-226	1	C6
ſ	54	CAPACITOR, .05µF, 10V CENTRALAB#UK	<10 - 50	3	61	C11-17,19,21-37,40,41
ľ				Andrew 1965 A. 18. Capture 1983 Art 1985 Annual Prince 1985		43-54,56-60,62-75, 77-79
Γ	55	CAPACITOR, .022 uF, 16V #DC222			1	C80
Ī	56	SOCKET, MICROCIRCUIT			31	AUGAT#514-AG10D
	57	SOCKET, MICROCIRCUIT			32	AUGAT#516-AG10D
Γ	58	RIVET		156111-005	2	
	59	EXTRACTOR		216250	2	
	60	STIFFENER		216242	1	
-						
-						
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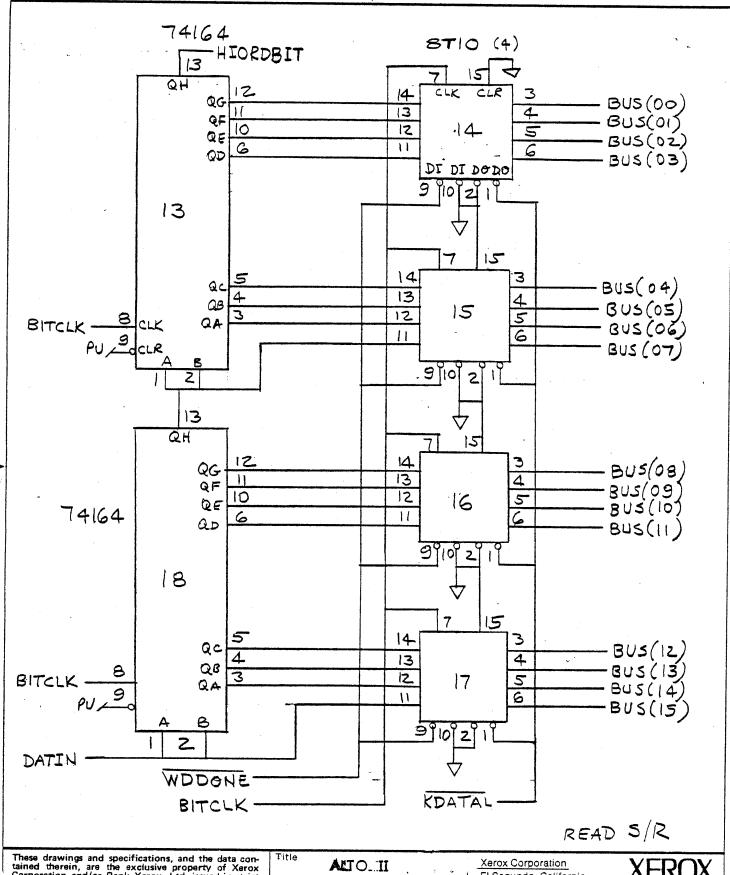
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ASSEMBLY, P.W.-DISK CONTROL

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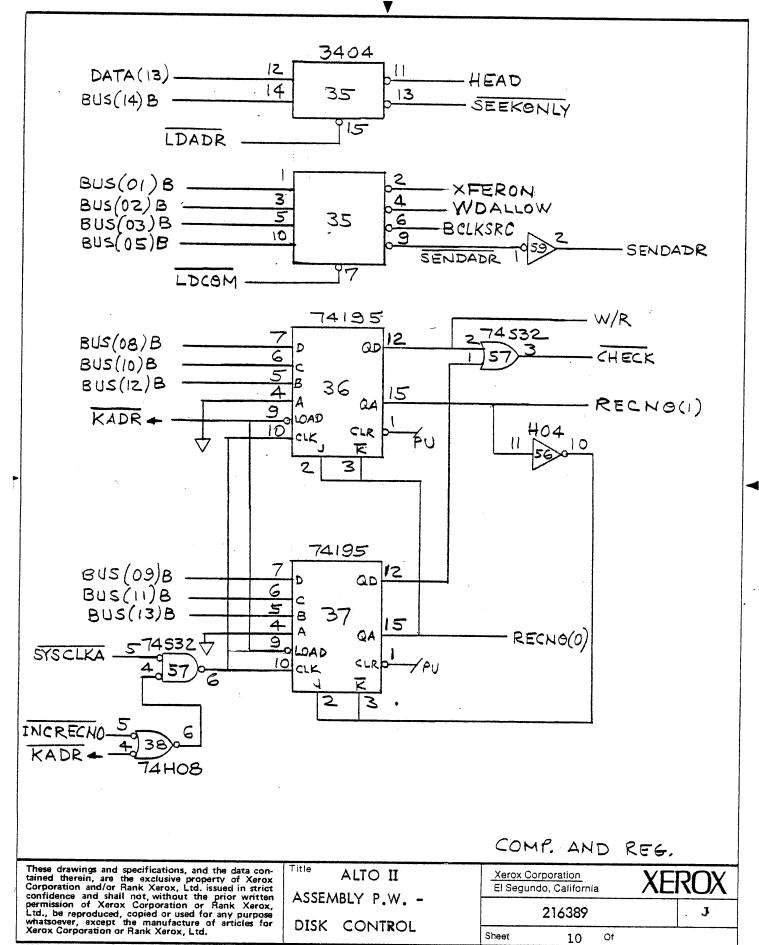
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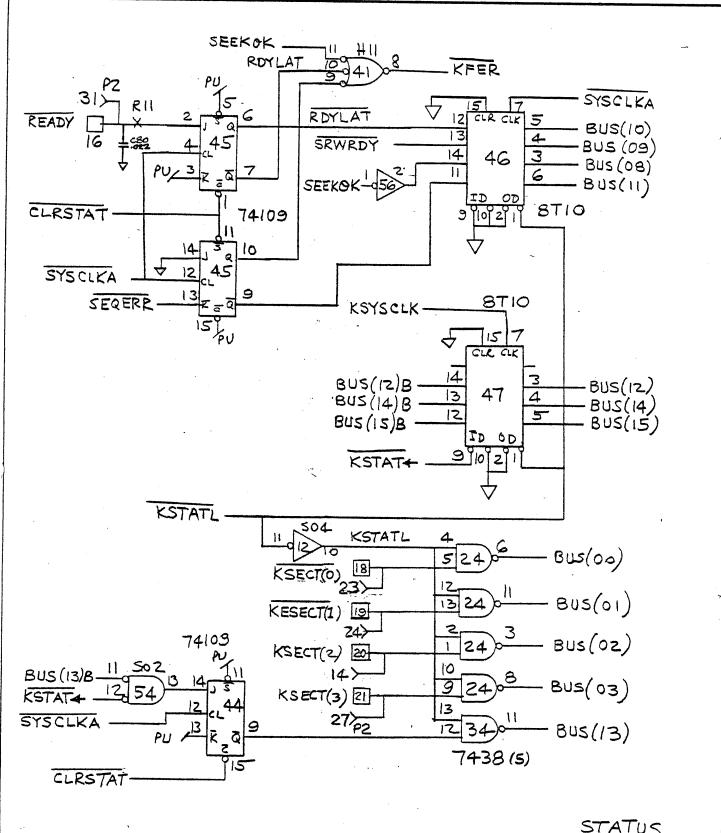
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ASSEMBLY, PRINTED W.

DISK CONTROL

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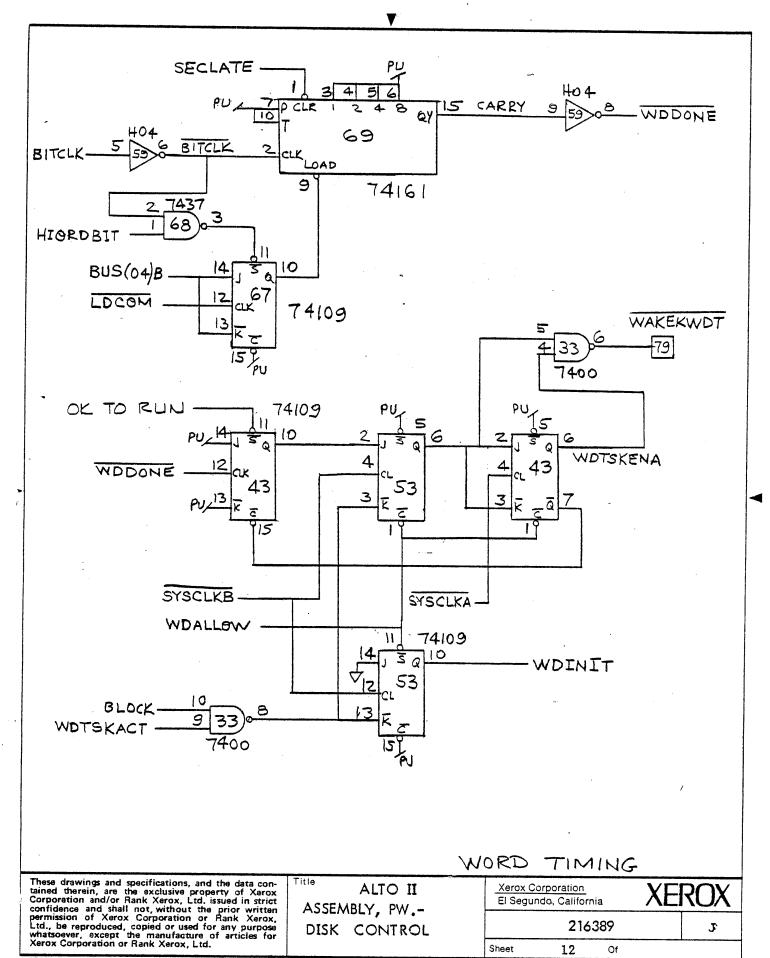


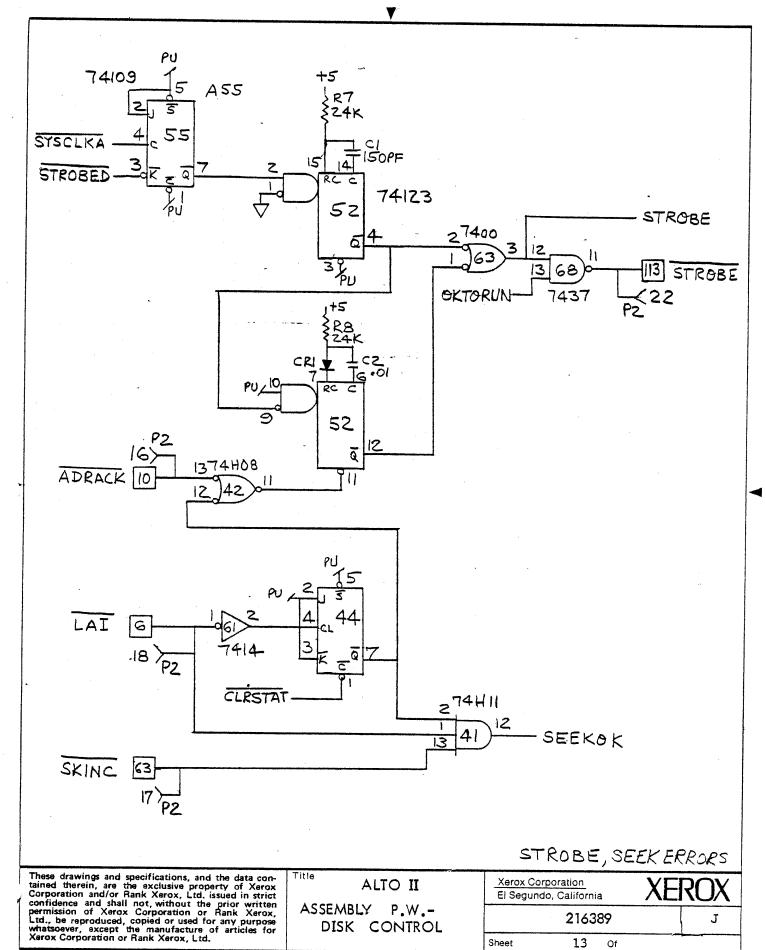
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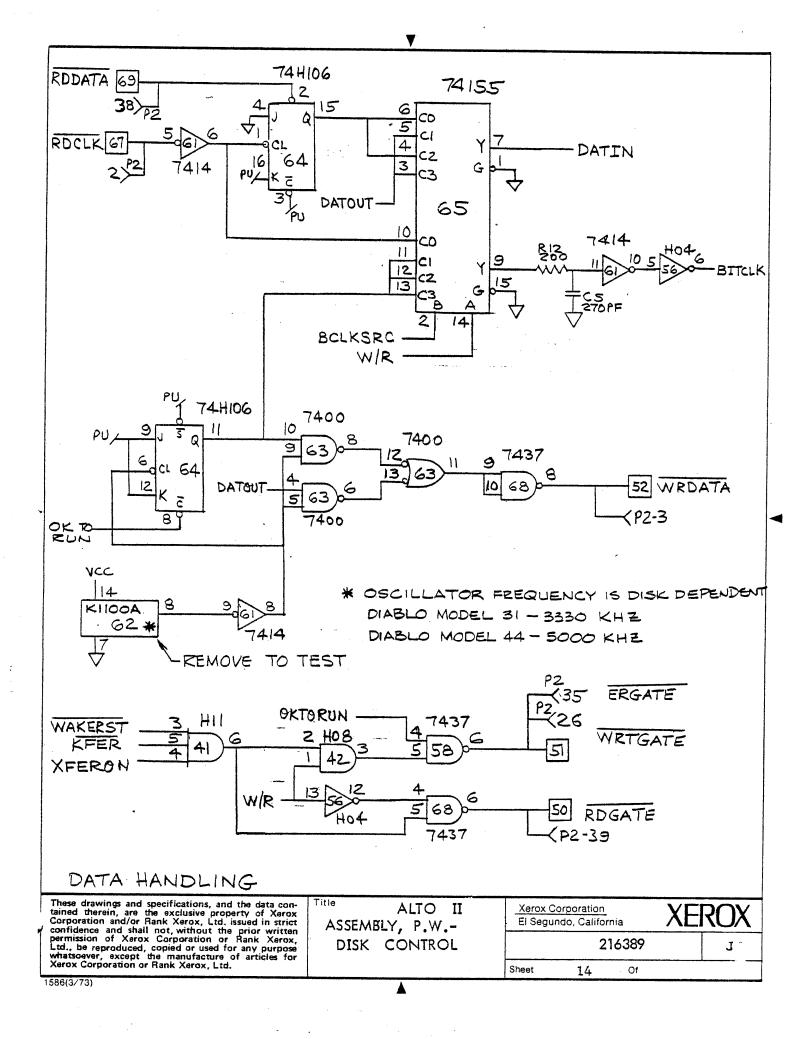
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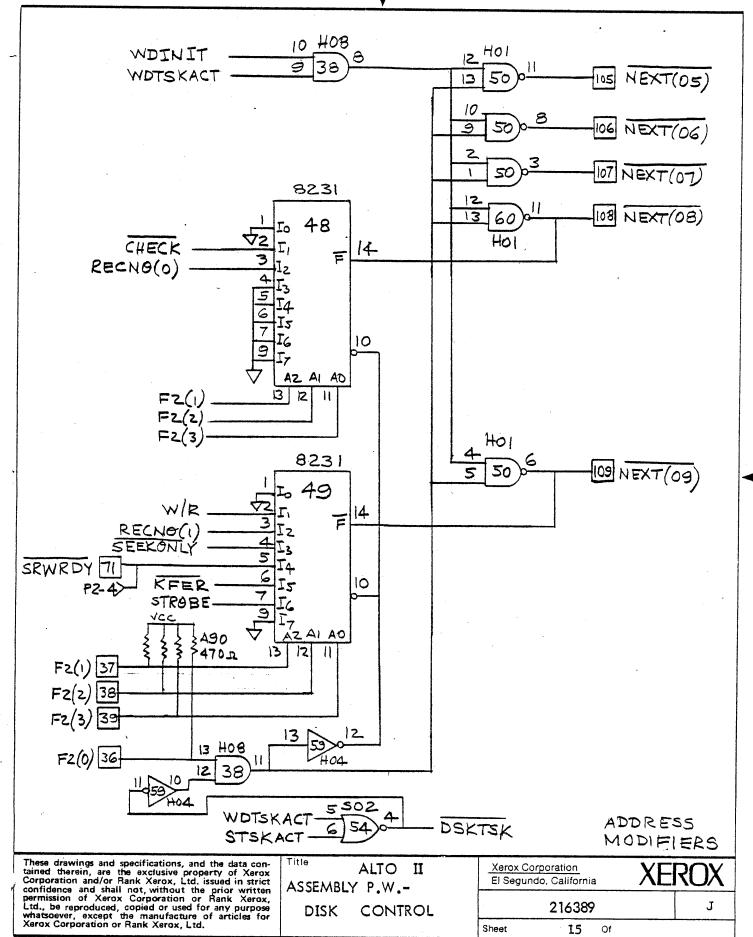
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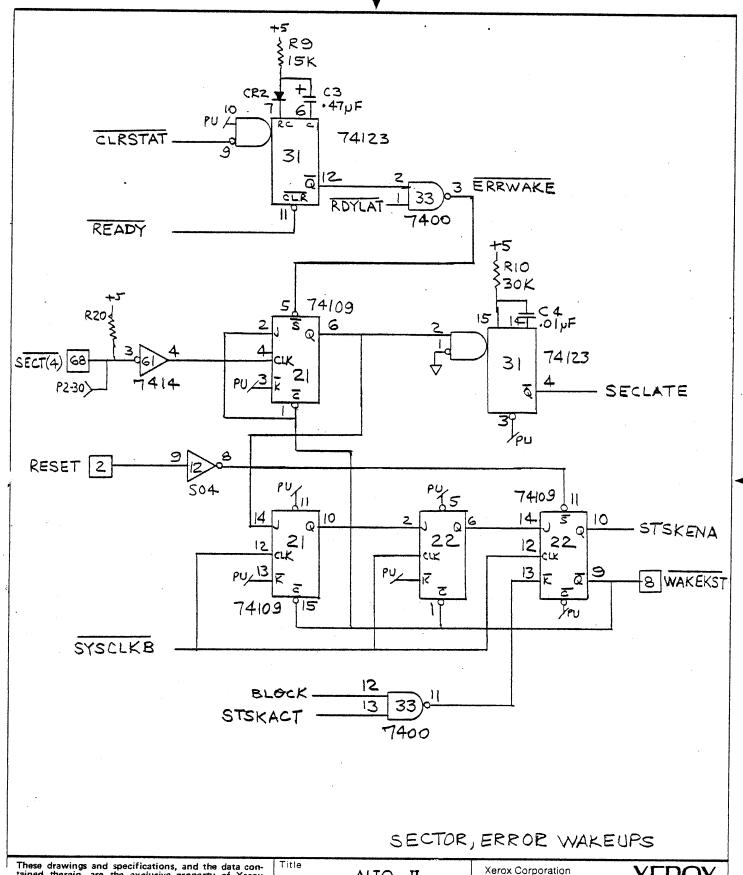
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ALTO II

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DISK CONTROL

Xerox Corporation
El Segundo, California

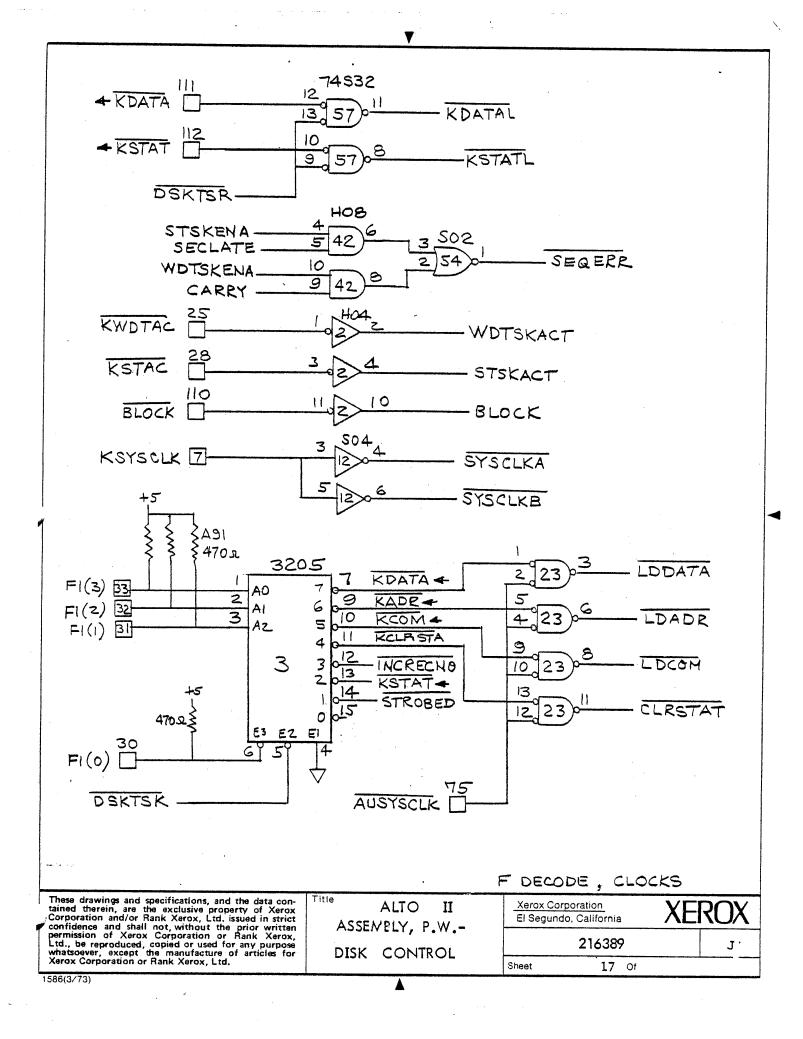
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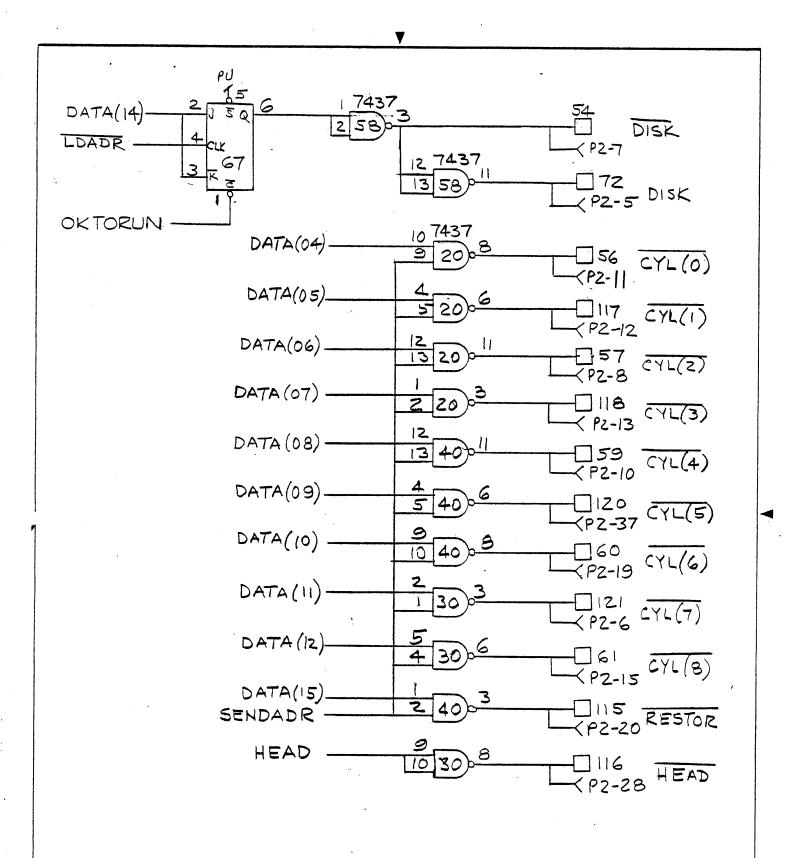
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DISK ADDRESSING

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ASSEMBLY, P.W.DISK CONTROL

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 \mathbf{A}

TRACK "O" ADJUSTMENT USING SEEK EXERCISER

- A) Load pack and bring up to ready state.
- B) Scope connections:

X axis on TPl on Jl board

Y axis on TP2 on J1 board

Time: X - Y

X axis volts .2 for 10 x probe

Y axis volts .5 for 10 x probe

C) Seek exerciser:

Address 1 and 2 ·

All "0"s

Restore - on

Start - on

Step mode - cont. (step position)

Cycle rate C/W

D) Does cont. restore - adjust for pattern to be $135^{\circ} + 40^{\circ}$

READ/WRITE HEADS ADJUSTMENT

USING SEEK EXERCISER

- A) Load C. E. pack and bring up to ready state.
- B) Scope connections:

Ch. #2 - J10 board TP2

Volts 20 m.v.

Time: 5 m sec.

C) Seek exerciser:

Address #1 off

Address #2 switches - 64, 32, 8, 1 - on

Restore - on

Start - on

Step mode - step position

Do a seek to track 105

D) Adjust heads for equal valleys. Single head clamp screws 26 in/ozs. torque. Dual head clamp screw 50 in/ozs. torque.

TRANSDUCER ADJUSTMENT

USING SEEK EXERCISER

- A) Load C. E. pack and bring up to ready state.
- B) Scope connections:

Ch. #1 - J9 board TP6

Volts .2v

Trigger with Ch. #1 Neg. Edge

Ch. #2 - J10 board TP2

Volts .2v

Time: 5 µ sec.

C) Seek exerciser:

Address #1 off

Address #2 switches 64, 32, 4 - on

Restore - on

Start - on

Step mode - step position

Do a seek to track #100

D) Adjust for index pulse to be 30 m sec.
Then change head socket for other head difference between both heads can't be more than
10 m sec.

DIABLO DISC DRIVE DATA GATE RETROFIT OF J-10 BOARD

- A) Need 2 200K pots on clip leads.
- B) O'scope

Ch. #1 - TP3 on J10 .2v

Signal ground on Ch. #1

Ch. #2 - TP5 on J10 .2v

Sync on Ch. #1 neg. edge

Time: .05 µ sec.

- C) Factory sets gate at 450-470 n sec.
 - D) Test floor modifies to 440-460 n sec.
 - E) Cut resistors F28, H53 out of circuit.
 - F) Put 200K pots in parallel with H56 and H28.
 - G) Adjust the pot in parallel with H56 for 440 n sec.
 - H) Adjust the pot in parallel with H28 for 460 n sec.
 - I) Measure the pots and find resistors that match the resistance of the pots. Then place the fixed resistors in place of F28 & H53. Recheck pulse for approx. time.

MODEL 31 DISC DRIVE SPECIAL TOOLS

Xerox P/N	Description	Diablo P/N
600T1372	Alignment Cartridge	70268
600T1373	Seek Restore Exercisor	11142
600T1374	Extender Board	11040-01
600T1375	Bar Tool	15172
600T1376	Cone Tool	15171
600T1377	Seratch Pack	3H Corp. #3M-902-24
600T1378*	Torque Wrench Handle	70342
600T1378* 600T1379*	Torque Wrench Handle Extender Xcelite Mod.	70342 70345-01
	•	
600Т1379*	Extender Xcelite Mod.	70345-01

*Note: These tools are part of Torque Wrench Set 600T1396.

DISC DRIVE DAISY CHAINING UNITS

NORI	MAL VIEW (BAC	K) 	
· · · · · · · · · · · · · · · · · · ·	ONE DRIVE P/N 11174-03	FEMALE TERM.	MALE CABLE FROM CONTROLLER
ORIGINAL DRIVE	TWO DRIVES P/N 11105-84	FEMALE CABLE TO NEW DRIVE	MALE CABLE FROM CONTROLLER
NEW DRIVE NEEL OR	D: 11175-03 '	FEMALE CABLE FROM ORIG. DRIVE TERM. & 11105-84 Ca	MALE TERM. P/N 11175-03
ORIGINAL DRIVE	TWO DRIVES	FEMALE CABLE TO NEW DRIVE	MALE CABLE FROM CONTROLLER
NEW DRIVE	P/N 11174-03	FEMALE TERM.	MALE CABLE FROM ORIG. DRIVE P/N 11245-60
NEEL	D: 11245-60 (Cable	

 $\underline{\text{NOTE}}$: XX on cable P/N's is length in inches.

TERMINATOR SERIES 30

P/N 11175-03

FOR ALTO II DISC DRIVE (DIABLO)

FEMALE CONNECTOR B1 В2 В4 D10 D11 A11 A13 $_{\mathsf{O}}\mathsf{A}$ A14 o D J17 oВ J19 D20 E20 J20 D21 J22 A23 H24 E24 A25 E26 H26 J26 A26 B27 D27 E27 H27 0 E29 H29 B30. D30

Leave the following resistors on the terminator, clip off the rest:

E30

H30

E1	D23	J11
E2		J13
E7		J14
E8		J16
E23		

Leave on the following caps, clip off the rest:

A22 H1

This leaves the following pins terminated:

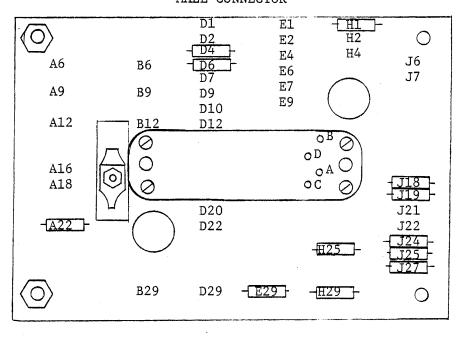
A,C,H,S,V

TERMINATOR SERIES 30

P/N 11174-03

FOR ALTO II DISC DRIVE (DIABLO)

MALE CONNECTOR



Leave the following resistors on the terminator, clip off the rest:

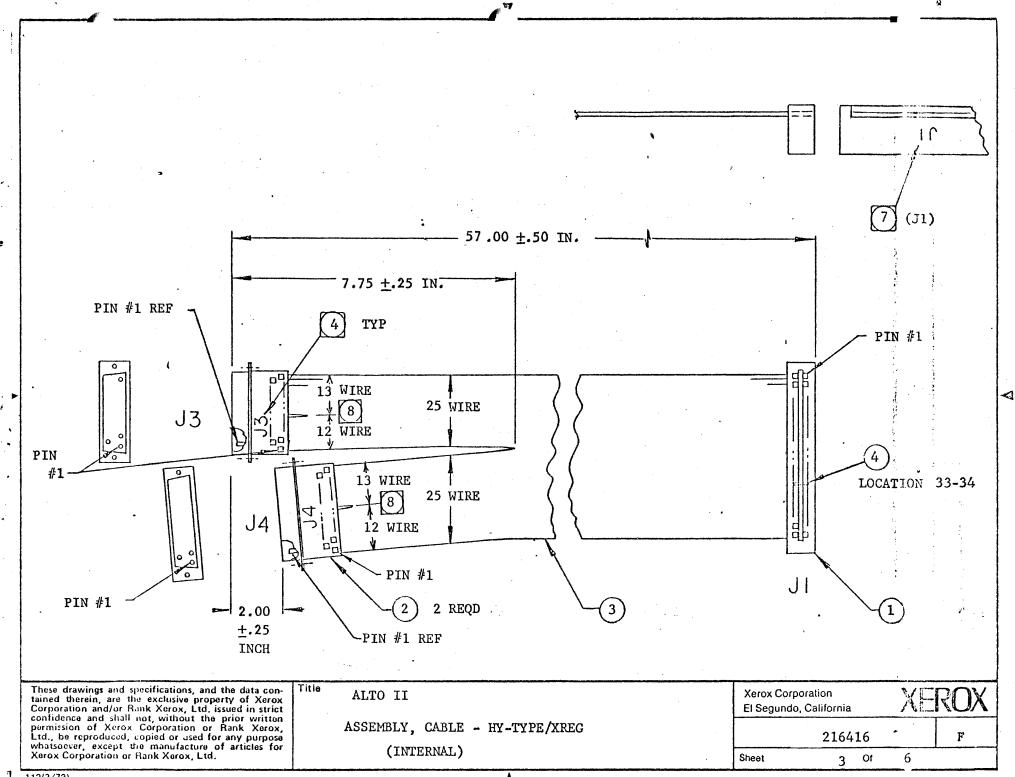
D4	E29	J18	H25
D6		J19	H29
		J24	
		J25	
		J27	

Leave on the following caps, clip off the rest:

A22 H1

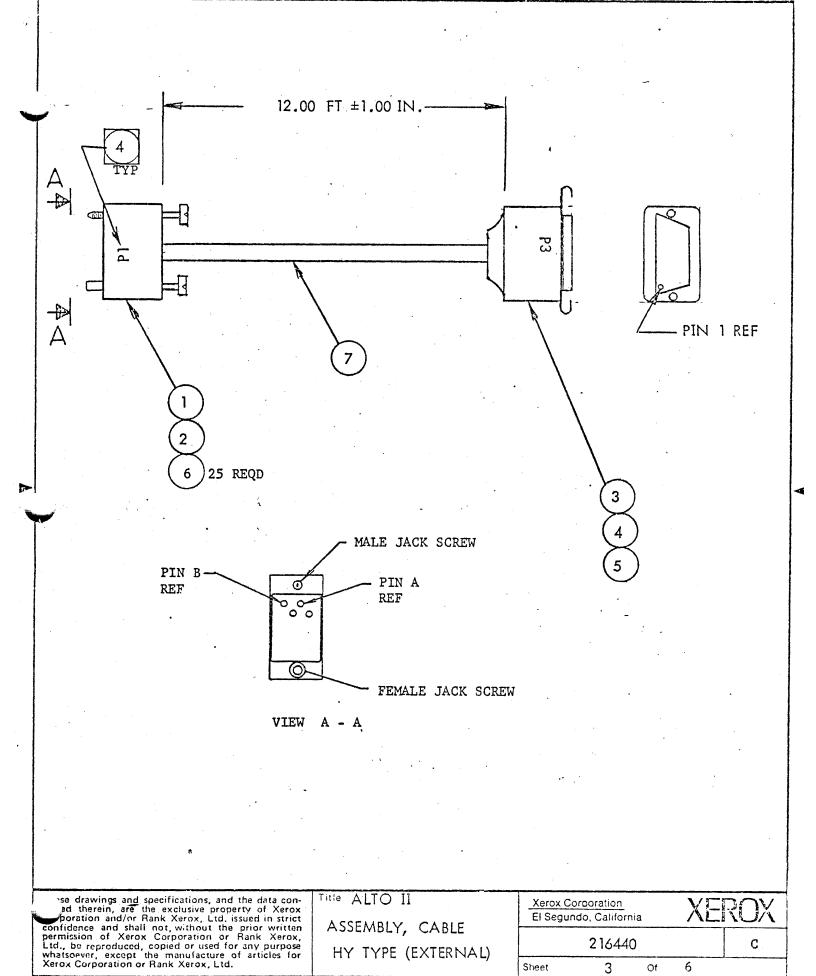
This leaves the following pins terminated:

A,C,H,S,V



• Wire No.	Term	From	То	Term	Wit- rype	Notes	Signal	Chg. Let.
1		J3 - 13	J1 - 1		50		PPFSTR	
2		25	2				PREST	
3		12	3				PRIB	
4		24	. 4			•	PCHSTR	
5		11	5				PCARSTR	
6		23	6				PDATA1024	
7		10	7				PDATA512	
8		22	8				PDATA256	
9		9	9			,	PDATA128	
10		21	10				PDATA64	
11		8	11				PDATA32	
12		20	12				PDATA16	
13		7	13				PDATA8	
14		19	14		·		PDATA4	
15		6	15			·	PDATA2	
16		18	16				PDATA1	
17		5	1.7				PPFRDY	
18		17	18			·	PCHK	
19		4	19				PPO	
20	•	16	20				PCHRDY	
21		3	21				PRDY	
22		15	22				PCARRDY	
23		2	23				GROUND	
24		14	24				GROUND	
25		J3 - 1	J1 - 25				GROUND	
26		J4 - 13	J1 - 26		50		GROUND	
tained therein, ar Corporation and/o	e the exclusor Rank Xero	ons, and the data con- ive property of Xerox ox, Ltd. issued in strict hout the prior written	1. Ref Item No's in Appl Material List. 2. Ref Designations Are		Title	ALTO II	Xerox Corporation El Segundo, California	EROX
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Torm	From	То	Term	Тура	Notes	Signal	Chg. Le
	J4 - 25	J1 - 27		50		X(0)	
	12	28			•	X(8)	
	24	29				X(1)	4
	11	30	·		¥.,		
3	23	31				X(2)	
	10	32				X(10)	
	22	33				X(3)	
	9	34				X(11)	
	21	35				X(4)	
·	8	36				X(12)	
	20	37				X(5)	
	7	38				X(13)	
	19	39				X(6)	
	6	40				X(14)	
	18	41				X(7)	
	5	42				X(15)	
	17	43				KEYWAY	
	4	44				KEYWAY	4
	16	45	·			GROUND	
	3	46				GROUND	
	15	47				· XREG	
	2	48				SEL1	
	14	49				X(6)	
	J4 - 1	J1 - 50		50		SELO	·
					•		
		_					
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	nd specification of the exclusive of the exclusive rail not wither the corporate of corporate of corporate rail not with the corporate rail no	J4 - 25 12 24 11 11 23 23 10 22 2 2 2 2 2 2 2 2	J4 - 25	J4 - 25	J4 - 25	J4 - 25	J4 - 25



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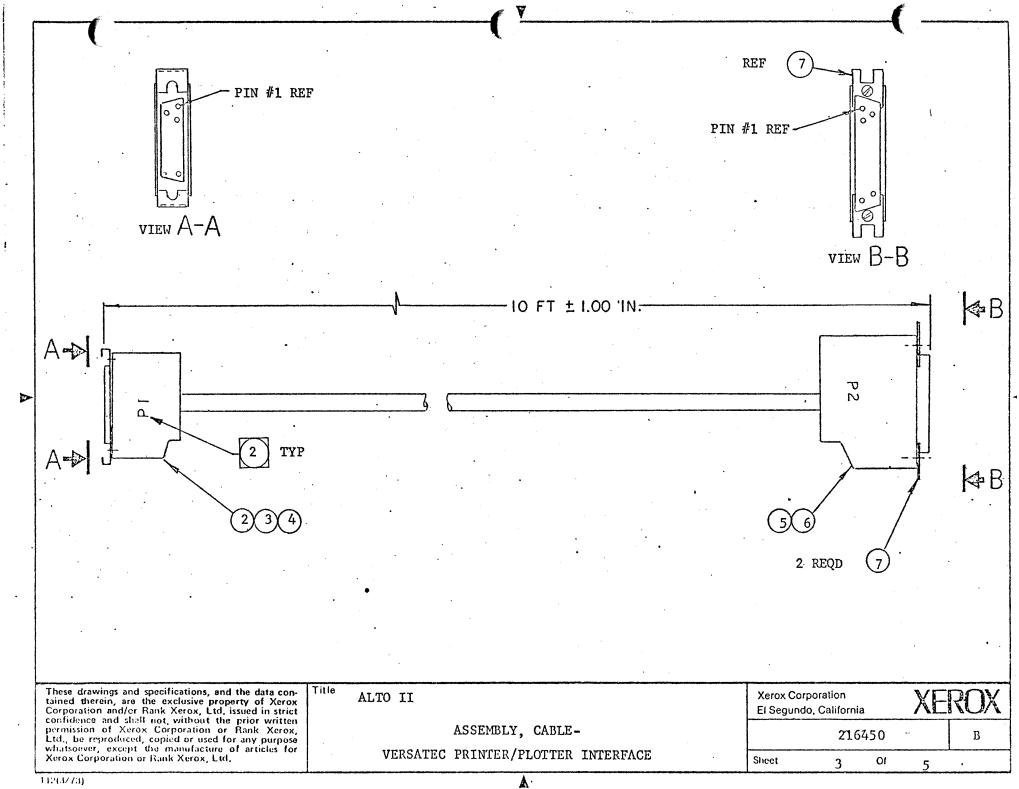
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	f	·			¥			
Wire N	Term	From	То	Term	Wire Type	Notes	Signal	Chg. Let.
1		P1- h	P3- 18		7	BLACK	PDATAT	
2		j	6			WHITE	PDATA2	
3		m	19			RED	PDATA4	
4		f	7			GREEN	PDATA8	
5		k	20			ORANGE	PDATA 16	
6		i	8			BLUE	PDATA32	
7		g	21			WHT/BLK	PDATA64	
8		d	9			RED/BLK	PDATA 128	
9		b	22			GRN/BLK	PDATA256	
10		V	10			ORN/BLK	PDATA512	
11		F	23			BLU/BLK	PDATA 1024	
12		E	25			BLK/WHT	PREST	
13		Р	24			RED/WHT	PCHSTR	
14			11			GRN/WHT	PCARSTR	
15		С	13	,		BLU/WHT	PPFSTR	
16		M	12			BLK/RED	PRIB	
17		а	3			WHT/RED	PRDY	,
18		В	17			ORN/RED	PCHK	
19		R	4			BLU/RED	PPO	
20		·Y	16			RED/GRN	PCHRDY	
21		W	15			ORN/GRN	PCARRDY	
22		С	5			BLK/WHT/GRN	PPFRDY	-
23		A	2			WHT/BLK/RED	GROUND	
24		D	14			RED/BLK/WHT	GROUND	
25		P1- J	P3- 1		7	ORN/BLK/WHT	GROUND	
26		P1- S	P1- T	·	8		SELECT PRINTER	
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Wire	Term		From		То	Term	COLOR	DIABLO MNEMONIC	VERSATEC MNEMONIC	Chg. Let
1		P	21 - 1	I	P2 - 25		WHT	GND	GND	
2			2		29	#18	BLK	- GND	GND	
3			11		12		BLK	PCARSTR 1	PRINT	
4			4		19		BRN	PPO'	NOPAP	
5			21		7		RED	PDATA64'	INO7	
6			20		5		ORN	PDATA16'	INO5	
7			19		3		YEL	PDATA4'	INO3	
8			18.		1		PUR	PDATA1'	INO1	
9			10		15		GRY	PDATA512'	RESET'	
10			12		18		BLU	PRIB'	RLTER '	
11			13		16		GRN	PPFSTR'	RFFED'	
12			14		20		WHT	GND :	GND	
13 .			16		11		BLU/BLK	FRDY '	READY '	
14			17		32		BLU/BRN	PCHK-	ONLIN'	
15			9 .		8		ORN/RED	PDATA128'	INO8	
16			8		6		ORN/BLU	PDATA32'	INO6	
17			7		4		ORN/BLK	PDATA8'	INO4	
18			6		2		ORN/GRN	PDATA2'	INO2	74
19			22	<u> </u>	17.		ORN/BRN	PDATA256	REOTR'	
20			23		14		YEL/RED	PDATA1024	SPP'	
21			24		10		YEL/BLU	PCHSTR'	PICLK'	
22		P	1 - 25	·	2 2 - 9		YEL/BLK	PREST '	CLEAR '	
			· · · · · · · · · · · · · · · · · · ·							
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ENGREDING

3-5-76 Dist. Code 117

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NOTES: UNLESS OTHERWISE SPECIFIED

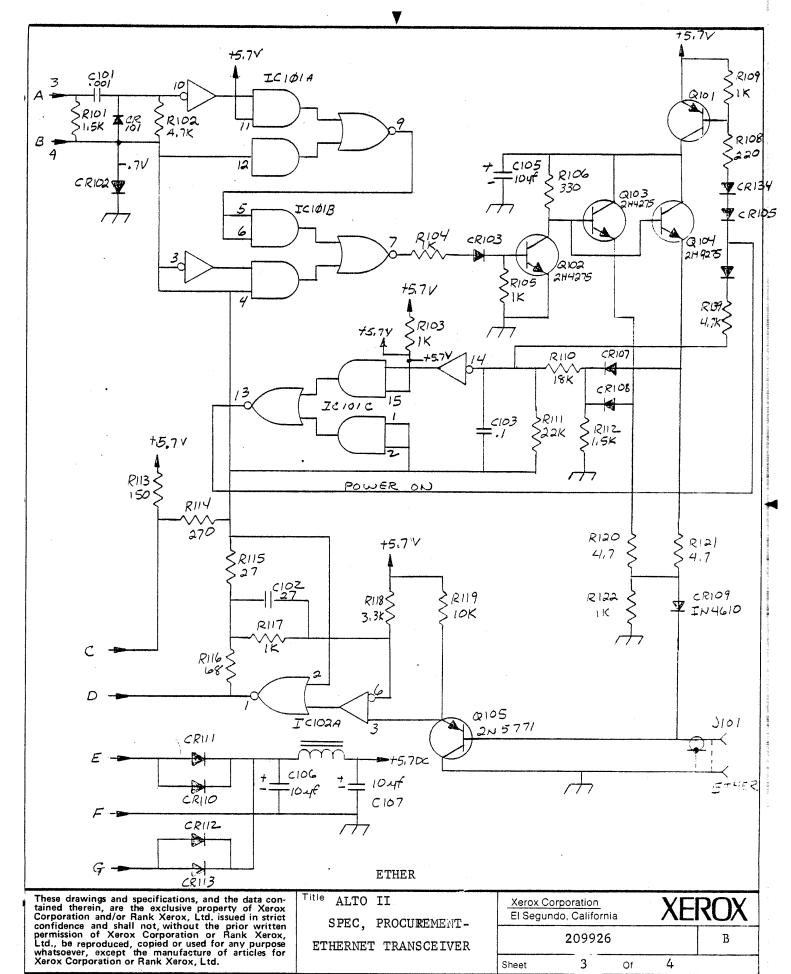
1. ETHERNET HEAD ASSY MAY BE PURCHASED FROM TAT LAM, 2717 PEACHWOOD COURT, SAN JOSE, CALIFORNIA 95132, VENDOR MODEL NO. 2000 ETHERNET TRANSCIEVER.

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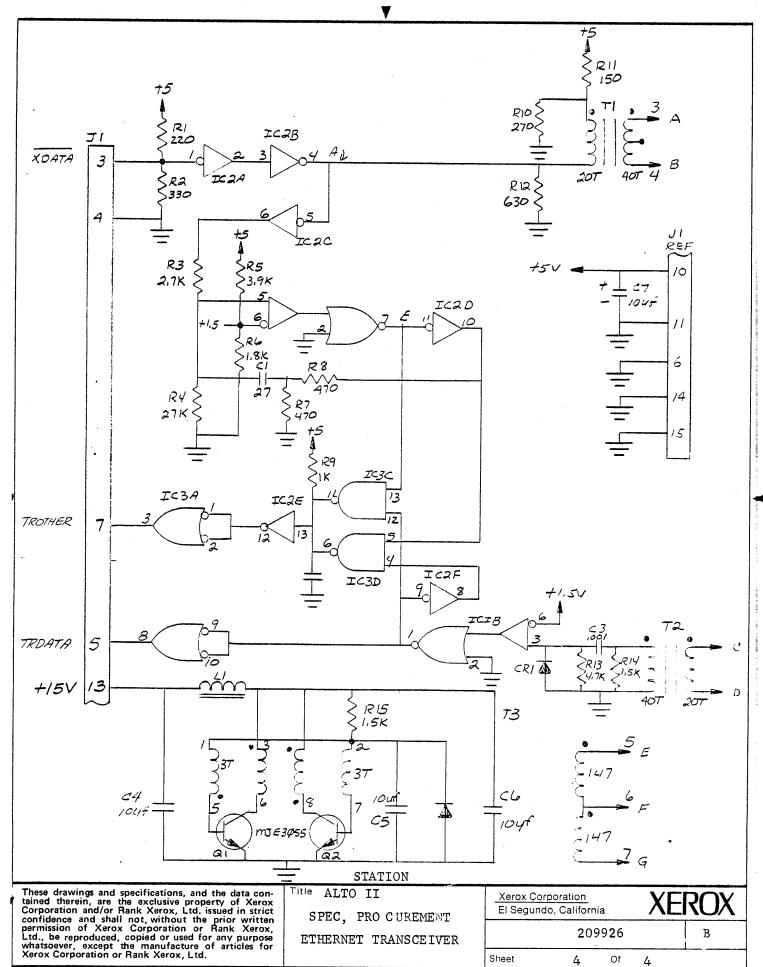
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		Revisions		216	L	С
LAL	Rev.	Description	Chk.	Date	Appr	oved
	A	ENGRG RELEASE	S.w.	3/5/5	8/11	/
	В	M/L ITEM 6 WAS QTY 2, M/L ITEM 7 WAS QTY 2, ADDED ITEM 9 & 10.		4-19/	20	
	С	DELETED ITEMS 3,5,&8 FROM M/L & F/D	BU	4-13	Bal	

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NOTES: UNLESS OTHERWISE SPECIFIED

1. FABRICATE PER XEROX SPEC 209154.

MARK CHARACTERS .12 HIGH "NEWS GOTHIC".

MAY BE PURCHASED FROM BELDEN CORP, RICHMOND, INDIANA, VENDOR PART NUMBER.

MAY BE PURCHASED FROM CANNON ELECTRIC, SANTA ANA, CALIF, VENDOR PART NUMBER.

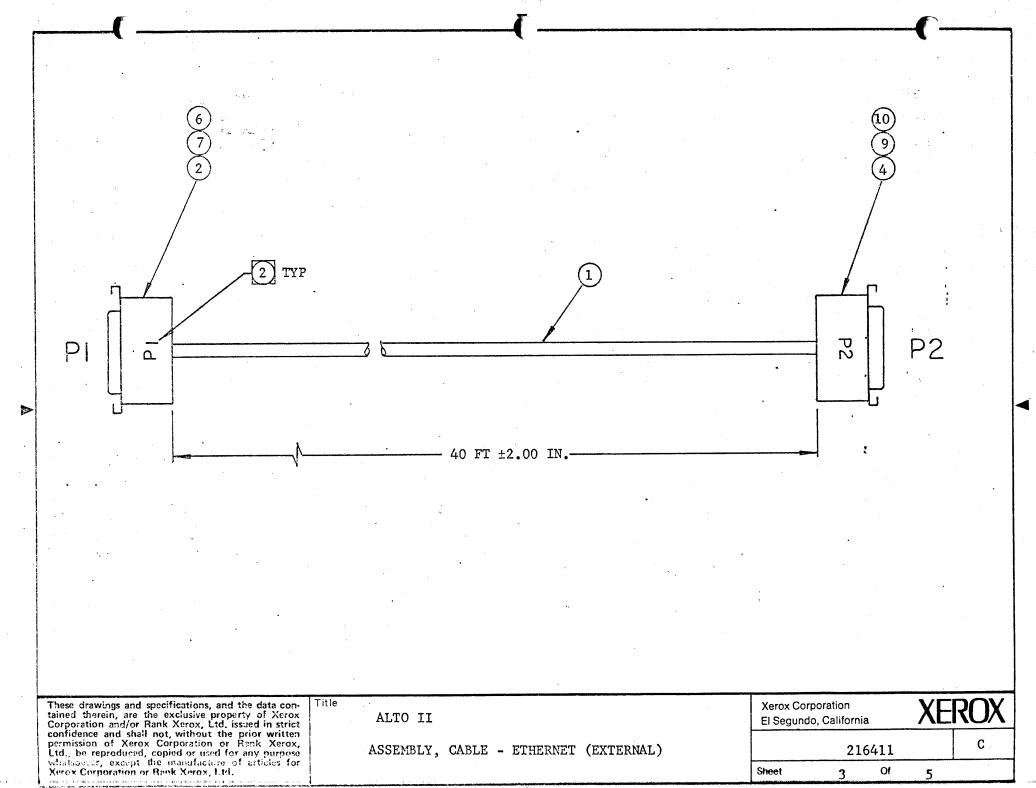
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ASSEMBLY, CABLE - ETHERNET (EXTERNAL)

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Xerox Corporation 701 South Aviation Boulevard El Segundo, California 90245 213 679-4511



Mate	erial L	ist	▼		P	ML	Drawing No. 21641	.1	Rev. C
216411 of	Drawing ⁻	ASSEMBLY, CABLE - ETHERNET (EXTERNAL)	in, are Xerox, prior w be repr the ma	the exclusive pro Ltd. issued in stri ritten permission of oduced, copied or nufacture of article	perty of Det confident of Xerox Coused for a subsection of Xerox	As, and the data contained theref Xerox Corporation and/or Rank dence and shall not, without the Corporation or Rank Xerox, Ltd., any purpose whatsoever, except tox Corporation or Rank Xerox, Ltd. 12/76 Sheet 4 Of 5			
No.	ttom No.	Drawing Title		Drawing No.	No. Req.	T .	Remark		J1
Drawing No. 216	Item No.			Drawing No.	A/R		8747		(3
Uray	1	Cable, 6 Pr, 22 AWG				(D1) DBC-25E		4
	2	Shell, Connector			1	(1)			
Σ	3	Contact, Pin			=12=		030 -1 95		ıtı.
	4	Shell, Connector			1	(P2	2) DAC-158		
	5	-Contact, Pin-			12		030-195	3-00	(C=
	6	Retainer, Sliding Lock			1		DB51221	-1	
	7	Shell, Junction (Hood)			1		DB51212	2-1	(4
	8	Strap, Cable Identification		162244	-2-		•		
	9	Shell, Junction (Hood)	•	·	1		DA51210)-1	4
	10	Retainer, Sliding Lock			1.		DA51220)-1	(4
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Wire No.	Term	From	. То	Term \	Wire Type	Notes	Signal	Chg. Let
1		P1 - 12	P2 - 9	,	1	BRN TWISTED	SPARE	
2		24	1			BLK PAIR	SPARE GND	
3		13	3			GRN)	XDATA	
4		25	4			BLK S	GND	
5		16	5			WHT	TRDATA	
6		3	6			BLK S	TRDATA GND	
7		8	7			BLU	TROTHER	
8		20	15			BLK S	TROTHER GND	
9		11	10			RED	+5V	
10		23	11			BLK S	+5V GND	
11		19	13			YEL TWISTED	+15V	
12		P1 - 6	P2 - 14		1	BLK J	+15V GND	
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These drawings tained therein, Corporation and	and specifications the exclusion of the	tions, and the data con- sive property of Xerox ox, Ltd. issued in strict	Ref Item No's in Applicable Material List. Ref Designations Are Abbreviated. Prefix Each Designation With:		ALTO II ASSEMBLY, CABLE -ETHERNET		Xerox Corporation El Segundo, California	XERO
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Ltd., be repro- whatsoever, ex-	duced, copied cept the man	or used for any purpose utasture of articlus for				(EXTERNAL)	Sheet 5 Of	5

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